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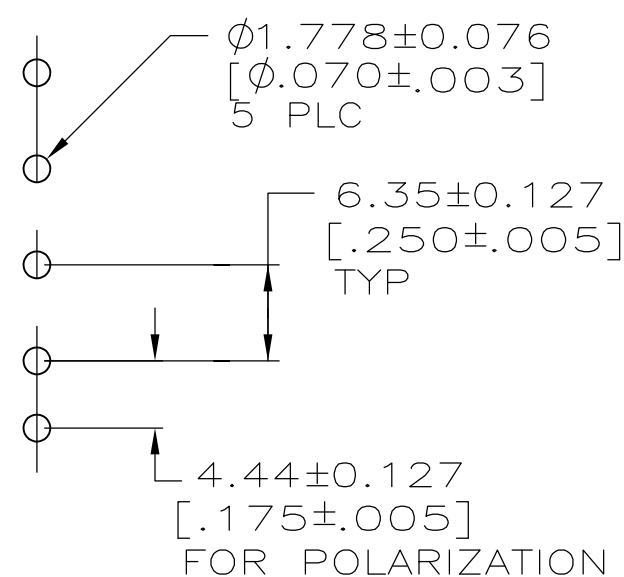
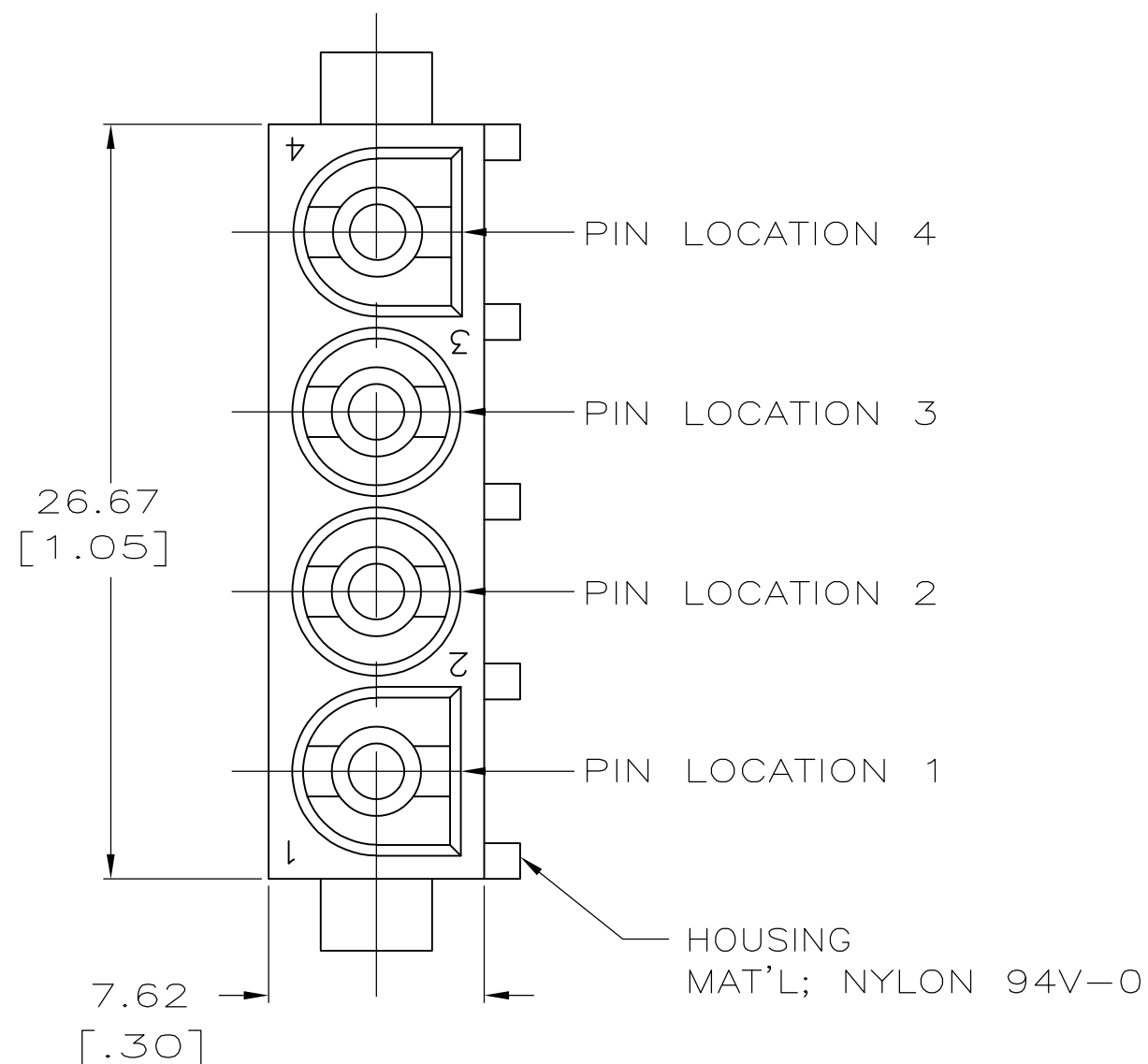
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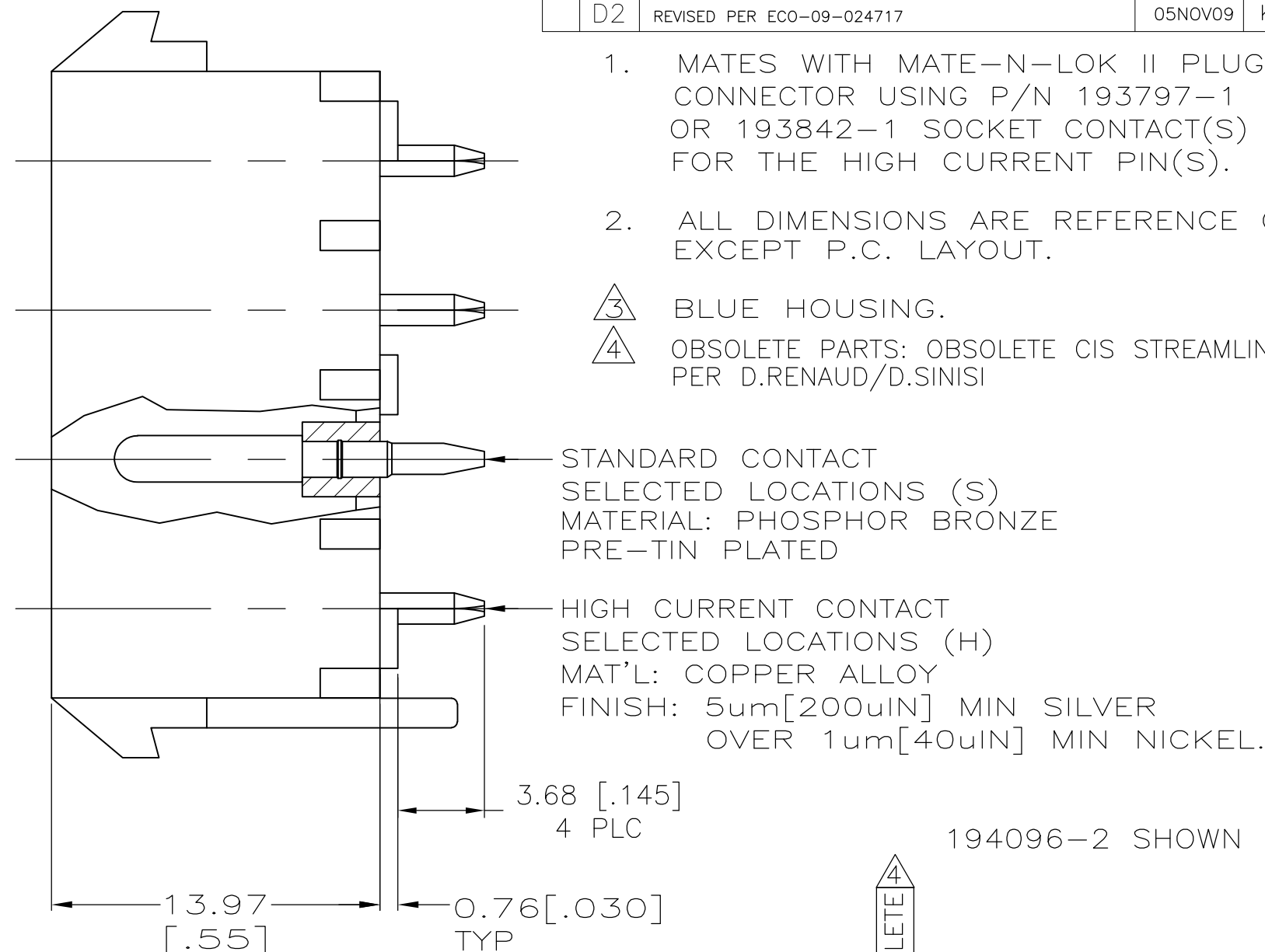
LOC
FTDIST
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REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	D1	REVISED PER ECO-06-21510	9/11/06	SS	GS
	D2	REVISED PER ECO-09-024717	05NOV09	KK	AEG



RECOMMENDED LAYOUT FOR
1.57 [.062] THK P.C. BOARD



- MATES WITH MATE-N-LOK II PLUG CONNECTOR USING P/N 193797-1 OR 193842-1 SOCKET CONTACT(S) FOR THE HIGH CURRENT PIN(S).
- ALL DIMENSIONS ARE REFERENCE ONLY EXCEPT P.C. LAYOUT.

- 3 BLUE HOUSING.
- 4 OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI

194096-2 SHOWN

4 OBSOLETE	3	S	S	S	PIN LOCATION 4
		S	S	S	PIN LOCATION 3
		H	H	H	PIN LOCATION 2
		H	S	S	PIN LOCATION 1
		-5	-3	-2	

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN R N POLK	29/JUN/95	Tyco Electronics Corporation Harrisburg, PA 17105-3608	
DIMENSIONS: mm [INCHES]		CHK -	-		
		APVD -	-	NAME PARTIAL HIGH CURRENT VERT. PIN HEADER ASSY SELECTIVE LOAD, MATE-N-LOK II, 4 CIRCUIT	
		PRODUCT SPEC -	-	-	
MATERIAL -		APPLICATION SPEC -	-	SIZE A3	RESTRICTED TO -
		WEIGHT -	-	CAGE CODE 00779	
FINISH -		CUSTOMER DRAWING		DRAWING NO C-194096	
		SCALE NONE		SHEET 1 OF 1	REV D2

Mouser Electronics

Authorized Distributor

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[TE Connectivity:](#)

[194096-3](#)