

PI3C3245

2.5V/3.3V, High Bandwidth, Hot Insertion 8-Bit, 2-Port, Bus Switch

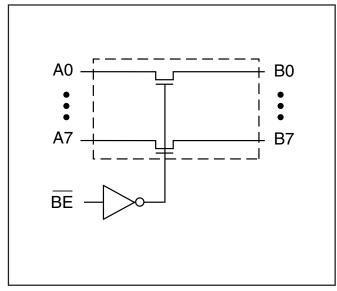
Features

- · Near-Zero propagation delay
- 5-ohm switches connect inputs to outputs
- High Bandwidth Operation (>400 MHz)
- · Permits Hot Insertion
- 5V I/O Tolerant
- Rail-to-Rail 3.3 V or 2.5 V Switching
- 2.5V Supply Voltage Operation
- Packaging (Pb-free & Green available):
 - -20-pin 150 mil wide plastic QSOP (Q)
 - -20-pin 173 mil wide plastic TSSOP(L)

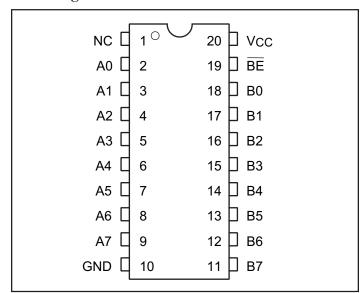
Description

Pericom Semiconductor's PI3C3245 is a 2.5 volt or 3.3 volt 8-bit, 2-port bus switch designed with a low On-Resistance (5-ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable $(\overline{\rm BE})$ input signal. It is very useful in switching signals that have high bandwidth (>400 MHz).

Block Diagram



Pin Configuration



Truth Table(1)

Function	BE	A0-7
Disconnect	Н	Hi-Z
Connect	L	В0–7

Note:

1. H = High Voltage Level
L = Low Voltage Level
Hi-Z = High Impedance

Pin Description

Pin Name	Description	
BE	Bus Enable Input (Active LOW)	
A0-7	Bus A	
B0-7	Bus B	
GND	Ground	
Vcc	Power	



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied—40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)0.5Vto+4.6V
Supply Voltage to Ground Potential (Outputs & D/O Only)0.5V to +4.6V
DC Input Voltage0.5V to+5.5V
DC Output Current
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 3.3$ V ±10%)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I _{IH}	Input HIGH Current	V _{CC} =Max., V _{IN} =V _{CC}			±1	
I _{IL}	Input LOW Current	V _{CC} =Max., V _{IN} =GND			±1	μΑ
I _{OZH} ⁽³⁾	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	Ī
V _{IK}	Clamp Diode Voltage	V _{CC} =Min., I _{IN} =-18mA	-0.73	-1.2		V
R _{ON}	Switch On Resistance ⁽⁴⁾	V _{CC} =Min., V _{IN} =0.0V, I _{ON} =48mA V _{CC} =Min., V _{IN} =2.4V, I _{ON} =15mA		5 8	7 15	Ω

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC}=3.3V$, $T_A=25^{\circ}C$ ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance $(T_A = 25^{\circ}C, f = 1 \text{ MHz})$

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Units
C _{IN}	Input Capacitance	$V_{IN} = 0V$	3.5	
C _{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$	5.0	pF
Con	A/B Capacitance, Switch On	$V_{IN} = 0V$	10.0	

Notes:

1. This parameter is determined by device characterization but is not production tested.

06-0244 2 PS8162D 05/09/06



Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. (2)	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} =Max.	V _{IN} =GND or V _{CC}		260	500	μA
ΔI _{CC}	Supply Current per Input HIGH	V _{CC} = Max.	$V_{IN} = 3.0V^{(3)}$			750	,

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient.
- 3. Per driven input (control input only); A and B pins do not contribute to ΔI_{CC} .

Switching Characteristics over 3.3V Operating Range

			PI3C	3245	
			Co	m.	
Parameters	Description	Conditions	Min.	Max.	Units
$t_{\rm PLH}$	Propagation Delay ^(1,2)	$C_L = 50 pF$		0.25	
$t_{ m PHL}$	Ax to Bx	$R_L = 500$ -ohm			
t_{PZH}	Bus Enable Time		1.5	6.5	ns
t_{PZL}	BE to Ax or Bx	$C_L = 50 pF$			
t_{PHZ}	Bus Disable Time	R_L =500-ohm	1.5	5.5	
t_{PLZ}	BE to Ax or Bx	R = 500-ohm			

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Switching Characteristics over 2.5V Operating Range

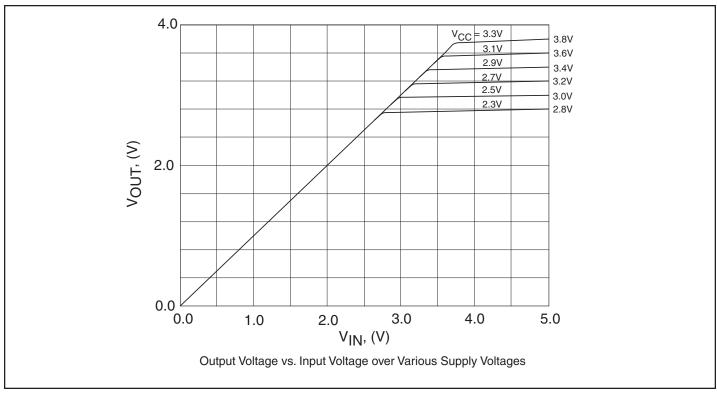
			PI3C3245		
			Com.		
Parameters	Description	Conditions	Min.	Max.	Units
tPLH	Propagation Delay ^(1,2)	$C_L = 50 pF$		0.25	
tPHL	Ax to Bx	R_L =500-ohm			
tpzH	Bus Enable Time		1.5	9.8	ns
t_{PZL}	\overline{BE} to Ax or Bx	$C_L = 50 pF$			
tPHZ	Bus Disable Time	R_L =500-ohm	1.5	8.3	
$t_{\rm PLZ}$	\overline{BE} to Ax or Bx	R=500-ohm			

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

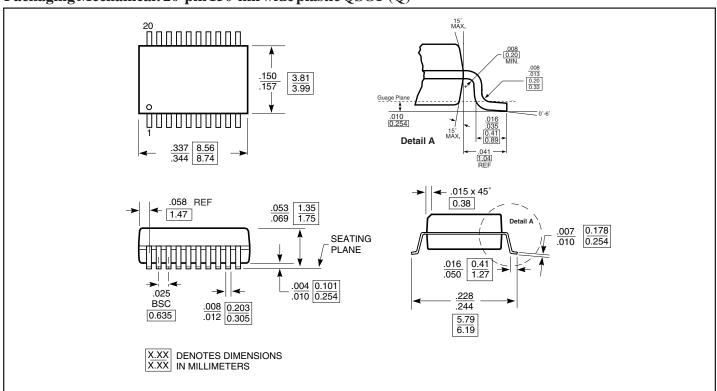
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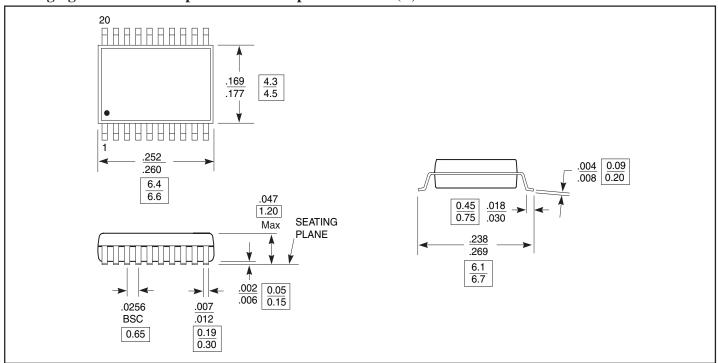
Output Voltage vs. Input Voltage over Various Supply Voltages

Packaging Mechanical: 20-pin 150-mil wide plastic QSOP(Q)





Packaging Mechanical: 20-pin 173-mil wide plastic TSSOP (L)



Ordering Information

Ordering Code	Package Code	Package Type
PI3C3245Q	Q	20-pin QSOP
PI3C3245QE	Q	Pb-free & Green, 20-pin QSOP
PI3C3245L	L	20-pin TSSOP
PI3C3245LE	L	Pb-free & Green, 20-pin TSSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel

06-0244 5 PS8162D 05/09/06