

Product Summary

V _{RRM} (V)	I _o (A)	V _{F(MAX)} (V) @ +25°C	I _{R(MAX)} (mA) @ +25°C
50	25	0.52	0.5

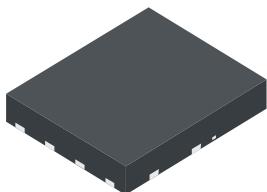
Description and Applications

Packaged in the compact thermally efficient POWERDI5060-8 package, the SBRT25U50SLP provides very low V_F and excellent reverse leakage stability at high temperatures. It is ideal for use as a rectifier, freewheel diode or blocking diode in:

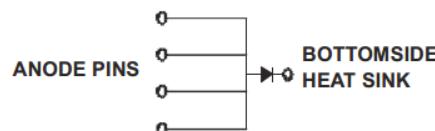
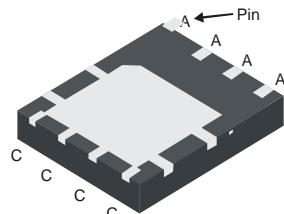
- DC-DC Converters
- AC-DC Adaptors

POWERDI5060-8

Top View



Bottom View



Note: All four anode pins must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

Part Number	Case	Packaging
SBRT25U50SLP-13	POWERDI5060-8	2,500/Tape & Reel

Notes:

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

POWERDI5060-8



SBRT25U50 = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 15 = 2015)
WW = Week (01-53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}		
Working Peak Reverse Voltage	V_{RWM}	50	V
DC Blocking Voltage	V_{RM}		
Average Rectified Output Current	I_O	25	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	200	A

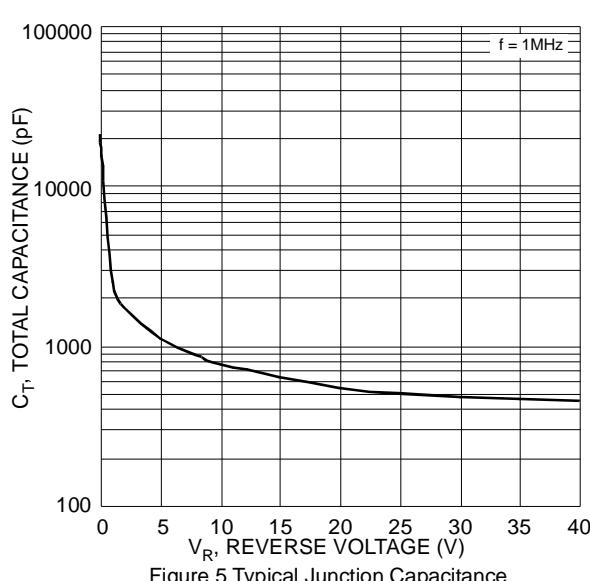
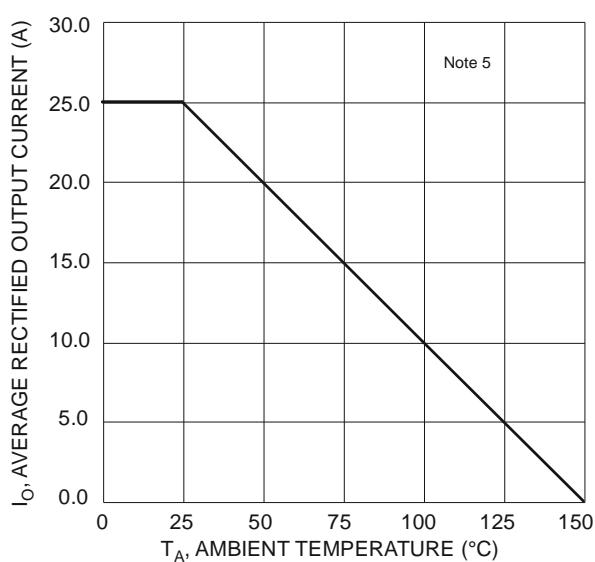
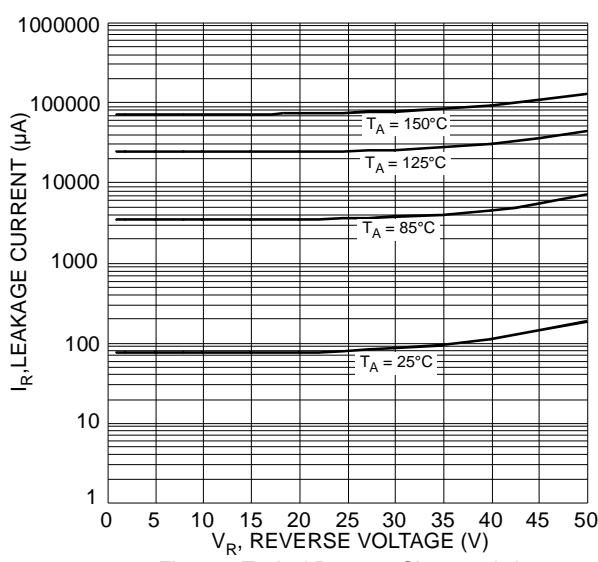
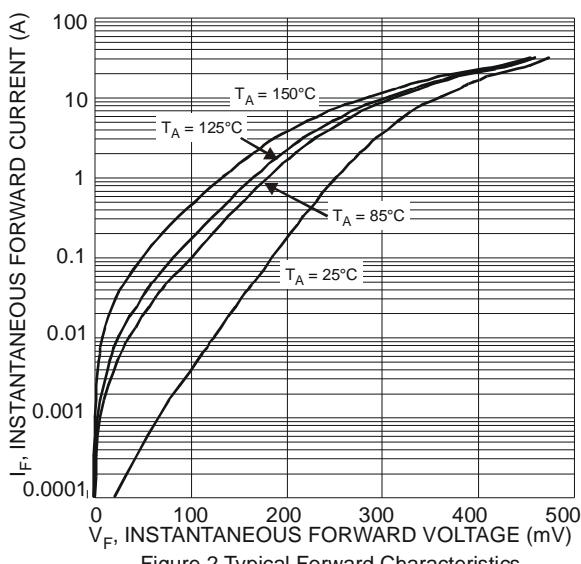
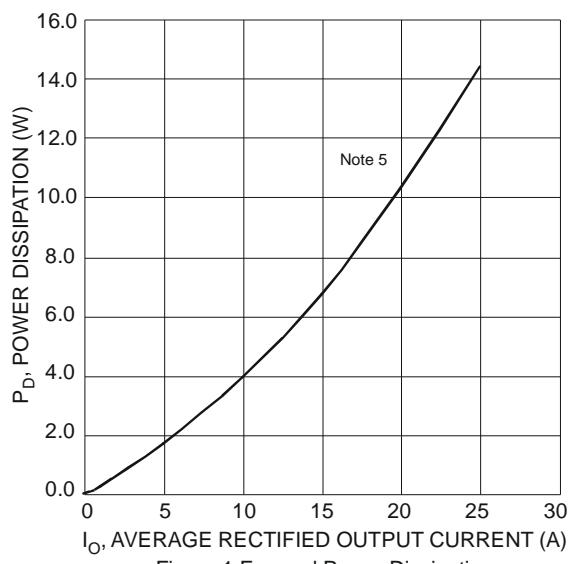
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	10	$^\circ\text{C}/\text{W}$
Typical Thermal Resistance Junction to Case (Note 5)	$R_{\theta JC}$	1	$^\circ\text{C}/\text{W}$
Operating Temperature Range	T_J	-55 to +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 to +175	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

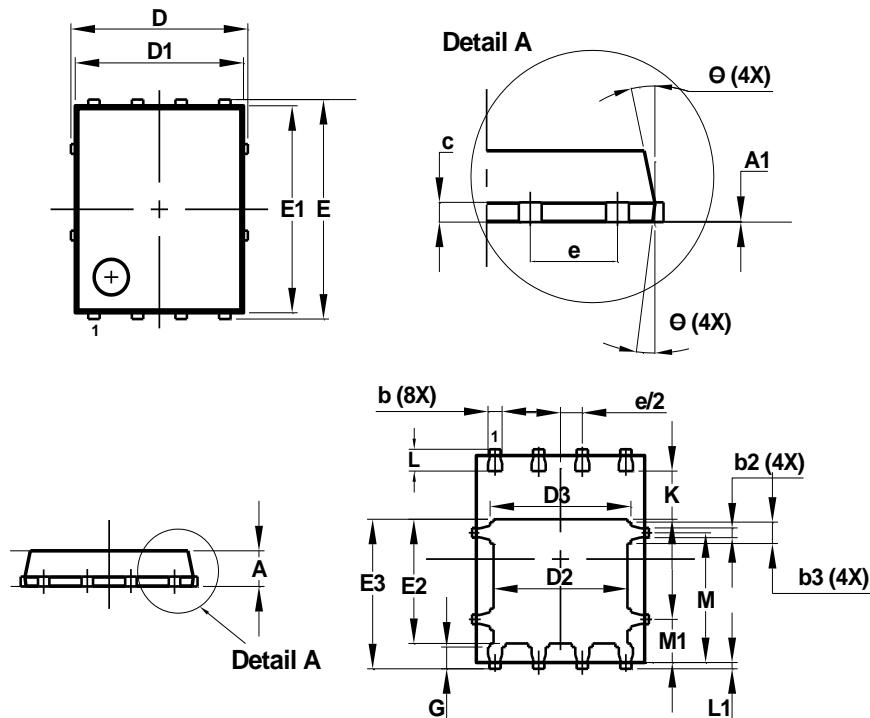
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop (Note 6)	V_F	— — —	0.380 0.455 0.430	— 0.52 —	V	$I_F = 12.5\text{A}, T_J = +25^\circ\text{C}$ $I_F = 25\text{A}, T_J = +25^\circ\text{C}$ $I_F = 25\text{A}, T_J = +125^\circ\text{C}$
Leakage Current (Note 6)	I_R	— —	0.18 —	0.50 100	mA	$V_R = 50\text{V}, T_J = +25^\circ\text{C}$ $V_R = 50\text{V}, T_J = +125^\circ\text{C}$

Notes: 5. Device mounted on Al substrate with 1-inch pad layout and additional HK (48mm x 35mm x80mm).
6. Short duration pulse test used to minimize self-heating effect.



Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

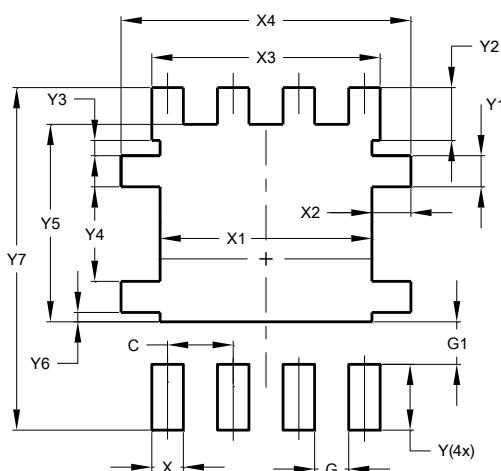


POWERDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	—
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	—	—
L	0.51	0.71	0.61
L1	0.100	0.20	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
Θ	10°	12°	11°
Θ1	6°	8°	7°

All Dimensions in mm

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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