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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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NC7SVL04 TinyLogic[®] Low-I_{CCT} Inverter

Features

- 0.9V to 3.6V V_{CC} Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at V_{CC} from 0.9V to 3.6V
- Power-Off High Impedance Inputs and Outputs
- Proprietary Quiet Series[™] Noise / EMI Reduction Circuitry
- Ultra-Small MicroPak[™] Package
- Ultra-Low Dynamic Power

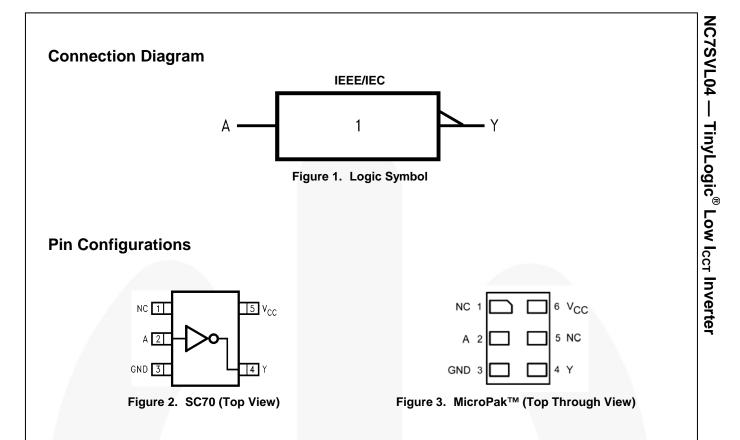
Description

The NC7SVL04 is a single inverter with a low-I_{CCT} input design from Fairchild's Ultra-Low Power (ULP-A) series of TinyLogic[®]. The NC7SVL04 features very low quiescent current, even when the input voltage is lower than the V_{CC} supply. This feature services mobile handset applications very well, allowing for direct interface with baseband processor general-purpose I/Os. Since mobile devices rely on a battery supply, the NC7SVL04 facilitates lower consumption in mixed-voltage rail environments.

This product is designed on an advanced CMOS technology for a wide low-voltage operating range (0.9V to $3.6V V_{CC}$), high drive needs (up to 24mA), and speed (maximum propagation delay of 3.5ns, V_{CC} =3.3V). It achieves this performance while maintaining low CMOS power dissipation.

Ordering Information

| Part Number | Top Mark | Package | Packing Method |
|-------------|---|---------------------------------------|------------------------------|
| NC7SVL04P5X | L04 | 5-Lead SC70, EIAJ SC-88a, 1.25mm Wide | 3000 Units on Tape & Reel |
| NC7SVL04L6X | CD | 6-Lead MicroPak™, 1.00mm Wide | 5000 Units on Tape & Reel |
| NC7SVL04FHX | C7SVL04FHX CD 6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch | | 5000 Units on Tape & Reel |



Pin Definitions

| Pin # SC70 | Pin # MicroPak™ | Name | Description |
|------------|-----------------|-----------------|----------------|
| 1 | 1 | NC | No Connect |
| 2 | 2 | A | Input |
| 3 | 3 | GND | Ground |
| 4 | 4 | Y | Output |
| 5 | 6 | V _{cc} | Supply Voltage |
| | 5 | NC | No Connect |

Function Table

Y=/A

| Inputs | Output |
|--------|--------|
| A | Y |
| L | Н |
| Н | L |

H = HIGH Logic Level

L = LOW Logic Level

NC7SVL04 — TinyLogic[®] Low I_{CCT} Inverter

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

| Symbol | Para | meter | Min. | Max. | Unit |
|-----------------------------------|---|----------------------------------|------|-----------------------|------|
| V _{cc} | Supply Voltage | | -0.5 | 4.6 | V |
| V _{IN} | DC Input Voltage | | -0.5 | 4.6 | V |
| N/ | | HIGH or LOW State ⁽¹⁾ | -0.5 | V _{CC} + 0.5 | M |
| Vout | DC Output Voltage | V _{CC} =0V | -0.5 | 4.6 | V |
| I _{IK} | DC Input Diode Current | $V_{IN} < 0V$ | | -50 | mA |
| - | DO Outeut Die de Ourrent | V _{OUT} < 0V | | -50 | |
| Ι _{ΟΚ} | DC Output Diode Current | $V_{OUT} > V_{CC}$ | | +50 | mA |
| I _{OH /} I _{OL} | DC Output Source/Sink Current | | ±50 | mA | |
| I_{CC} or I_{GND} | DC V _{CC} or Ground Current per Su | | ±50 | mA | |
| T _{STG} | Storage Temperature Range | | -65 | +150 | °C |
| TJ | Junction Temperature Under Bias | 3 | | +150 | °C |
| TL | Junction Lead Temperature, Sold | ering 10 Seconds | | +260 | °C |
| | | SC70-5 | | 150 | |
| PD | Power Dissipation at +85°C | MicroPak [™] -6 | | 130 | mW |
| | | MicroPak2 [™] -6 | | 120 | |
| | Human Body Model, JEDEC:JES | D22-A114 | | 4000 | N |
| ESD | Charge Device Model, JEDEC:JE | SD22-C101 | | 2000 | V |

Note:

1. I_O absolute maximum ratings must be observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

| Symbol | Parameter | Conditions | Min. | Max. | Unit | |
|----------------------------------|--|--|------|-----------------|------|--|
| V _{cc} | Supply Voltage | | 0.9 | 3.6 | V | |
| VIN | Input Voltage | | 0 | 3.6 | V | |
| V | | V _{CC} =0V | 0 | 3.6 | v | |
| Vout | Output Voltage | HIGH or LOW State | 0 | V _{CC} | v | |
| | | V _{CC} =3.0V to 3.6V | | ±24.0 | | |
| | Output Current in I _{OH} /I _{OL} | V _{CC} =2.3V to 2.7V | | ±18.0 | mA | |
| 1 /1 | | V _{CC} =1.65V to 1.95V | | ±6.0 | | |
| I _{OH} /I _{OL} | | V _{CC} =1.4V to 1.6V | | ±4.0 | | |
| | | V _{CC} =1.1V to 1.3V | | ±2.0 | | |
| | | V _{CC} =0.9V | | ±0.1 | | |
| TA | Operating Temperature, Free Air | | -40 | +85 | °C | |
| $\Delta t / \Delta V$ | Minimum Input Edge Rate | V _{IN} =0.8V to 2.0V, V _{CC} =3.0V | | 10 | ns/V | |
| | | SC70-5 | | 425 | | |
| θ_{JA} | Thermal Resistance | MicroPak [™] -6 | | 500 | °C/W | |
| | | MicroPak2 [™] -6 | | 560 | | |

Note:

2. Unused inputs must be held HIGH or LOW. They may not float.

| | | Parameter V _{cc} Condition | T _A =25°C T | T _A =25°C | | T _A =-40 1 | to +85°C | |
|-----------------|------------------------------|-------------------------------------|-------------------------|-----------------------|------------------------|------------------------|------------------------|-------|
| Symbol | Parameter | | Conditions | Min. | Max. | Min. | Max. | Units |
| | | 0.90 | | .65 x V _{CC} | | $.65 \times V_{CC}$ | | |
| | | $1.10 \leq V_{CC} \leq 1.30$ | | .65 x V _{CC} | | .65 x V _{CC} | | |
| M | HIGH Level Input | $1.40 \leq V_{CC} \leq 1.60$ | | .65 x V _{CC} | | $.65 \times V_{CC}$ | | |
| VIH | Voltage | $1.65 \leq V_{CC} \leq 1.95$ | | 0.90 | | .0.90 | | V |
| | | $2.30 \leq V_{CC} \leq 2.70$ | | 1.50 | | 1.50 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | 1.50 | | 1.50 | | |
| | | 0.90 | | | $.25 \text{ x V}_{CC}$ | | .25 x V_{CC} | |
| | | $1.10 \leq V_{CC} \leq 1.30$ | | | $.25 \text{ x V}_{CC}$ | | .25 x V_{CC} | |
| | LOW Level Input | $1.40 \leq V_{CC} \leq 1.60$ | | | .25 x V _{CC} | | $.25 \text{ x V}_{CC}$ | V |
| V _{IL} | Voltage | $1.65 \leq V_{CC} \leq 1.95$ | | | .25 x V _{CC} | | .25 x V_{CC} | v |
| | | $2.30 \leq V_{CC} \leq 2.70$ | | | 0.70 | | 0.70 | - |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | | 0.80 | | 0.80 | |
| | | 0.90 | - | V _{cc} -0.10 | | V _{CC} -0.10 | | |
| | | $1.10 \leq V_{CC} \leq 1.30$ | | V _{CC} -0.10 | | V _{CC} -0.10 | | |
| | | $1.40 \leq V_{CC} \leq 1.60$ | 1004 | V _{cc} -0.20 | | V _{CC} -0.20 | | |
| | | $1.65 \leq V_{CC} \leq 1.95$ | I _{ОН} =-100µА | V _{CC} -0.20 | | V _{CC} -0.20 | | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | | V _{CC} -0.20 | | V _{CC} -0.20 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | V _{cc} -0.20 | | V _{CC} -0.20 | | |
| | | $1.10 \leq V_{CC} \leq 1.30$ | I _{OH} =-2mA | .75 x V _{CC} | | $.75 \text{ x V}_{CC}$ | | |
| V _{OH} | HIGH Level Output Voltage | $1.40 \leq V_{CC} \leq 1.60$ | I _{OH} =-4mA | .75 x V _{CC} | | $.75 \times V_{CC}$ | | V |
| | Vollago | $1.65 \leq V_{CC} \leq 1.95$ | I _{OH} =-6mA | 1.25 | | 1.25 | | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | IOH=-OIIIA | 2.00 | | 2.00 | | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | 1 10m A | 1.80 | | 1.80 | | |
| | | $2.70{\leq}~V_{CC}{\leq}~3.60$ | I _{OH} =-12mA | 2.20 | | 2.20 | | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | 10m0 | 1.70 | | 1.70 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | I _{OH} =-18mA | 2.40 | | 2.40 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | I _{OH} =-24mA | 2.20 | 7 | 2.20 | | |

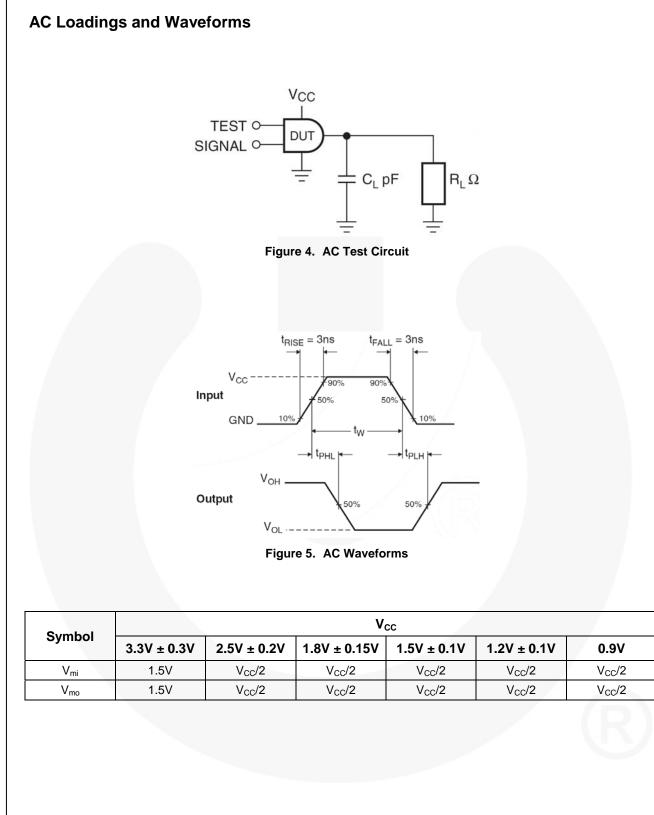
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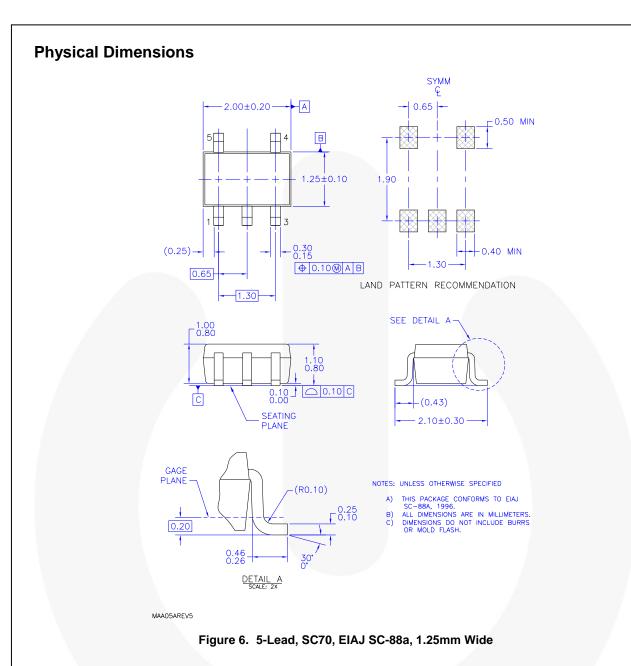
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| 0 | Demonstern | Ň | O an allitic ma | T _A =25°C | | T _A =-40 to 85°C | | Units |
|------------------|---|------------------------------|--|----------------------|------------------------|-----------------------------|------------------------|-------|
| Symbol | Parameter | V _{cc} | Conditions | Min. | Max. | Min. | Max. | Unite |
| | | 0.90 | | | 0.10 | | 0.10 | |
| | | $1.10 \leq V_{CC} \leq 1.30$ | | | 0.10 | | 0.10 | |
| | $1.40 \le V_{CC} \le 1.60$ | | | 0.20 | | 0.20 | | |
| | | $1.65 \leq V_{CC} \leq 1.95$ | I _{OL} =100μA | 2 | 0.20 | | 0.20 | |
| | $\begin{array}{c} \text{LOW Level} \\ \text{Output Voltage} \end{array} \begin{array}{ c c c c } \hline 2.30 \leq V_{CC} \leq 2.70 \\ \hline 2.70 \leq V_{CC} \leq 3.60 \\ \hline 1.10 \leq V_{CC} \leq 1.30 \\ \hline 1.40 \leq V_{CC} \leq 1.60 \\ \hline 1.40 \leq V_{CC} \leq 1.60 \\ \hline 1.65 \leq V_{CC} \leq 1.95 \\ \hline 1.20 \leq V_{CC} \leq 2.70 \\ \hline 2.70 \leq V_{CC} \leq 3.60 \\ \hline 1.20 \leq V_{CC} \leq 0.70 \\ \hline 1.20 \leq V_{CC} $ | | | 0.20 | | 0.20 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | | 0.20 | | 0.20 | |
| ., | | $1.10 \leq V_{CC} \leq 1.30$ | I _{OL} =2mA | | 0.25 x V _{CC} | | 0.25 x V _{CC} | |
| V _{OL} | | $1.40 \leq V_{CC} \leq 1.60$ | I _{OL} =4mA | | 0.25 x V _{CC} | | $0.25 \times V_{CC}$ | V |
| | | $1.65 \leq V_{CC} \leq 1.95$ | I _{OL} =6mA | | 0.30 | | 0.30 | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | 10-10-1 | | 0.40 | | 0.40 | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | I _{OL} =12mA | | 0.40 | | 0.40 | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | 10-0 | | 0.60 | | 0.60 | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | I _{OL} =18mA | | 0.40 | | 0.40 | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | I _{OL} =24mA | | 0.55 | | 0.55 | |
| I _{IN} | Input Leakage Current | 0.90 to 3.60 | $0 \leq V_{IN} \leq 3.60V$ | | ±0.1 | | ±0.5 | μA |
| I _{OFF} | Power Off Leakage Current | 0 | $0 \leq (V_{\text{IN}}, V_{\text{O}}) \leq 3.60 V$ | | 0.5 | | 0.5 | μA |
| | Quiescent | 0.00 to 2.60 | V _{IN} =V _{CC} or GND | | 0.9 | | 0.9 | |
| Icc | Supply Current | 0.90 to 3.60 | $V_{CC} \leq V_{IN} \leq 3.60 V$ | | | | ±0.9 | μA |
| 1 | Increase in I _{CC} | 1.95 | V _{IN} =0.9V | | 6 | | 8 | |
| ICCT | per Input | 3.60 | V _{IN} =1.5V | | 6 | | 8 | μA |

AC Electrical Characteristics

| Symbol | Parameter | V | Conditions | Conditions T _A =25°C | | T _A =-40 to 85°C | | Units | Figure | |
|-----------------|-------------------------------------|------------------------------|---|---------------------------------|------|-----------------------------|------|-------|--------|----------------------|
| Symbol | Farameter | V _{cc} | Conditions | Min. | Тур. | Max. | Min. | Max. | Units | Figure |
| | | 0.90 | C_L =15pF, R_L =1M Ω | | 34.0 | | | | | |
| | | $1.10 \leq V_{CC} \leq 1.30$ | $C_L=15pF, R_L=2k\Omega$ | 3.5 | 8.1 | 16.5 | 3.0 | 27.8 | | |
| | | $1.40 \leq V_{CC} \leq 1.60$ | | 1.5 | 3.7 | 7.0 | 1.5 | 7.5 | | Figure 4 Figure 5 |
| IPHL, IPLH | | $1.65 \leq V_{CC} \leq 1.95$ | C _L =30pF, R _L =500Ω | 1.1 | 2.8 | 5.8 | 1.0 | 6.3 | | |
| | | $2.30 \leq V_{CC} \leq 2.70$ | | 0.6 | 2.0 | 4.0 | 0.6 | 4.5 | | |
| | | $2.70 \leq V_{CC} \leq 3.60$ | | 0.5 | 1.5 | 3.5 | 0.5 | 4.0 | | \geq |
| C _{IN} | Input Capacitance | 0 | | | 3 | | | | pF | $\mathbf{\Sigma}$ |
| C _{PD} | Power Dissipation Capacitance | 0.90 to 3.60 | V _{IN} =0V or V _{CC} , f=10MHz | | 5 | | | | pF | |





Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: <u>http://www.fairchildsemi.com/products/analog/pdf/sc70-5_tr.pdf</u>.

| Package Designator | Tape Section | Cavity Number | Cavity Status | Cover Type Status |
|--------------------|--------------------|---------------|---------------|-------------------|
| | Leader (Start End) | 125 (Typical) | Empty | Sealed |
| P5X | Carrier | 3000 | Filled | Sealed |
| | Trailer (Hub End) | 75 (Typical) | Empty | Sealed |

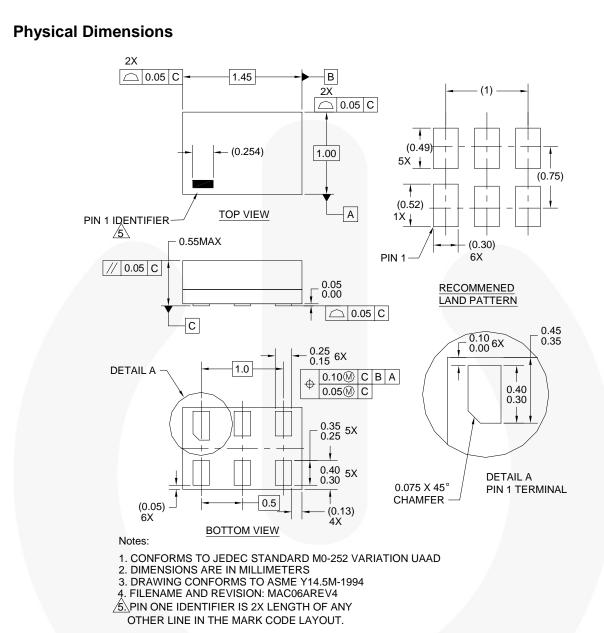


Figure 7. 6-Lead, MicroPak™, 1.0mm Wide

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Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/logic/pdf/micropak_tr.pdf.

| | Package Designator | Tape Section | Cavity Number | Cavity Status | Cover Type Status |
|---|--------------------|--------------------|---------------|---------------|-------------------|
| ſ | | Leader (Start End) | 125 (Typical) | Empty | Sealed |
| | L6X | Carrier | 5000 | Filled | Sealed |
| | | Trailer (Hub End) | 75 (Typical) | Empty | Sealed |

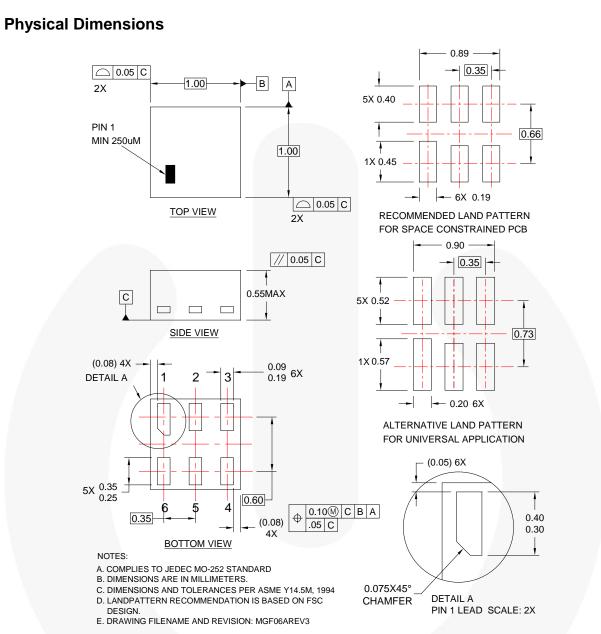


Figure 8. 6-Lead, MicroPak2[™], 1x1mm Body, .35mm Pitch

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Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: <u>http://www.fairchildsemi.com/packaging/MicroPAK2_6L_tr.pdf</u>.

| Package Designator | Tape Section | Cavity Number | Cavity Status | Cover Type Status |
|--------------------|--------------------|---------------|---------------|-------------------|
| | Leader (Start End) | 125 (Typical) | Empty | Sealed |
| FHX | Carrier | 5000 | Filled | Sealed |
| | Trailer (Hub End) | 75 (Typical) | Empty | Sealed |



ESBC™

Fairchild®

Fairchild Semiconductor® FACT Quiet Series™ FACT[®] FastvCore™ FETBench™ FlashWriter®* **FPSTM**

MicroPak™ MicroPak2™ MillerDrive™ MotionMax™ Motion-SPM™ OptoHiT™ **OPTOLOGIC®** OPTOPLANAR® PDP SPM™ Power-SPM™

SPM® STEALTH™ SuperFET[®] SuperSOT™-3 SuperSOT™6 SuperSOT™-8 SupreMOS⁶ SyncFET™ Sync-Lock™ SYSTEM GENERAL®*

TriFault Detect™ TRUECURRENT" µSerDes™

W Ser UHC Ultra FRFET™

UniFFT™ VCXTM VisualMax™ XSTM

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- 2 A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|-----------------------|--|
| Advance Information | Formative / In Design | Datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | Datasheet contains preliminary data, supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
| No Identification Needed | Full Production | Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design. |
| Obsolete | Not In Production | Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only. |

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