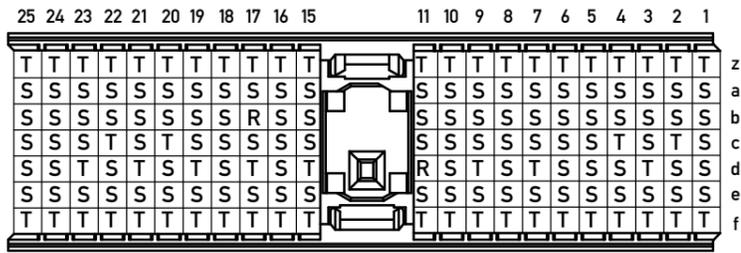
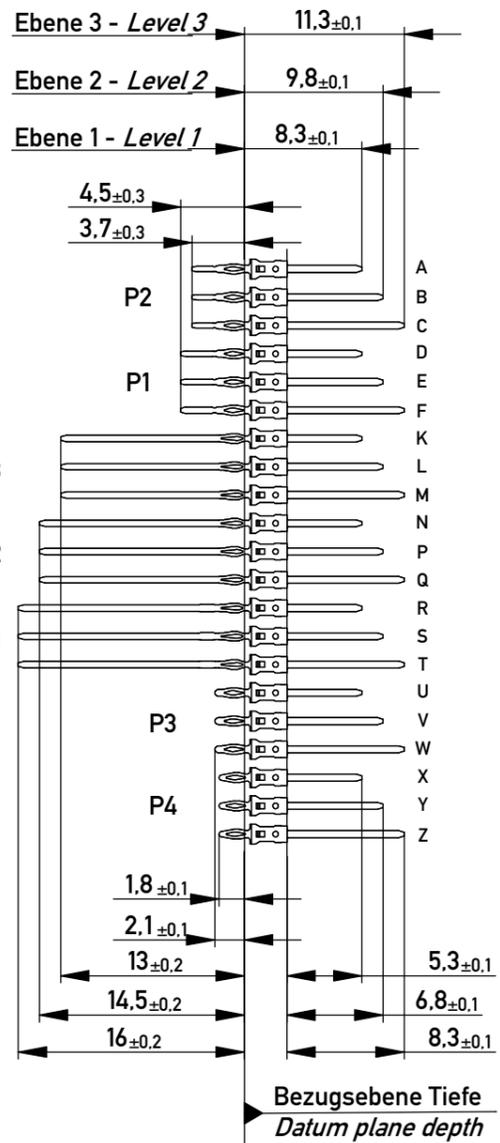
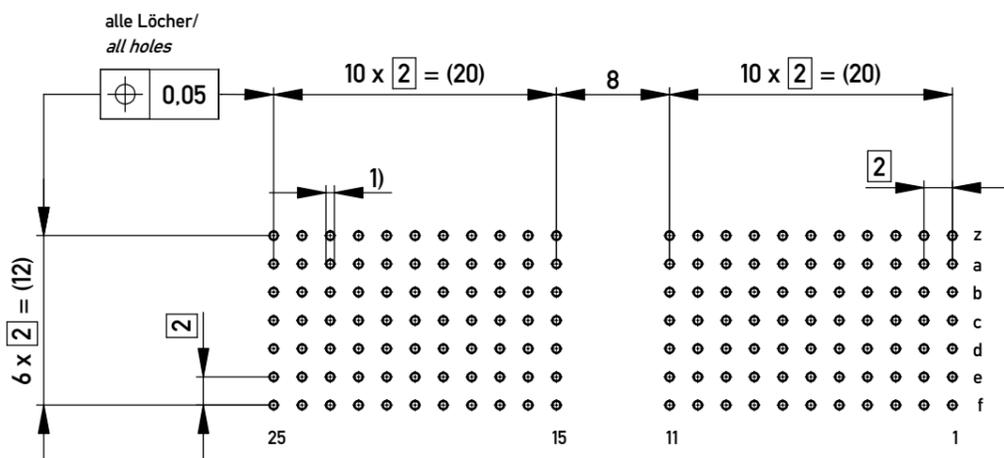


Bestückungsplan / contact layout



Lochbild für Leiterplatte  
(Bestückungsseite)

board hole pattern  
(component mounting side)



1)  $\phi 0,6 \pm 0,05$  Durchmesser des metallisierten Loches  
 $\phi 0,6 \pm 0,05$  Diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 164062 Nr.1  
Metal plating of plated-through hole according to 164062 No.1

Fehlende Maße und Angaben nach IEC 61076-4-101  
Missing information and dimensions per IEC 61076-4-101

Information:  
R1 Level 1, 2, 3  
siehe Bestückungsplan / see contact layout  
Anforderungsstufe 2 / class 2  
ohne Fixierstift / without locating peg

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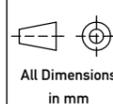
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21.02.2018

Index

Date

Tolerances



Scale 2:1

Subject to modification without prior notice.  
Drawing will not be updated.

Designation

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