

Si4446DY

**Vishay Siliconix** 

# N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)			
40	0.040 at V <sub>GS</sub> = 10 V	5.2	8			
	0.045 at V <sub>GS</sub> = 4.5 V	4.9	0			

8 D D

7

6 D

5 D

Si4446DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

SO-8

Top View

Ordering Information: Si4446DY-T1-E3 (Lead (Pb)-free)

S 1

S 2

S 3

G

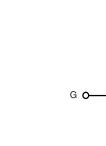
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## **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub>
- 100 % R<sub>a</sub> UIS Tested

### **APPLICATIONS**

CCFL Inverter



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T<sub>A</sub> = 25 °C, unless otherwise noted Parameter Symbol 10 s Steady State Unit **Drain-Source Voltage** V<sub>DS</sub> 40 ٧ Gate-Source Voltage  $V_{GS}$ ± 12 T<sub>A</sub> = 25 °C 5.2 3.9 Continuous Drain Current (T<sub>J</sub> = 150 °C)<sup>a</sup>  $I_D$ T<sub>A</sub> = 70 °C 4.2 3.1 Pulsed Drain Current  $I_{DM}$ 30 А  $I_S$ 1.7 Continuous Source Current (Diode Conduction)<sup>a</sup> 0.9 Avalanche Current  $I_{AS}$ 13 L = 0.1 mH Single-Pulse Avalanche Energy  $E_{AS}$ 8.5 mJ T<sub>A</sub> = 25 °C 2.0 1.1  $P_D$ W Maximum Power Dissipation<sup>a</sup> T<sub>A</sub> = 70 °C 1.3 0.7 T<sub>J</sub>, T<sub>stg</sub> Operating Junction and Storage Temperature Range - 55 to 150 °C

THERMAL RESISTANCE RATINGS									
Parameter	Symbol	Typical	Maximum	Unit					
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	R <sub>thJA</sub>	52	62.5					
Maximum Junction-to-Ambient	Steady State		90	110	°C/W				
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	32	40					

Notes:

a. Surface mounted on 1" x 1" FR4 board.



Available





Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit	
Static							
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.6		1.6	V	
V <sub>DS</sub> Temperature Coefficient	$\frac{\Delta V_{DS}/T_J}{\Delta V_{GS(th)}/T_J} I_D = 250 \ \mu A$		40		mV/°C		
V <sub>GS(th)</sub> Temperature Coefficient		1 <sub>D</sub> = 250 μA		- 3.8		mv/°C	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
		$V_{DS}$ = 40 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	20			А	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5.2 \text{ A}$		0.033	0.040	Ω	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 4.9 \text{ A}$		0.037	0.045		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5.2 \text{ A}$		18		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.2	V	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			700		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, f = 1 MHz		76			
Reverse Transfer Capacitance	C <sub>rss</sub>			45			
Total Gate Charge	Qg			8	12	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 20$ V, $V_{GS} = 4.5$ V, $I_{D} = 5.2$ A		1.5			
Gate-Drain Charge	Q <sub>gd</sub>			2.4			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.9	2.9	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			7	11		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		11	17	ns	
Turn-Off DelayTime	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		27	40		
Fall Time	t <sub>f</sub>			8	13		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.7 A, dl/dt = 100 A/μs		25	40		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$Q_{\rm rr}$ $F = 1.7 A, avat = 100 A/\mu s$		17	26	nC	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

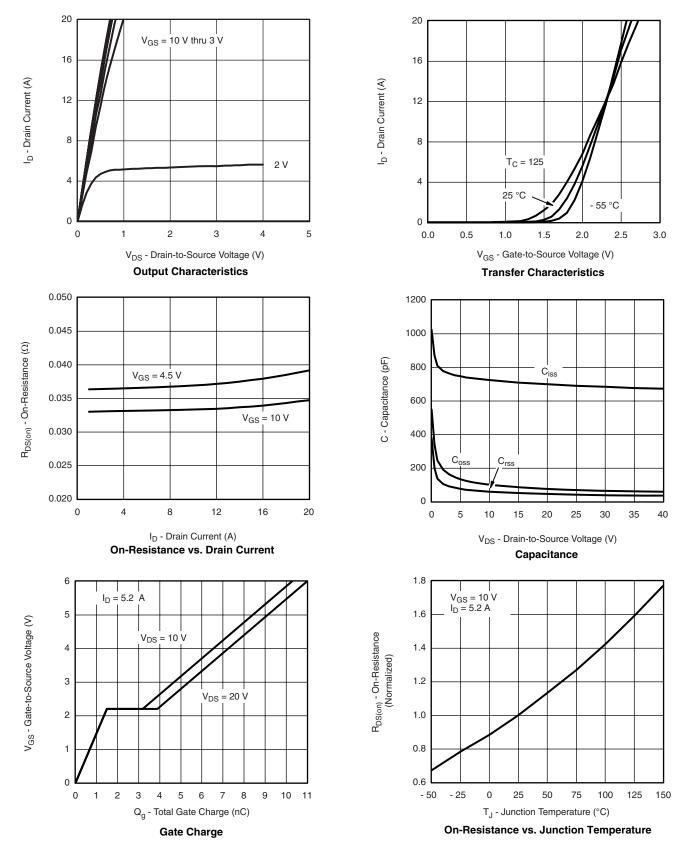




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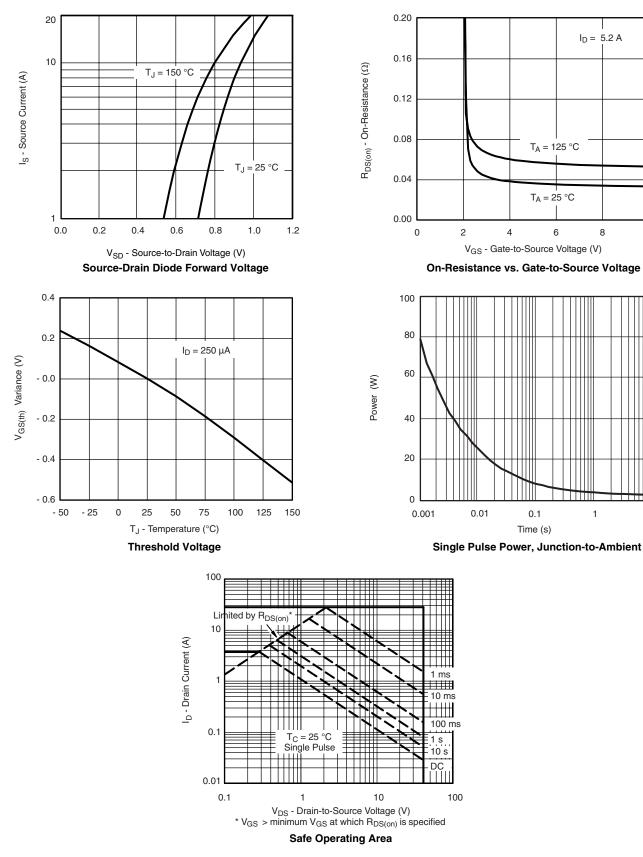
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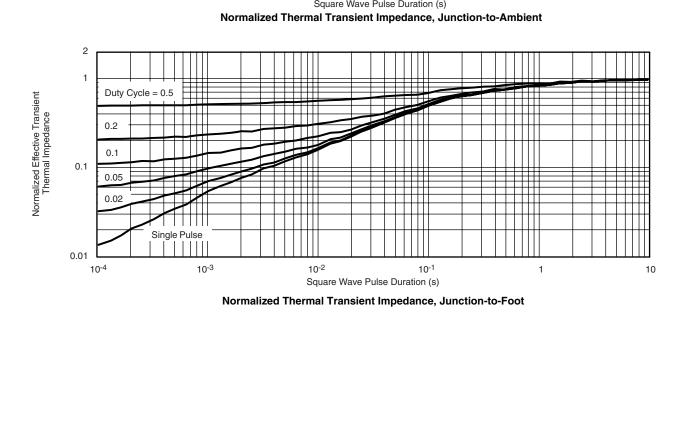


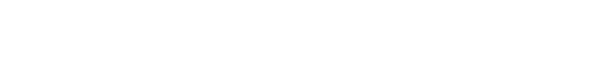
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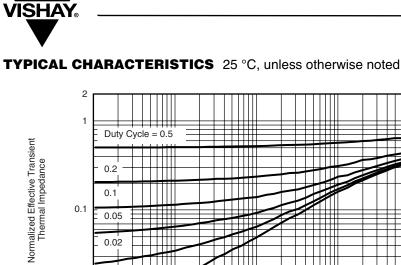
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







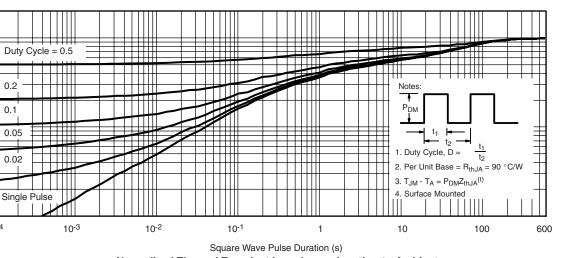
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0.1

0.01

10-4



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