

DMG3415UFY4

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	39mΩ @ V _{GS} = -4.5V	-2.5A
-16V	52mΩ @ V _{GS} = -2.5V	-2.1A
	65mΩ @ V _{GS} = -1.8V	-1.8A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

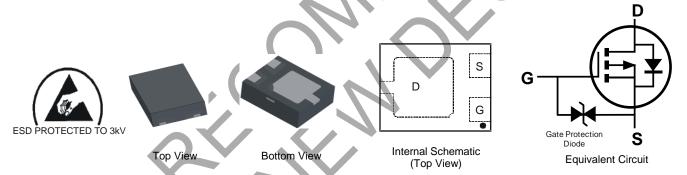
- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMG3415UFY4Q)

Mechanical Data

- Case: X2-DFN2015-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208⁽²⁾
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

	Part Number	Case	Packaging
	DMG3415UFY4-7	X2-DFN2015-3	3,000/Tape & Reel
Notes:	1. No purposely added lead. Fully EU Direc	tive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/	863/EU (RoHS 3) compliant.
	2. See https://www.diodes.com/quality/lead	free/ for more information about Diodes Incorporated's of	definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

•	34P
	YM

34P = Marking CodeYM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = Senter

M = Month (ex: 9 = September)

Date Code Key

Year	2009	-	~	2015	2016	20	17	2018	2019	20	20	2021
Code	W	-	,	С	D	l	Ξ	F	G	ŀ	1	Ι
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	-16	V	
Gate-Source Voltage		V _{GSS}	±8	V	
Continuous Drain Current (Note 6) V_{GS} = -4.5V	Steady State	ID	-2.5 -2.2	A	
Pulsed Drain Current (Note 6)		I _{DM}	-12	А	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.65	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R ₀ JA	197	°C/W
Total Power Dissipation (Note 6)	·	PD	1.35	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	95	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _{0JC}	22	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-16			V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I _{DSS}	-	_	-1.0	μA	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Source Leakage				±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
,	IGSS		_	±500	nA	$V_{GS} = \pm 5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.3	-0.55	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
			31	39		$V_{GS} = -4.5V, I_D = -4.0A$
Static Drain-Source On-Resistance	RDS(ON)	_	40	52	mΩ	$V_{GS} = -2.5V, I_D = -3.5A$
			51	65		$V_{GS} = -1.8V, I_D = -2.0A$
Forward Transfer Admittance	Y _{fs}	_	7.9		S	$V_{DS} = -5V, I_D = -2.5A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	282		pF	
Output Capacitance	Coss		152		pF	−V _{DS} = -10V, V _{GS} = 0V −f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	38		pF	
Gate Resistance	Rg		250		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge	Qg	—	10		nC	
Gate-Source Charge	Q _{gs}	—	1.5		nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_D = -4A$
Gate-Drain Charge	Q _{gd}		2.4		nC	
Turn-On Delay Time	t _{D(ON)}	_	79		ns	
Turn-On Rise Time	t _R		175		ns	$V_{DS} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	tD(OFF)	_	885	_	ns	$R_D = 2.5\Omega, R_G = 3.0\Omega$
Turn-Off Fall Time	t _F		568		ns	

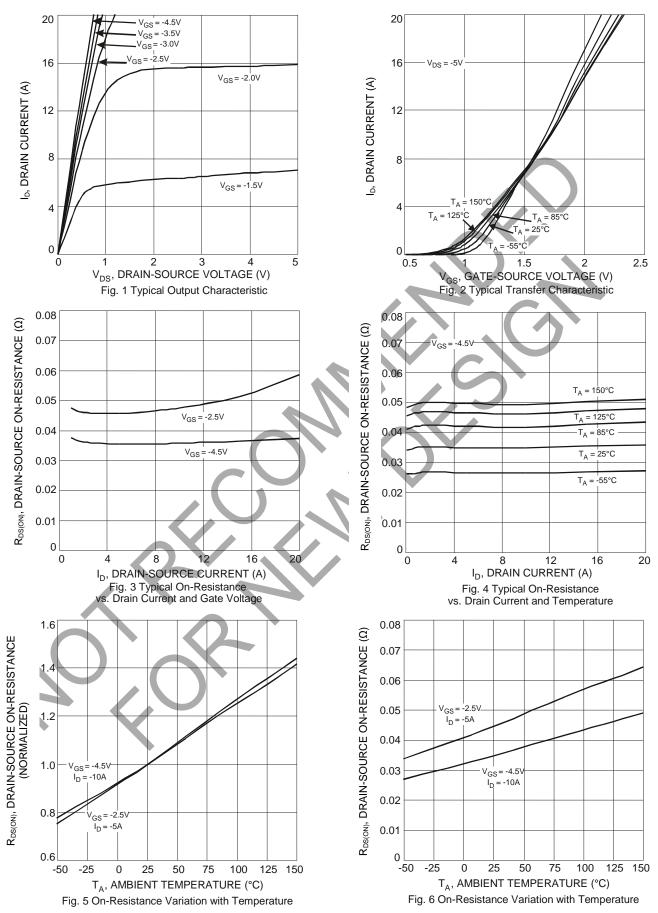
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect. Notes:

8. Guaranteed by design. Not subject to product testing.



NOT RECOMMENDED FOR NEW DESIGN USE <u>DMP2045UFY4</u>

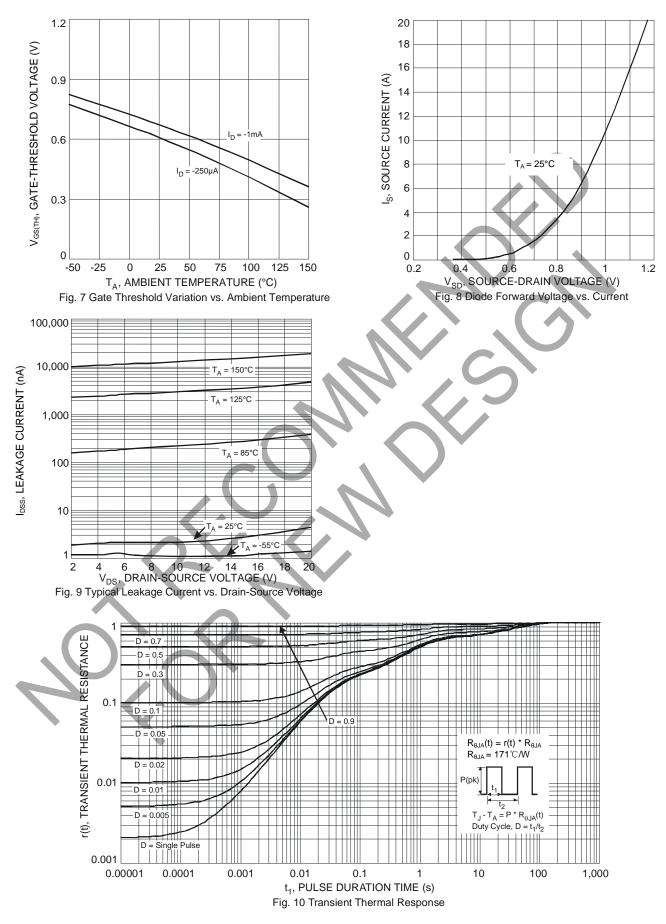
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Value

(in mm)

1.000

0.150 0.310

1.300

0.500

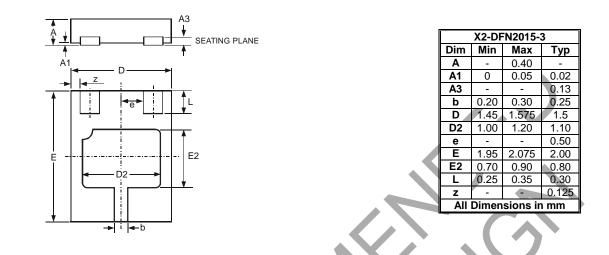
0.650

1.000

Package Outline Dimensions

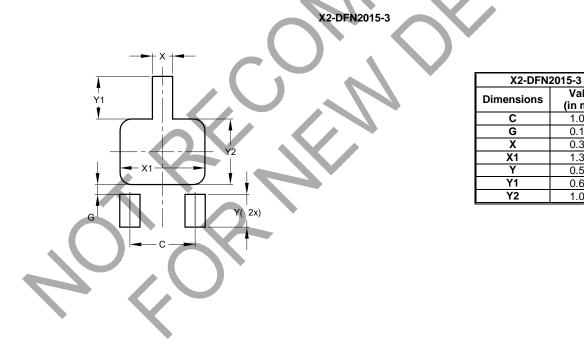
Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN2015-3



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





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