#### PHYSICAL DIMENSIONS:

IMPEDANCE (Ω)

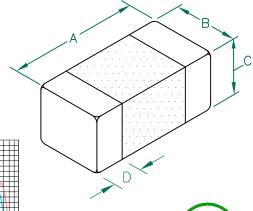
# MI0603J601R-10

## UNCONTROLLED DOCUMENT

A 1.60 [.063] ± 0.15 [.006]

B 0.80 [.031] ± 0.15 [.006]

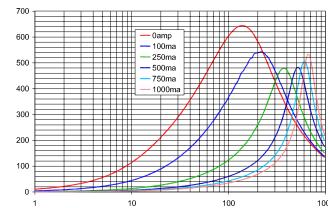
D 0.36 [.014] ± 0.15 [.006]



NOTES: UNLESS OTHERWISE SPECIFIED

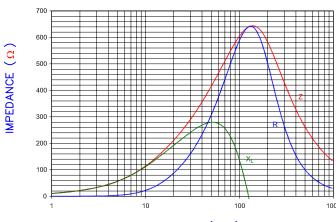
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

### Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



#### FREQUENCY (MHz)

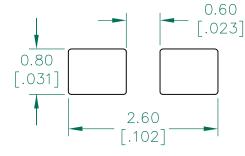
#### |Z| , R, AND X vs. FREQUENCY



FREQUENCY (MHz)

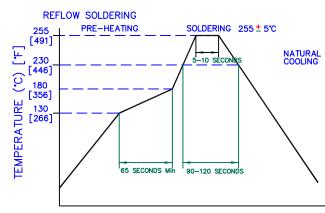
- X<sub>1</sub>

#### LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension)

#### RECOMMENDED SOLDERING CONDITIONS





DIMENSIONS ARE IN mm [INCHES].					This print is the property of Laire	<b>.</b>				
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					with the understanding that no	""	Laird			
					copies shall be made without the					
					written consent of Laird Tech. All	ч				
					rights to design or invention are reserved.	- 1				
					PROJECT/PART NUMBER:	TR	EV	PART TO	rPE:	DRAWN BY:
	С	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	MI0603J601R-10		С	co-	-FIRE	ТМВ
	В	UPDATE COMPANY LOGO	07/24/08	JRK	DATE: 04/05/04 S	CALE	:	_	SHEET:	
	Α	ORIGINAL DRAFT	04/05/04	ТМВ	, , , , , , , , , , , , , , , , , , ,	OOL			1	of 1
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