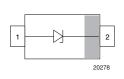
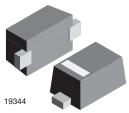


# **Single ESD Protection Diode in SOD-523**





#### **DESIGN SUPPORT TOOLS**

click logo to get started



#### **MARKING** (example only)



Bar = cathode marking

X = date code

Y = type code (see table below)

#### **FEATURES**

- Single-line ESD protection
- · Low leakage current
- ESD immunity acc. IEC 61000-4-2 ± 8 kV contact discharge ± 15 kV air discharge
- e3 Sn
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





**HALOGEN** FREE

**GREEN** (5-2008)

ORDERING INFORMATION							
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY				
VESD01-02V	VESD01-02V-G-08	3000	3000				
VESD03-02V	VESD03-02V-G-08	3000	3000				
VESD05-02V	VESD05-02V-G-08	3000	3000				
VESD08-02V	VESD08-02V-G-08	3000	3000				
VESD12-02V	VESD12-02V-G-08	3000	3000				

PACKAGE DAT	ГА					
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VESD01-02V	SOD-523	.∀	1.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals
VESD03-02V	SOD-523	В.	1.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals
VESD05-02V	SOD-523	.Э	1.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals
VESD08-02V	SOD-523	. П	1.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals
VESD12-02V	SOD-523	. Ξ	1.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals



ABSOLUTE MAXIMUM RATINGS VESD01-02V							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I <sub>PPM</sub>	7	Α			
Peak pulse power	Acc. IEC 61000-4-5, 8/20 μs/single shot	$P_{PP}$	63	W			
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 8	kV			
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV			
Operating temperature	Junction temperature	TJ	-40 to +125	°C			
Storage temperature		T <sub>stg</sub>	-55 to +150	°C			

ABSOLUTE MAXIMUM RATINGS VESD03-02V							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	9	Α			
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	108	W			
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	± 8	kV			
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 15	kV			
Operating temperature	Junction temperature	TJ	-40 to +125	°C			
Storage temperature		T <sub>stg</sub>	-55 to +150	°C			

ABSOLUTE MAXIMUM RATINGS VESD05-02V						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	6	Α		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	$P_PP$	120	W		
ECD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	W	± 8	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Operating temperature	Junction temperature	TJ	-40 to +125	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS VESD08-02V						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	4	Α		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	$P_{PP}$	120	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 8	kV		
ESD IIIIIIdriity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Operating temperature	Junction temperature	T <sub>J</sub>	-40 to +125	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM	RATINGS VESD12-02V			
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	2	Α
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	$P_PP$	25	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 8	kV
ESD IIIIIIdriity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV
Operating temperature	Junction temperature	TJ	-40 to +125	°C
Storage temperature		T <sub>stg</sub>	-55 to +150	°C



<b>ELECTRICAL CHARAC</b> (T <sub>amb</sub> = 25 °C, unless oth	TERISTICS VESD01-02V erwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	=	-	1	V
Reverse voltage	at I <sub>R</sub> = 100 μA	$V_R$	1	-	-	V
Reverse current	at V <sub>R</sub> = 1 V	I <sub>R</sub>	=	-	100	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	1.5	-	-	V
Reverse clamping voltage	at I <sub>PP</sub> (see fig. 1)	V <sub>C</sub>	=	9	-	V
Capacitance	at $V_B = 0$ V: $f = 1$ MHz	Cn	-	180	-	pF

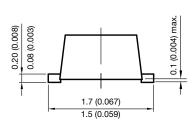
ELECTRICAL CHARACT (T <sub>amb</sub> = 25 °C, unless other	TERISTICS VESD03-02V rwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	=	1	lines
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	3	V
Reverse voltage	at I <sub>R</sub> = 20 μA	$V_R$	3	-	-	V
Reverse current	at V <sub>R</sub> = 3 V	I <sub>R</sub>	-	-	20	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	4	-	-	V
Reverse clamping voltage	at I <sub>PP</sub> (see fig. 1)	V <sub>C</sub>	-	12	-	V
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	-	110	-	pF

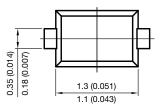
<b>ELECTRICAL CHARAC</b> (T <sub>amb</sub> = 25 °C, unless othe	TERISTICS VESD05-02V rwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	5	V
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	5	-	-	V
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	0.1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6.5	-	-	V
Reverse clamping voltage	at I <sub>PP</sub> (see fig. 1)	V <sub>C</sub>	-	20	-	V
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	-	55	-	pF

ELECTRICAL CHARACT (T <sub>amb</sub> = 25 °C, unless other	TERISTICS VESD08-02V rwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	8	V
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	8	-	-	V
Reverse current	at V <sub>R</sub> = 8 V	I <sub>R</sub>	-	-	0.1	μA
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	9	-	-	V
Reverse clamping voltage	at I <sub>PP</sub> (see fig. 1)	V <sub>C</sub>	-	30	-	V
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	35	-	pF

<b>ELECTRICAL CHARAC</b> (T <sub>amb</sub> = 25 °C, unless other	TERISTICS VESD12-02V rwise specified)					
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	12	V
Reverse voltage	at I <sub>R</sub> = 0.1 μA	$V_R$	12	=	-	V
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	-	-	0.1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	14	=	-	V
Reverse clamping voltage	at I <sub>PP</sub> (see fig. 1)	V <sub>C</sub>	-	25	-	V
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	-	30	-	pF

### PACKAGE DIMENSIONS in millimeters (Inches): SOD-523

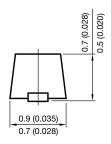




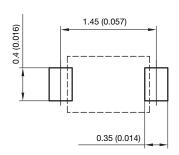
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