COMPLIANT

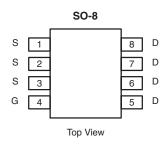
HALOGEN FREE





N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
30	0.0052 at V _{GS} = 10 V	24.5	21.5 nC			
	0.0067 at $V_{GS} = 4.5 \text{ V}$	21.7	21.5110			



Ordering Information: Si4634DY-T1-E3 (Lead (Pb)-free)

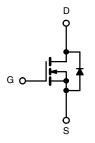
Si4634DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Buck Converter
- Synchronous Rectifier
 - Secondary Rectifier
- Notebook



N-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V_{DS}	30 ± 20	v		
Gate-Source Voltage				V_{GS}	
	T _C = 25 °C		24.5		
Continuous Drain Current (T _{.1} = 150 °C)	$T_C = 70 ^{\circ}C$	I _D	19.5		
Commission Brain Garrent (1) = 100 (0)	T _A = 25 °C	J .0 _	16.3 ^{b, c}		
	T _A = 70 °C		13.0 ^{b, c}	Α	
Pulsed Drain Current		I _{DM}	70		
Continuous Source-Drain Diode Current	T _C = 25 °C		5.1		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.2 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	30		
Avalanche Energy	L = 0.111111	E _{AS}	45	mJ	
	T _C = 25 °C		5.7		
Maximum Power Dissipation	T _C = 70 °C	P_{D}	3.6	w	
Maximum Fower Dissipation	T _A = 25 °C] '0 [2.5 ^{b, c}	VV	
	T _A = 70 °C	1	1.6 ^{b, c}		
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	39	50	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	18	22	O/ VV		

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under Steady State conditions is 85 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			l				
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I - 250 uA		33		m\//06	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_{D} = 250 \mu A$		- 6.4		mV/°(
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1.4		2.6	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oata Walkana Basin Oamant	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	6	
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
	Б	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$		0.0043	0.0052	_	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.0055	0.0067	.0067	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A		78		S	
Dynamic ^b	1		·	I			
Input Capacitance	C _{iss}			3150		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		420			
Reverse Transfer Capacitance	C _{rss}			166			
·		$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		45.5	68		
Total Gate Charge	al Gate Charge Q _g		21.5	33			
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		8.0		nC	
Gate-Drain Charge	Q_{gd}			6.2			
Gate Resistance	R_{g}	f = 1 MHz		0.75	1.5	Ω	
Turn-on Delay Time	t _{d(on)}			30	50		
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		15	30		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		33	55		
Fall Time	t _f			10	20	no	
Turn-on Delay Time	t _{d(on)}			14	25	ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω		33	55		
Fall Time	t _f			8	16		
Drain-Source Body Diode Characterist	ics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			5.1	Α	
Pulse Diode Forward Current ^a	I _{SM}				70		
Body Diode Voltage	V_{SD}	I _S = 3 A		0.75	1.1	٧	
Body Diode Reverse Recovery Time	t _{rr}			30	60	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 10 A dl/dt = 100 A/us T. = 25 °C		35	70	nC	
Reverse Recovery Fall Time	ta			20			
Reverse Recovery Rise Time	t _b			10		ns	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

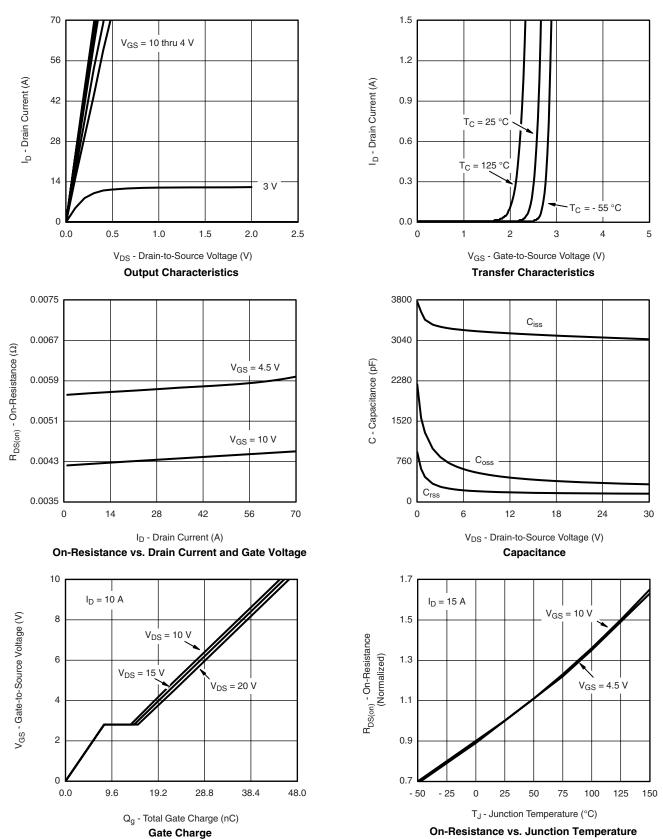
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing.





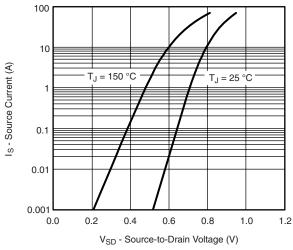
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



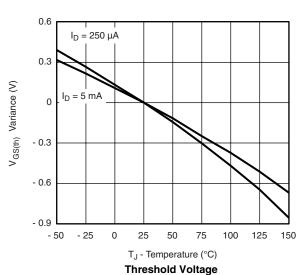
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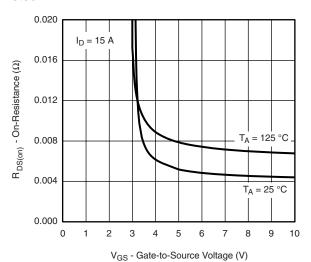
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

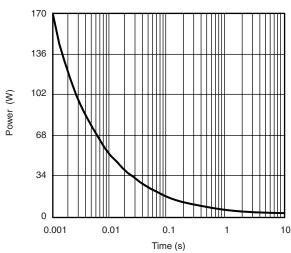


Source-Drain Diode Forward Voltage

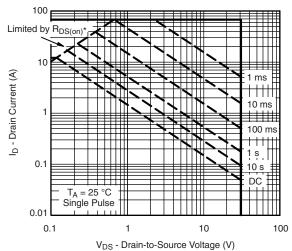




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

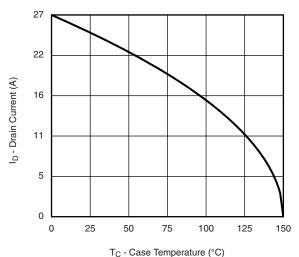


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

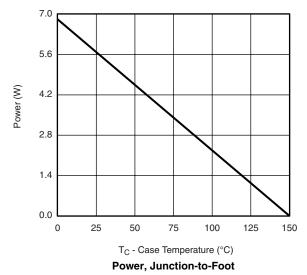


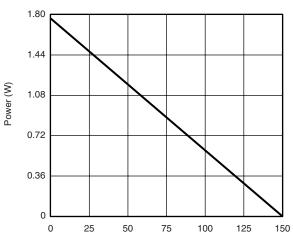
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*

Current Derating*





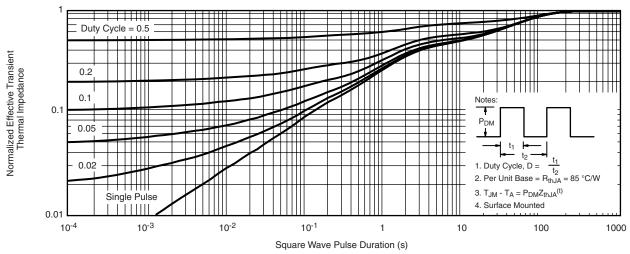
T_A - Ambient Temperature (°C) **Power, Junction-to-Ambient**

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

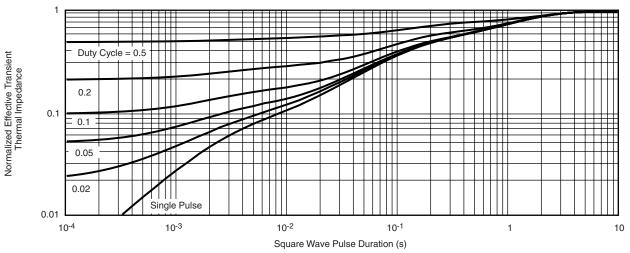
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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