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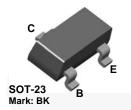


March 2014

BCX71K PNP General-Purpose Amplifier

Description

This device is designed for applications requiring extremely high-current gain at collector currents to 300 mA. Sourced from process 68.



Ordering Information

Part Number	Marking	Package	Packing Method
BCX71K	BK	SOT-23 3L	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-45	V
V _{CES}	Collector-Emitter Voltage -45		V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current - Continuous -500		mA
T_{J} , T_{STG}	Junction and Storage Temperature Range -55 to +150		°C

Notes

- 1. These ratings are based on a maximum junction temperature of 150°C.
- These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

1

Thermal Characteristics(3)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Unit
В	Total Device Dissipation	350	mW
P _D	Derate Above T _A = 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

Note:

3. Device mounted on FR-4 PCB 40 mm X 40 mm X 1.5 mm.

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_C = -1.0 \text{ mA}, I_B = 0$	-45		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = -10 \mu A, I_C = 0$	-5.0		V
I _{CES} Coll	Collector Cut-Off Current	$V_{CE} = -45 \text{ V}, I_{E} = 0$		-20	nA
		$V_{CE} = -45 \text{ V}, I_{E} = 0,$ $T_{A} = 100^{\circ}\text{C}$		-20	μА
	DC Current Gain	$I_C = -10 \mu A, V_{CE} = -5.0 V$	100		
h _{FE}		$I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$	380	630	
		$I_C = -50 \text{ mA}, V_{CE} = -1.0 \text{ V}$	110		
\/. (cat)	Collector-Emitter Saturation Voltage	$I_C = -10 \text{ mA}, I_B = -0.25 \text{ mA}$	-0.06	-0.25	V
V _{CE} (sat)		$I_C = -50 \text{ mA}, I_B = -1.25 \text{ mA}$	-0.12	-0.55	V
\/ (cat)	Base-Emitter Saturation Voltage	$I_C = -10 \text{ mA}, I_B = -0.25 \text{ mA}$	-0.60	-0.85	V
V _{BE} (sat)		$I_C = -50 \text{ mA}, I_B = -1.25 \text{ mA}$	-0.68	-1.05	
V _{BE} (on)	Base-Emitter On Voltage	$I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$	-0.60	-0.75	V
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_E = 0,$ f = 1.0 MHz		6.0	pF
NF	Noise Figure	$\begin{split} &I_{C} = \text{-0.2 mA, V}_{CE} = \text{-5.0 V,} \\ &R_{S} = 2.0 \text{ k}\Omega, \text{ f} = 1.0 \text{ kHz,} \\ &B_{W} = 200 \text{ Hz} \end{split}$		6.0	dB
t _{on}	Turn-On Time	$I_C = -10 \text{ mA}, I_{B1} = -1.0 \text{ mA}$		150	ns
t _{off}	Turn-Off Time	$\begin{split} I_{B2} = -1.0 &\text{ mA, V}_{BB} = -3.6 \text{ V,} \\ R_1 = R_2 = 5.0 &\text{ k}\Omega, \\ R_L = 990 &\Omega, \end{split}$		800	ns

Typical Performance Characteristics

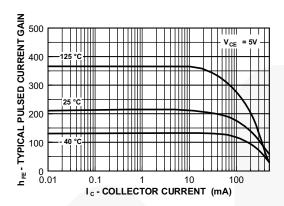


Figure 1. Typical Pulsed Current Gain vs. Collector Current

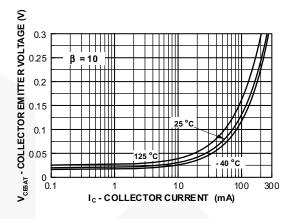


Figure 2. Collector-Emitter Saturation Voltage vs. Collector Current

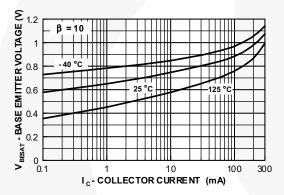


Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

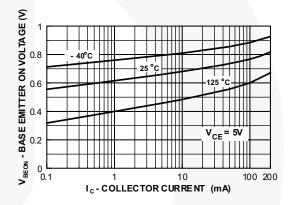


Figure 4. Base-Emitter On Voltage vs. Collector Current

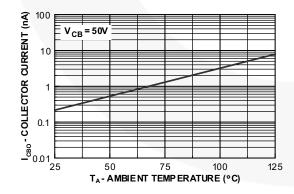


Figure 5. Collector Cut-Off Current vs.
Ambient Temperature

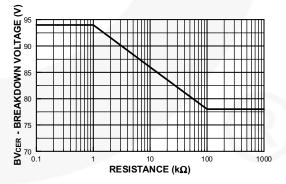


Figure 6. Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base

Typical Performance Characteristics (Continued)

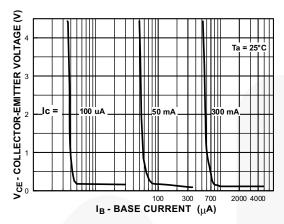


Figure 7. Collector Saturation Region

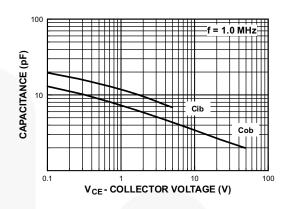


Figure 8. Input and Output Capacitance vs. Reverse Voltage

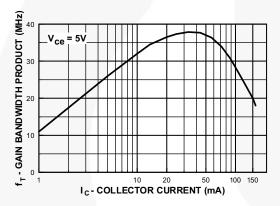


Figure 9. Gain Bandwidth Product vs. Collector Current

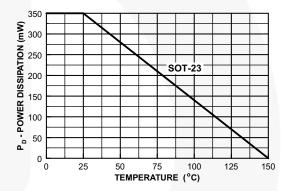


Figure 10. Power Dissipation vs. Ambient Temperature

Physical Dimensions

0.95 2.92±0.20 3 1.40 1.30^{+0.20}_{-0.15} 2.20 0.60 0.37 (0.29) -0.95 ⊕ 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION 1.20 MAX SEE DETAIL A (0.93)0.10 ○ 0.10 M C C 2.40±0.30 NOTES: UNLESS OTHERWISE SPECIFIED **GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. B) ALL DIMENSIONS ARE IN MILLIMETERS. 0.23 0.08 C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994. 0.20 MIN E) DRAWING FILE NAME: MA03DREV10 SEATING

SOT-23

Figure 11. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

PLANE

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(0.55)

DETAIL A





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Definition of Torms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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