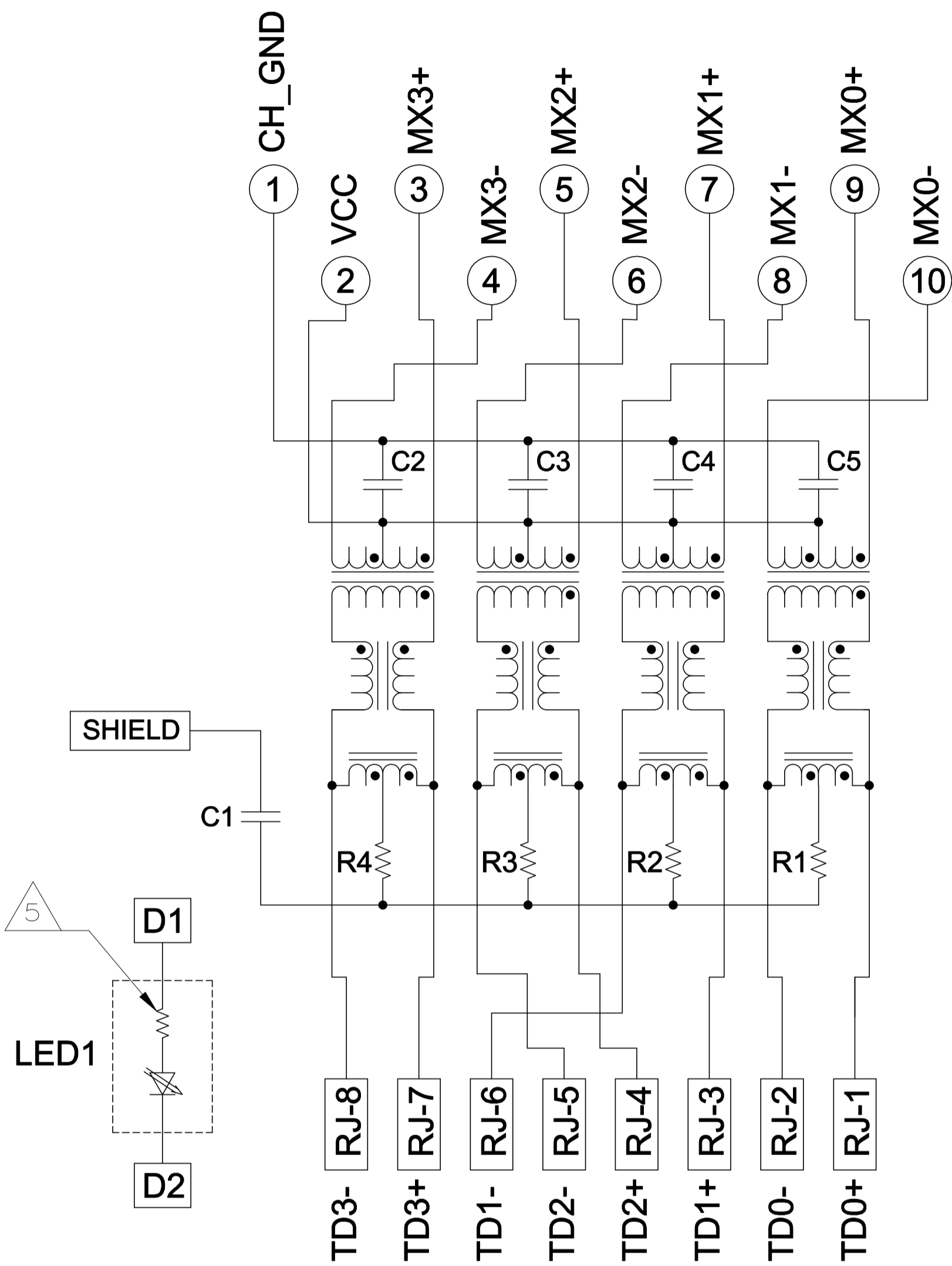
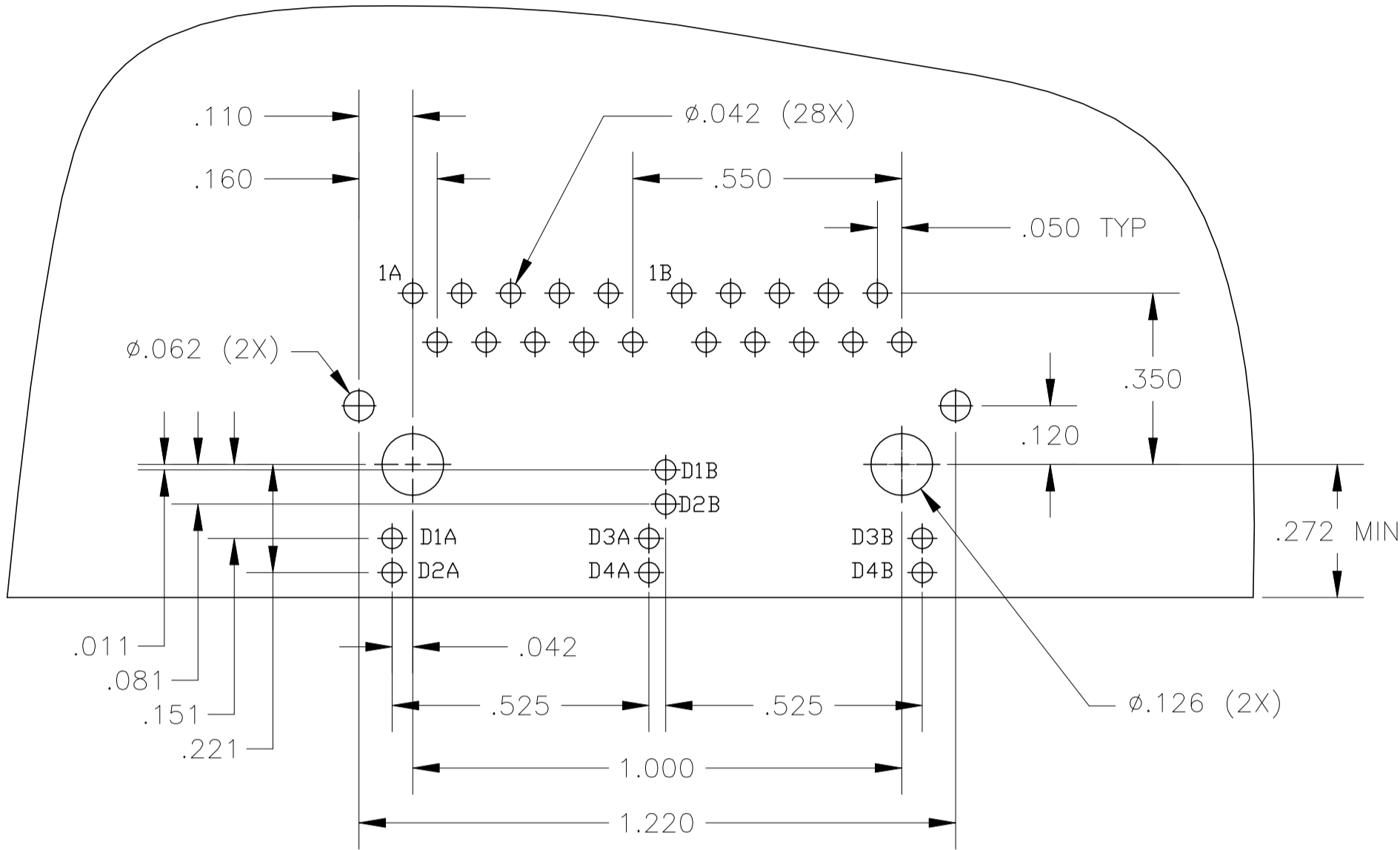


7G05_GIGABIT_SCHEMATIC

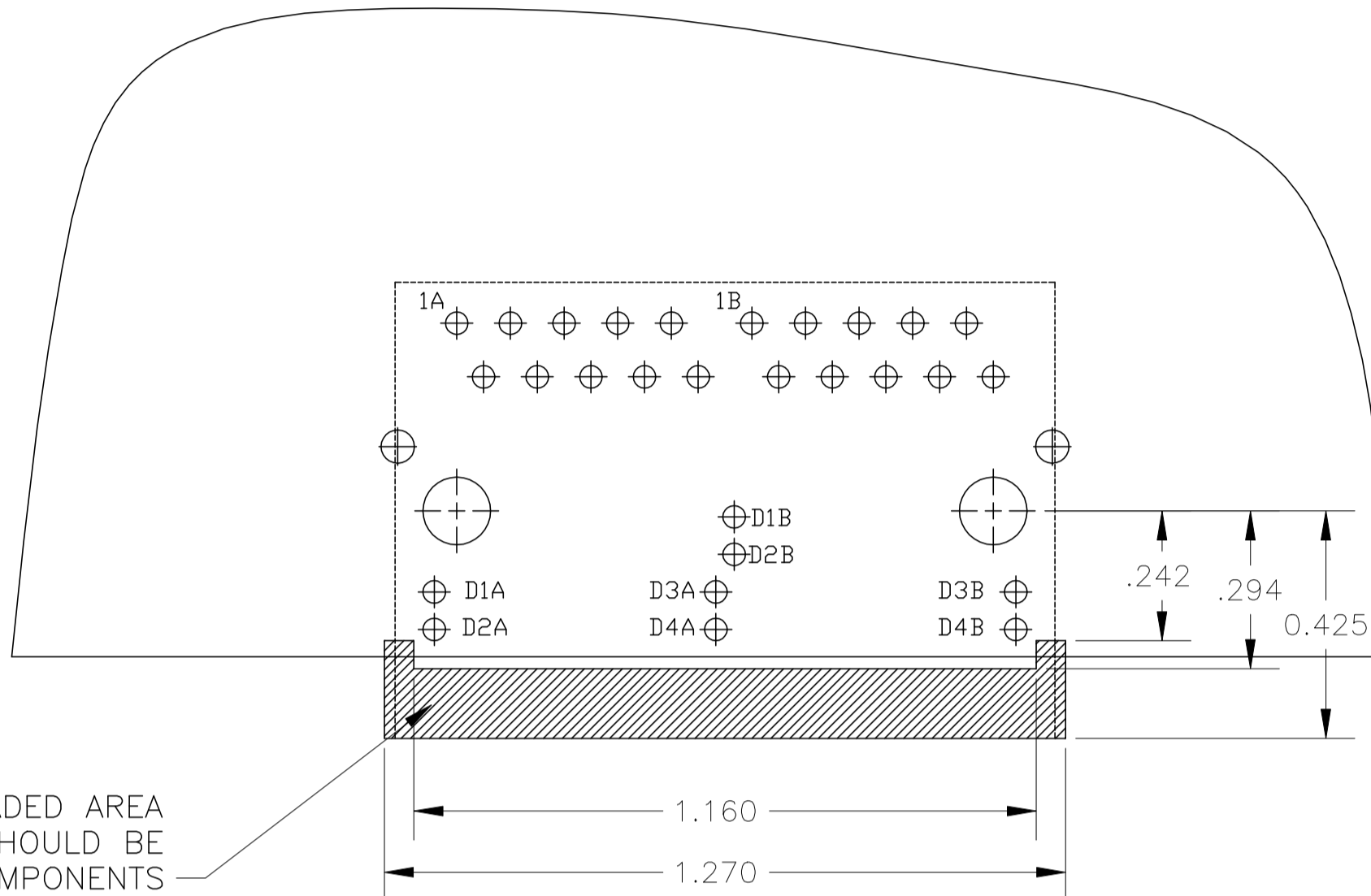


C1 = 1000 pF, 2kV DECOUPLING CAPACITOR
R1-R4 = 75 OHMS, 1/16 W, 5% RESISTORS
C2-C5 = 0.1uF, 10%, 50V, X7R CAPACITORS

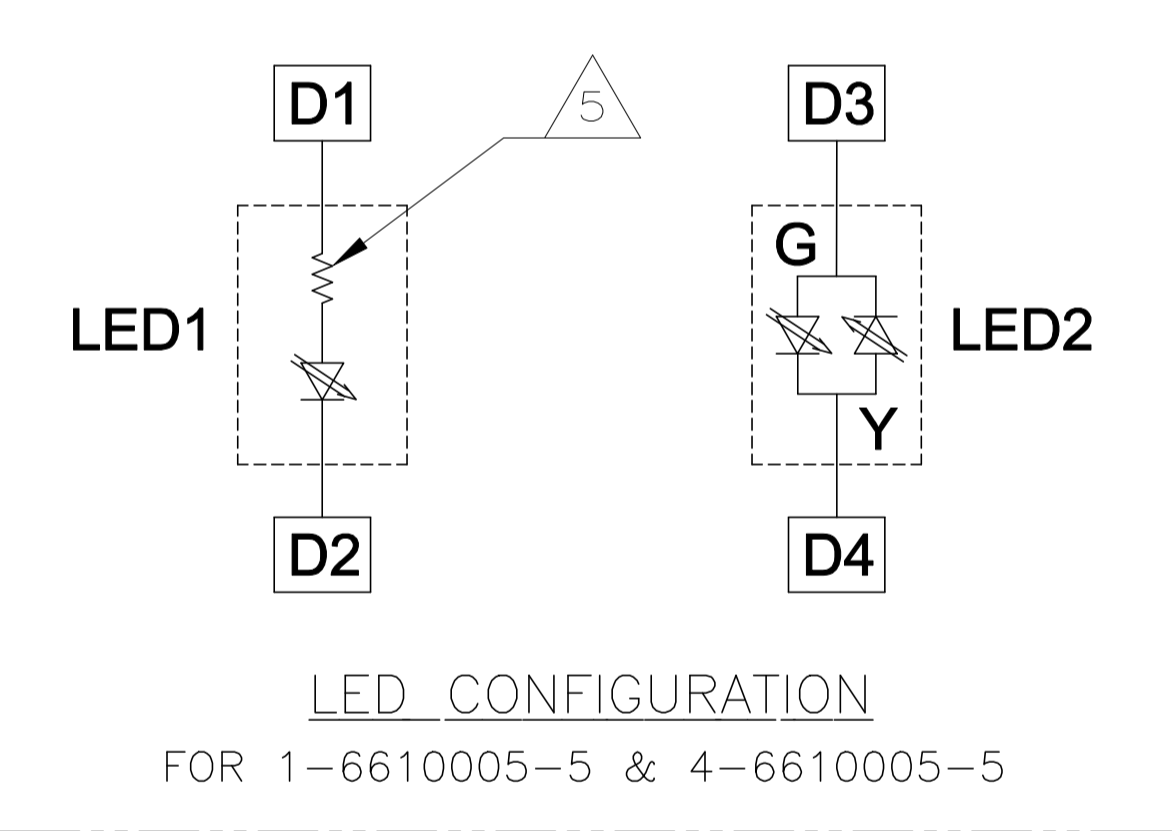


SUGGESTED PCB LAYOUT
(Component Side)

IT IS RECOMMENDED THAT SHADED AREA
OF CUSTOMER PC BOARD SHOULD BE
CLEAR OF ANY VIA HOLES AND COMPONENTS

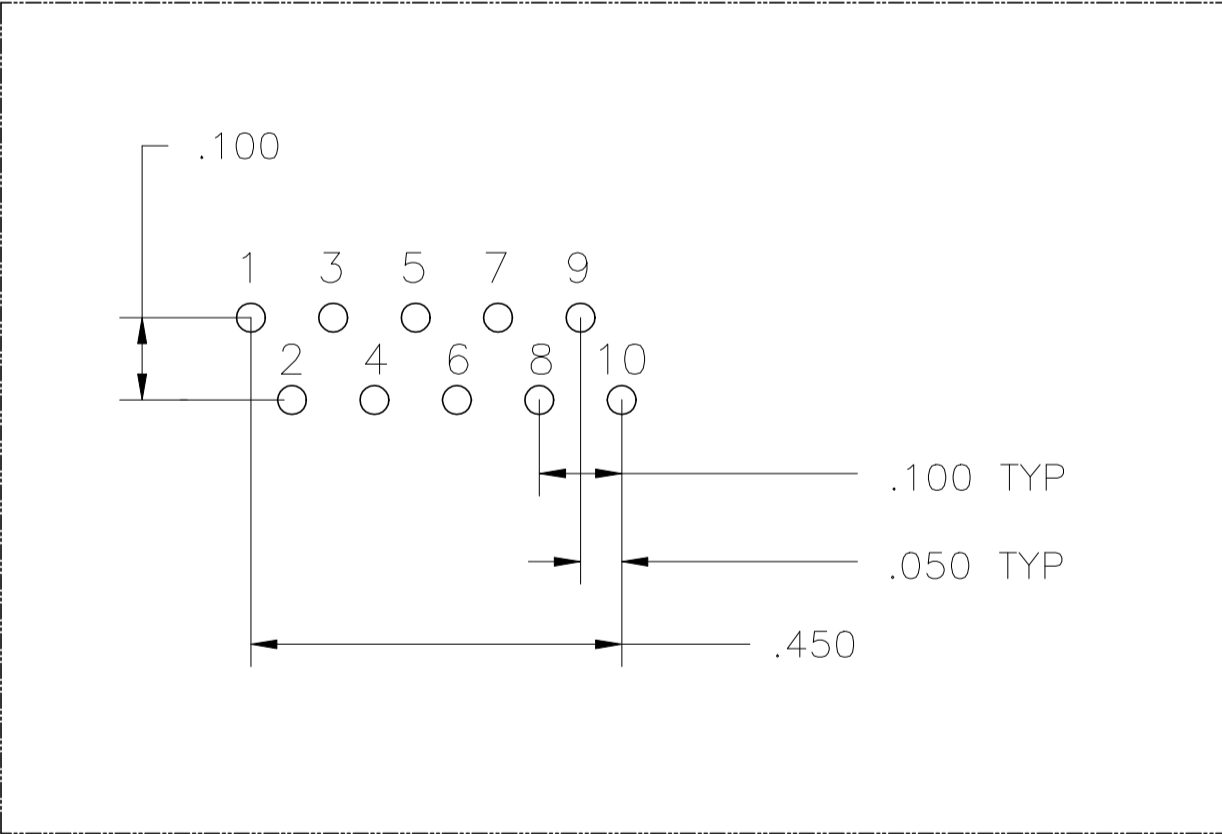


SUGGESTED PCB KEEP OUT AREA
(Component Side)

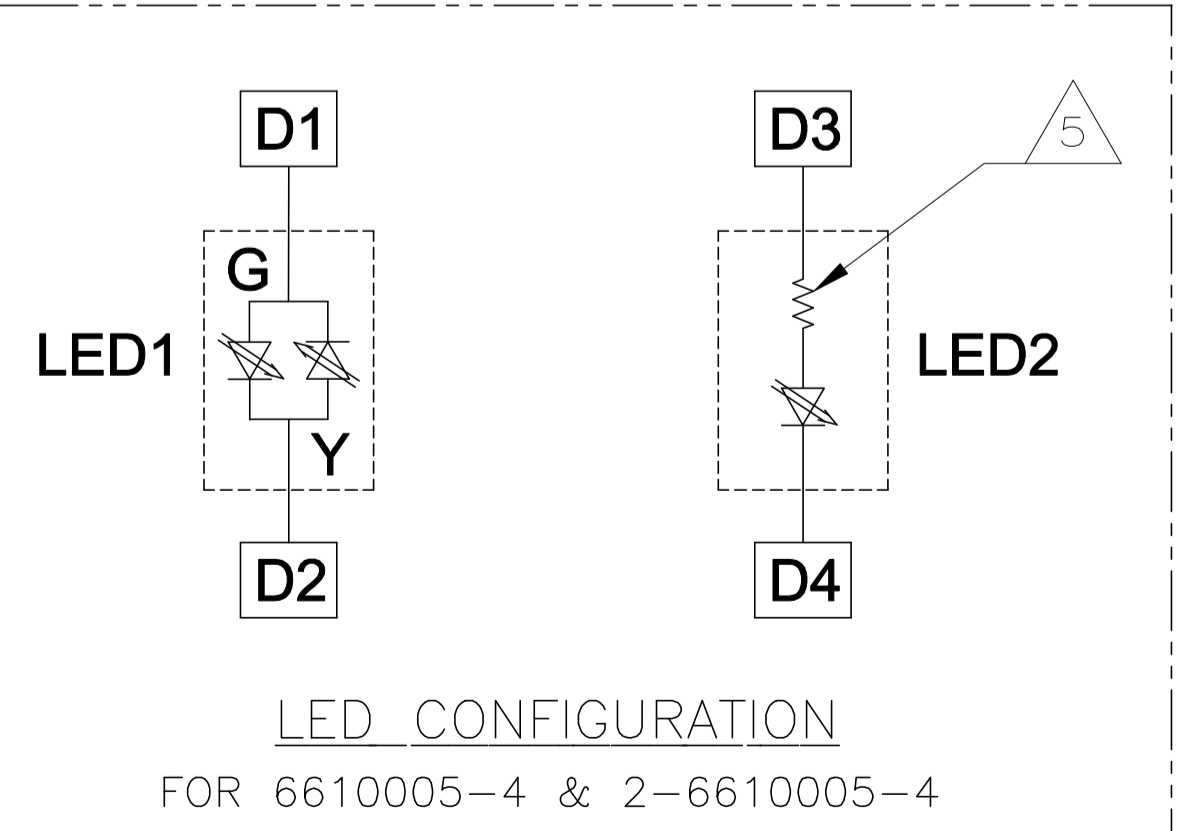


LED CONFIGURATION

FOR 1-6610005-5 & 4-6610005-5

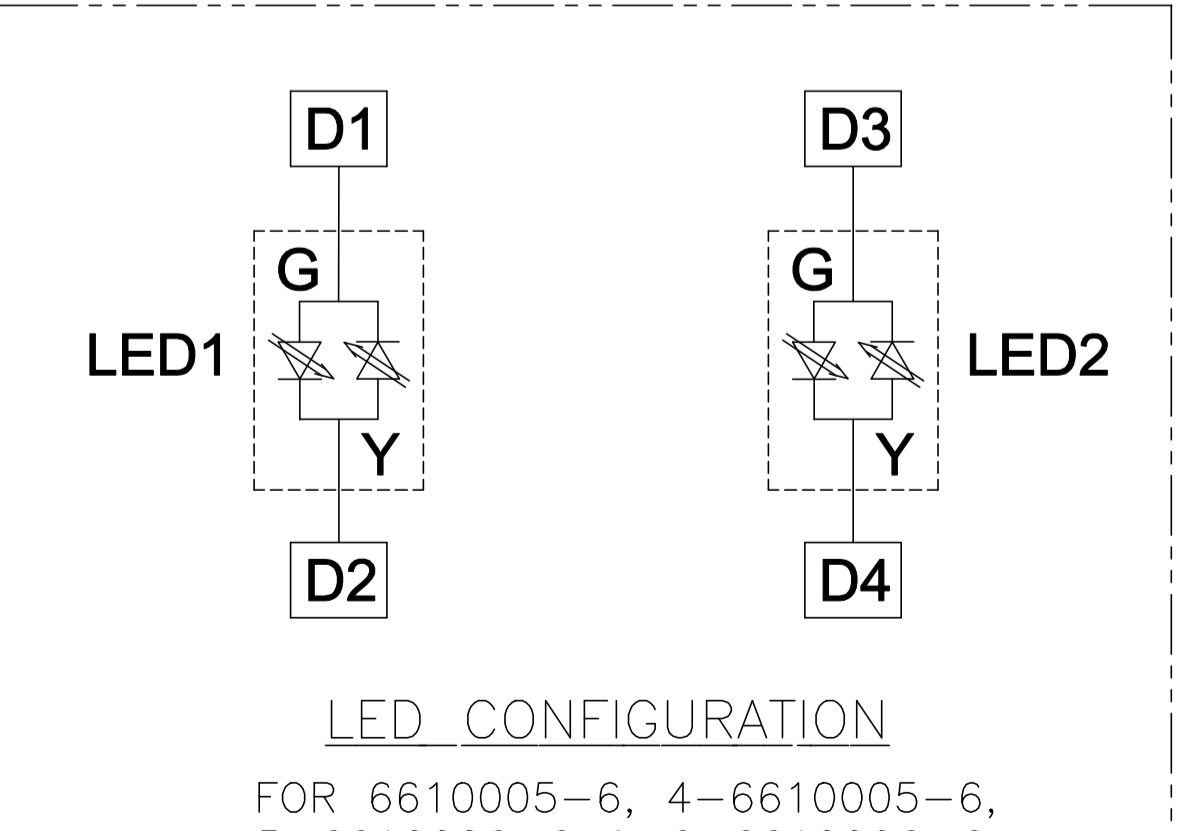


PIN DESIGNATIONS
(APPLIES TO ALL PORTS)



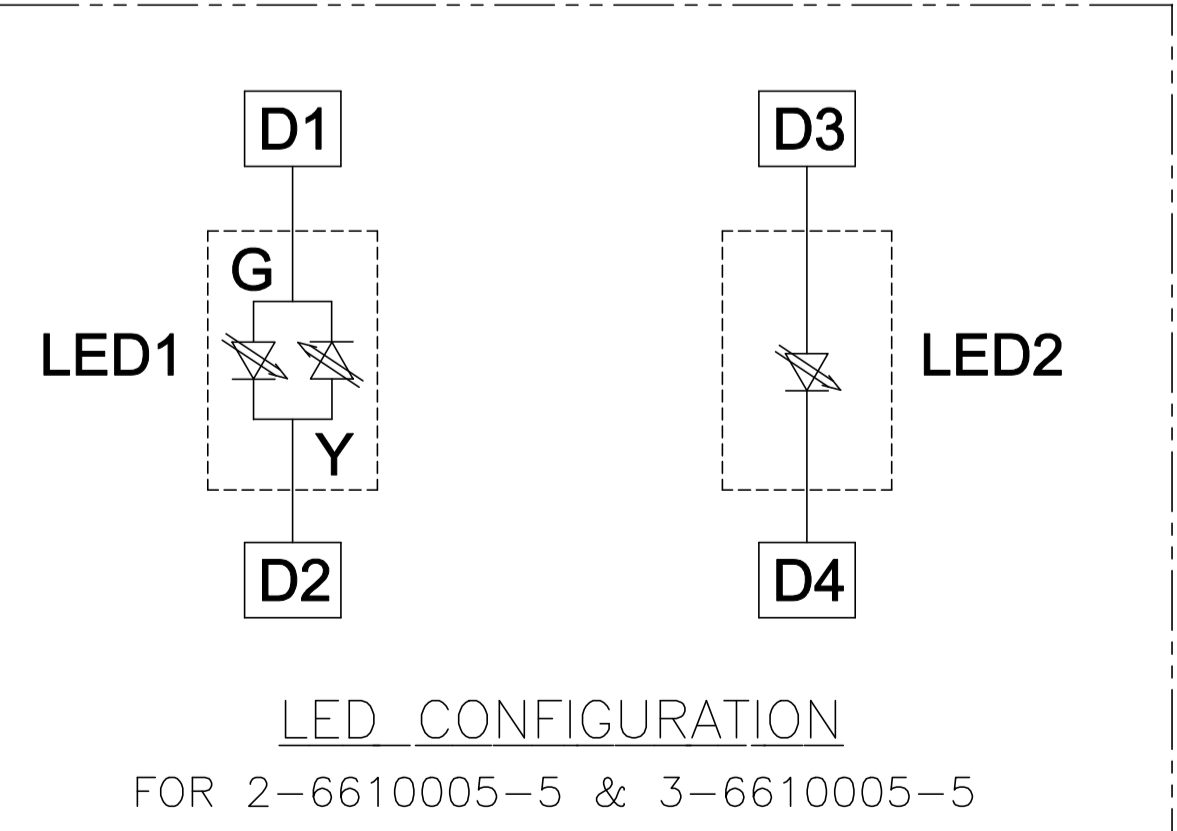
LED CONFIGURATION

FOR 6610005-4 & 2-6610005-4




LED CONFIGURATION

FOR 6610005-6, 4-6610005-6,
5-6610006-6 & 6-6610006-6



LED CONFIGURATION

FOR 2-6610005-5 & 3-6610005-5

THIS DRAWING IS A CONTROLLED DOCUMENT.		DRW R.GRZYBOWSKI 13JAN05			DONGGUAN CHINA	
		CHK D.FAROLE 13JAN05				
		APVD D.FAROLE 13JAN05	MODEL NAME		DESC 1X2 7G05 GIGABIT TAB UP W/ LED	
		MAGJACK GANG		SIZE A1		RESTRICTED TO —
PRODUCT SPEC 108-2100		APPLICATION SPEC —		NON-POE		C=6610005
		CUSTOMER DRAWING		SCALE 4:1		SHEET 2 OF 2
						REV S

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Bel:

[6610005-6](#) [1-6610005-5](#) [1-6610005-1](#) [6610005-2](#)