



#### **COMPLEMENTARY 100V ENHANCEMENT MODE MOSFET H-BRIDGE**

#### **Product Summary**

Device	BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
	400)/	$0.7\Omega$ @ $V_{GS} = 10V$	1.4A
N-Channel	100V	0.9Ω @ V <sub>GS</sub> = 6V	1.1A
	400)/	1.0Ω @ V <sub>GS</sub> = -10V	-1.3A
P-Channel	-100V	1.45Ω @ V <sub>GS</sub> = -6V	-0.9A

#### **Description**

This new generation complementary MOSFET H-Bridge features low on-resistance achievable with low gate drive.

#### **Applications**

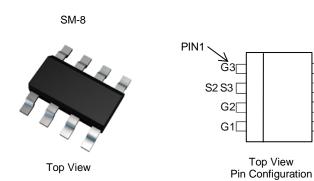
- DC Motor Control
- DC-AC Inverters

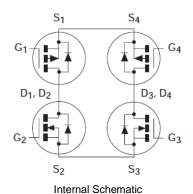
#### **Features**

- 2 x N + 2 x P Channels in a SOIC Package
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

- Case: SM-8 (8 LEAD SOT223)
- Case Material: Molded Plastic, "Green" Molding Compound;
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
  Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.117 grams (Approximate)





#### Ordering Information (Note 4)

Part Number	Part Number Reel Size		Quantity per Reel	
ZXMHC10A07T8TA	7"	12mm	1,000 units	

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

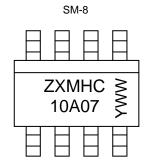
∏G4

□D3 D4

ີ S1 S4 ີ D1 D2

- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**



ZXMHC10A07 = Product Type Marking Code YWW = Date Code Marking Y or  $\overline{Y}$  = Last Digit of Year (ex: 5= 2015) WW or  $\overline{WW}$  = Week Code (01~53)



# **Maximum Ratings** $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic	Symbol	N-channel	P-channel	Units	
Drain-Source Voltage		V <sub>DSS</sub>	100	-100	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	±20	V	
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 8)	I <sub>D</sub>	1.1 0.9 1.0	-0.9 -0.8 -0.8	Α	
Maximum Body Diode Forward Current (Note 6)	Is	2.3	-2.2	Α	
Pulsed Drain Current (Note 7)	I <sub>DM</sub>	5.2	-4.5	А	
Pulsed Source Current (Note 7)	I <sub>SM</sub>	5.2	-4.5	Α	

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 8) Linear Derating Factor	T <sub>A</sub> = +25°C (Note 5)	P <sub>D</sub>	1.3 10.4	W mW/°C
otal Power Dissipation (Note 8) inear Derating Factor T <sub>A</sub> = +25°C (Note 6)		P <sub>D</sub>	1.3 10.4	W mW/°C
Thermal Resistance, Junction to Ambient (Note 8)	Steady State (Note 5)	В	94.5	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State (Note 6)	R <sub>θJA</sub>	73.3	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

Notes: 5. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions, with the heat sink split into two equal areas one for each drain connection.

 <sup>6.</sup> For a device surface mounted on FR4 PCB measured at t ≤10 seconds.
 7. Repetitive rating 50mm x 50mm x 1.6mm FR4 PCB, D = 0.02, pulse width 300µs - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

<sup>8.</sup> For device with one active die.



# **Electrical Characteristics N-CHANNEL** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 10)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1.0	μΑ	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 10)		•	•	•			
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	_	4.0	٧	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain Source On Registance (Note 0)	D-s/s/iii	_	_	0.7	Ω	$V_{GS} = 10V, I_D = 1.5A$	
Static Drain-Source On-Resistance (Note 9)	R <sub>DS(ON)</sub>	_	_	0.9		$V_{GS} = 6.0V, I_D = 1.0A$	
Forward Transfer Admittance (Notes 9 & 11)	<b>g</b> fs	_	1.6	_	S	$V_{DS} = 15V, I_{D} = 1.0A$	
Diode Forward Voltage (Note 9)	V <sub>SD</sub>	_	_	0.95	V	$V_{GS} = 0V, I_{S} = 1.5A$	
DYNAMIC CHARACTERISTICS (Note 11)							
Input Capacitance	C <sub>iss</sub>	_	138	_			
Output Capacitance	Coss	_	12	_	pF	$V_{DS} = 60V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	6	_		I = IIVII IZ	
Total Gate Charge	Qg	_	2.9	_			
Gate-Source Charge	Qgs	_	0.7	_	nC	$V_{DS} = 50V, I_{D} = 1.0A, V_{GS} = 1.0V$	
Gate-Drain Charge	Q <sub>gd</sub>	_	1.0	_		100	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	1.8	_			
Turn-On Rise Time	t <sub>R</sub>	_	1.5	_	20	$V_{DD} = 50V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	4.1	_	ns	$I_D = 1.0A, R_G = 6.0\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	2.1	_			
Reverse Recovery Time	t <sub>RR</sub>	_	27	_	ns	4 2 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	
Reverse Recovery Charge	Q <sub>rr</sub>	_	12	_	nC	$I_S = 1.8A$ , di/dt = 100A/ $\mu$ s	

## Electrical Characteristics P-CHANNEL (@TA = +25°C, unless otherwise specified.)

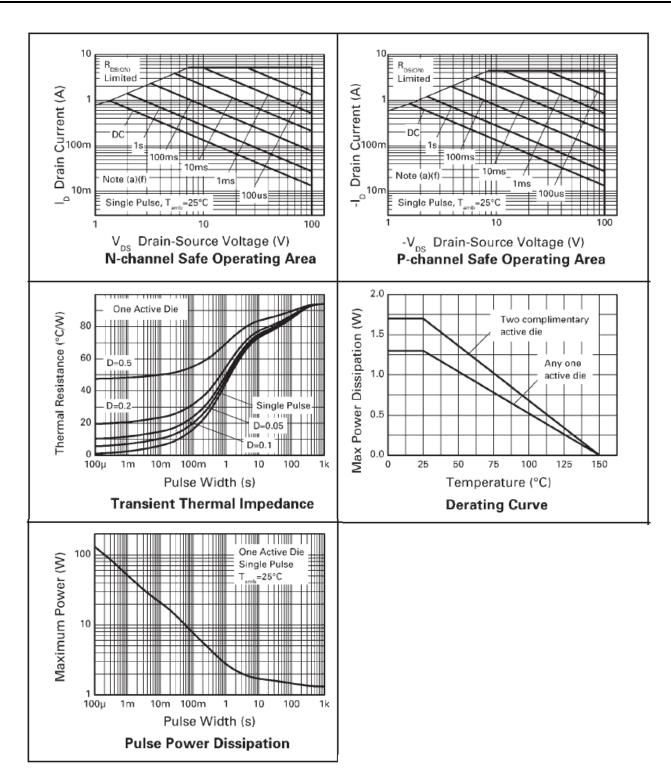
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 10)		,		,		-	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-100	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1.0	μΑ	$V_{DS} = -100V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 10)							
Gate Threshold Voltage	$V_{GS(TH)}$	-2.0	_	-4.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance (Note 9)	D	_	_	1.0	Ω	$V_{GS} = -10V, I_D = -0.6A$	
Static Diani-Source Off-Resistance (Note 9)	R <sub>DS(ON)</sub>	_	_	1.45	22	$V_{GS} = -6.0V, I_D = -0.5A$	
Forward Transfer Admittance (Notes 9 & 11)	9 <sub>fs</sub>	_	1.2	_	S	$V_{DS} = -15V, I_{D} = -0.6A$	
Diode Forward Voltage (Note 9)	$V_{SD}$	_	-0.85	-0.95	V	$V_{GS} = 0V, I_{S} = -0.75A$	
DYNAMIC CHARACTERISTICS (Note 11)							
Input Capacitance	Ciss	_	141	_	pF	., 50,4,4, 0,4	
Output Capacitance	Coss	_	13.1	_	pF	$V_{DS} = -50V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	10.8	_	pF		
Gate Charge (V <sub>GS</sub> = -5.0V)	Qg	_	1.6	_	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg		3.5	_	nC	V 50V I 0.6A	
Gate-Source Charge	$Q_{gs}$	_	0.6	_	nC	$V_{DS} = -50V, I_{D} = -0.6A$	
Gate-Drain Charge	Q <sub>qd</sub>	_	1.6	_	nC	1	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	1.6	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	2.1	_	ns	$V_{DD} = -50V, V_{GS} = -10V,$ $R_G = 6.0\Omega, I_D = -1.0A$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	5.9	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	3.3	_	ns	1	
Reverse Recovery Time	t <sub>RR</sub>	_	29	_	ns		
Reverse Recovery Charge	Q <sub>rr</sub>	_	31	_	nC	$I_S = -0.9A$ , di/dt = 100A/ $\mu$ s	

Notes:

- Measured under pulsed conditions. Width≤300µs. Duty cycle ≤ 2%.
  Short duration pulse test used to minimize self-heating effect.
  Guaranteed by design. Not subject to product testing.

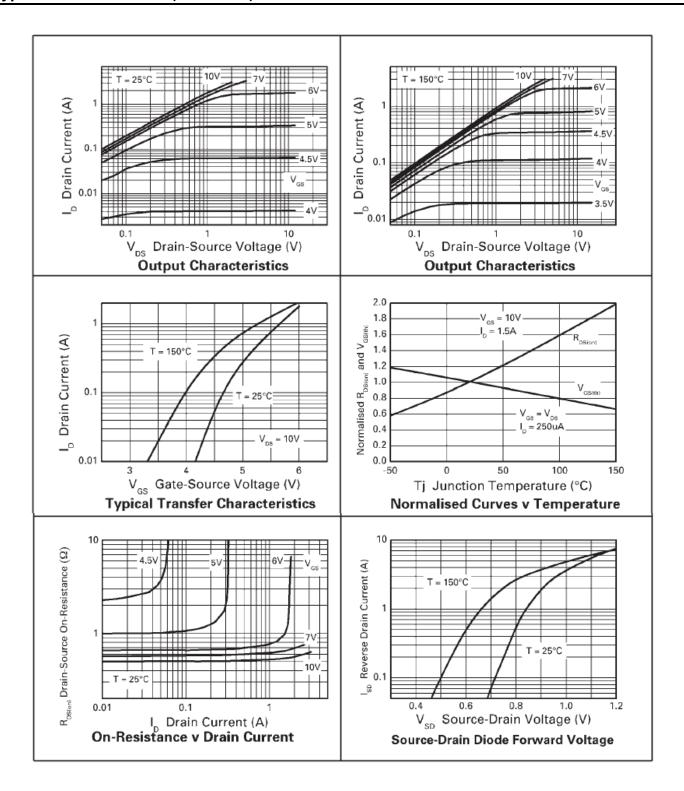


## **Typical Characteristics**



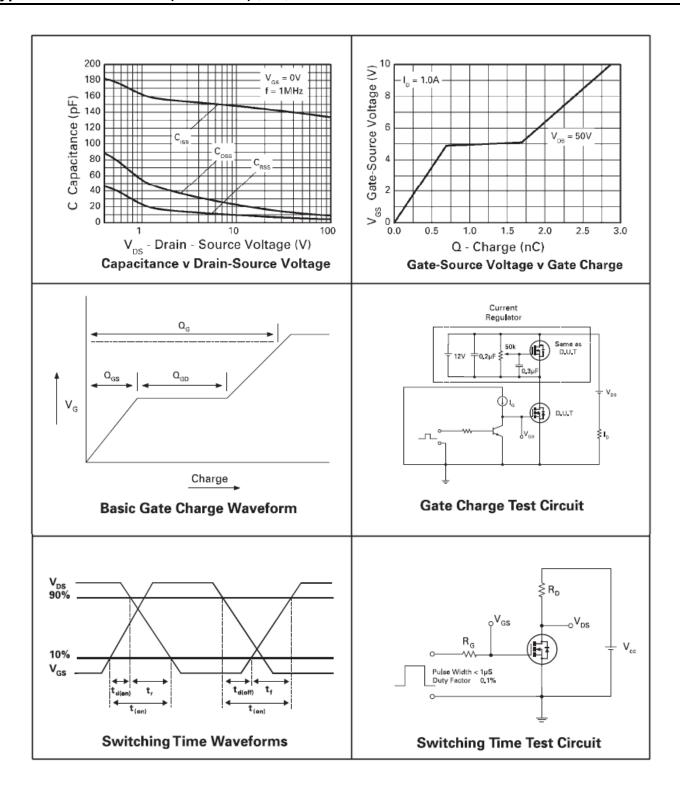


## **Typical Characteristics (N-Channel)**



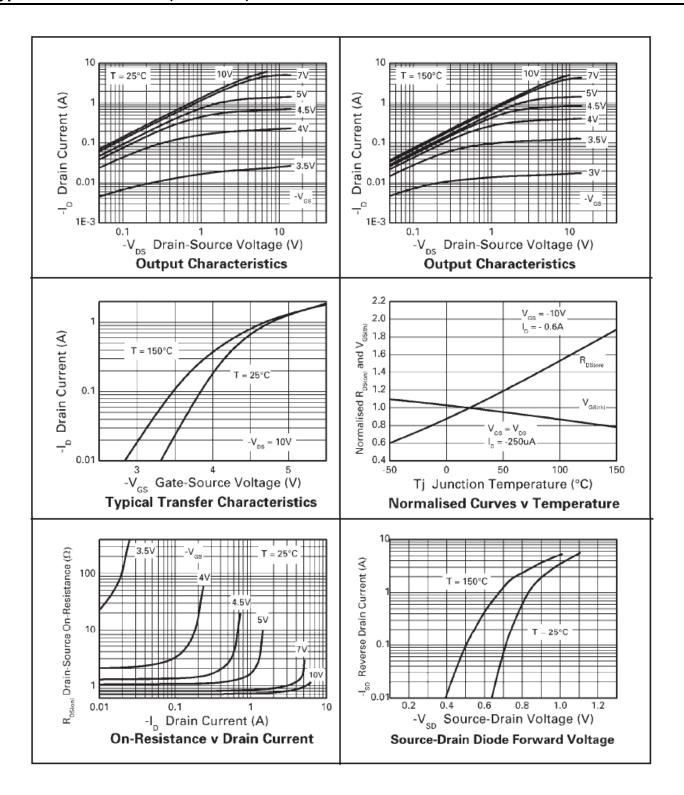


## Typical Characteristics (N-Channel) (Cont.)



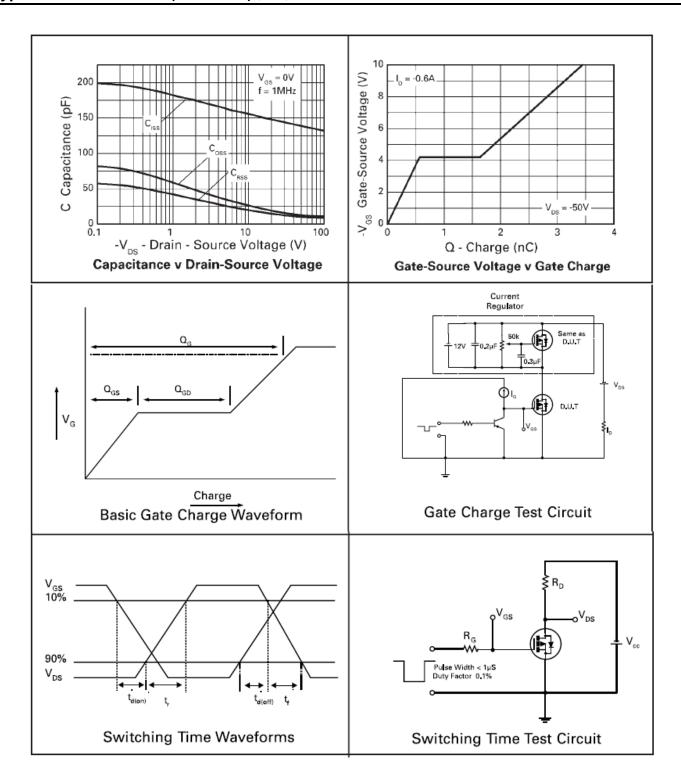


## **Typical Characteristics (P-Channel)**





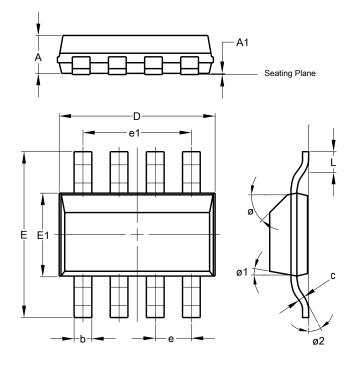
## Typical Characteristics (P-Channel) (Cont.)





## **Package Outline Dimensions**

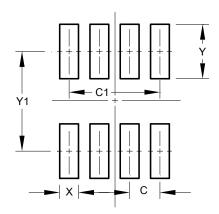
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SM-8						
Dim	Min Max Typ					
Α		1.70	1.60			
A1	0.02	0.10	0.04			
b	0.70	0.90	0.80			
С	0.24	0.32	0.28			
D	6.30	6.30 6.70 6.60				
е	1.53 REF					
e1	4.59 REF					
Е	6.70 7.30 7.00					
E1	3.30	3.70	3.50			
٦	0.75 1.00 0.90					
Ø	45°					
Ø1	15°					
Ø2			10°			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.52
C1	4.60
Х	0.95
Y	2.80
Y1	6.80



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