

## AAP Gen 7 (TO-240AA) Power Modules Thyristor/Thyristor, 95 A


**ADD-A-PAK**


### FEATURES

- High voltage
- Industrial standard package
- Low thermal resistance
- UL approved file E78996 
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- High surge capability
- Easy mounting on heatsink

### PRIMARY CHARACTERISTICS

$I_{T(AV)}$	95 A
Type	Modules - thyristor, standard
Package	AAP Gen 7 (TO-240AA)

### MECHANICAL DESCRIPTION

The AAP Gen 7 (TO-240AA), new generation of AAP module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### ELECTRICAL DESCRIPTION

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

### MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{T(AV)}$	85 °C	95	A
$I_{T(RMS)}$		150	
$I_{TSM}$	50 Hz	2000	
	60 Hz	2094	
$I^2t$	50 Hz	20	$kA^2s$
	60 Hz	18.26	
$I^{2\sqrt{t}}$		200	$kA^2\sqrt{s}$
$V_{RRM}$	Range	400 to 1600	V
$T_{Stg}$		-40 to +125	°C
		-40 to +125	°C

**ELECTRICAL SPECIFICATIONS**

<b>VOLTAGE RATINGS</b>					
<b>TYPE NUMBER</b>	<b>VOLTAGE CODE</b>	<b><math>V_{RRM}</math>, MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V</b>	<b><math>V_{RSM}</math>, MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V</b>	<b><math>V_{DRM}</math>, MAXIMUM REPETITIVE PEAK OFF-STATE VOLTAGE, GATE OPEN CIRCUIT V</b>	<b><math>I_{RRM}, I_{DRM}</math> AT 125 °C mA</b>
VS-VSK.91	04	400	500	400	15
	08	800	900	800	
	12	1200	1300	1200	
	16	1600	1700	1600	

<b>ON-STATE CONDUCTION</b>						
<b>PARAMETER</b>	<b>SYMBOL</b>	<b>TEST CONDITIONS</b>			<b>VALUES</b>	<b>UNITS</b>
Maximum average on-state current	$I_{T(AV)}$	180° conduction, half sine wave, $T_C = 85^\circ\text{C}$			95	A
Maximum continuous RMS on-state current	$I_{T(RMS)}$	DC			150	
Maximum peak, one-cycle non-repetitive on-state current	$I_{TSM}$	$t = 10 \text{ ms}$	No voltage reapplied	Sinusoidal half wave, initial $T_J = T_J$ maximum	2000	A
		$t = 8.3 \text{ ms}$			2094	
		$t = 10 \text{ ms}$			1682	
		$t = 8.3 \text{ ms}$			1760	
Maximum $I^2t$ for fusing	$I^2t$	$t = 10 \text{ ms}$	No voltage reapplied	Initial $T_J = T_J$ maximum	20	kA <sup>2</sup> s
		$t = 8.3 \text{ ms}$			18.26	
		$t = 10 \text{ ms}$			14.14	
		$t = 8.3 \text{ ms}$			12.91	
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$ (1)	$t = 0.1 \text{ ms}$ to $10 \text{ ms}$ , no voltage reapplied $T_J = T_J$ maximum			200	kA <sup>2</sup> s
Maximum value of threshold voltage	$V_{T(TO)}$ (2)	Low level (3)	$T_J = T_J$ maximum		0.97	V
		High level (4)			1.1	
Maximum value of on-state slope resistance	$r_t$ (2)	Low level (3)	$T_J = T_J$ maximum		2.76	mΩ
		High level (4)			2.38	
Maximum on-state voltage drop	$V_{TM}$	$I_{TM} = \pi \times I_{T(AV)}$	$T_J = 25^\circ\text{C}$		1.73	V
Maximum non-repetitive rate of rise of turned on current	$dl/dt$	$T_J = 25^\circ\text{C}$ , from $0.67 V_{DRM}$ , $I_{TM} = \pi \times I_{T(AV)}$ , $I_g = 500 \text{ mA}$ , $t_r < 0.5 \mu\text{s}$ , $t_p > 6 \mu\text{s}$			150	A/μs
Maximum holding current	$I_H$	$T_J = 25^\circ\text{C}$ , anode supply = 6 V, resistive load, gate open circuit			250	mA
Maximum latching current	$I_L$	$T_J = 25^\circ\text{C}$ , anode supply = 6 V, resistive load			400	

**Notes**

(1)  $I^2t$  for time  $t_x = I^2\sqrt{t} \times \sqrt{t_x}$

(2) Average power =  $V_{T(TO)} \times I_{T(AV)} + r_t \times (I_{T(RMS)})^2$

(3)  $16.7\% \times \pi \times I_{AV} < I < \pi \times I_{AV}$

(4)  $I > \pi \times I_{AV}$

<b>TRIGGERING</b>							
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS	
Maximum peak gate power	P <sub>GM</sub>				12	W	
Maximum average gate power	P <sub>G(AV)</sub>				3.0		
Maximum peak gate current	I <sub>GM</sub>				3.0	A	
Maximum peak negative gate voltage	- V <sub>GM</sub>				10	V	
Maximum gate voltage required to trigger	V <sub>GT</sub>	T <sub>J</sub> = - 40 °C	Anode supply = 6 V resistive load	4.0			
		T <sub>J</sub> = 25 °C		2.5			
		T <sub>J</sub> = 125 °C		1.7			
Maximum gate current required to trigger	I <sub>GT</sub>	T <sub>J</sub> = - 40 °C	Anode supply = 6 V resistive load	270		mA	
		T <sub>J</sub> = 25 °C		150			
		T <sub>J</sub> = 125 °C		80			
Maximum gate voltage that will not trigger	V <sub>GD</sub>	T <sub>J</sub> = 125 °C, rated V <sub>DRM</sub> applied			0.25	V	
Maximum gate current that will not trigger	I <sub>GD</sub>	T <sub>J</sub> = 125 °C, rated V <sub>DRM</sub> applied			6	mA	

<b>BLOCKING</b>						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum peak reverse and off-state leakage current at V <sub>RRM</sub> , V <sub>DRM</sub>	I <sub>RRM</sub> , I <sub>DRM</sub>	T <sub>J</sub> = 125 °C, gate open circuit			15	mA
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz			3000 (1 min) 3600 (1 s)	V
Maximum critical rate of rise of off-state voltage	dV/dt	T <sub>J</sub> = 125 °C, linear to 0.67 V <sub>DRM</sub>			1000	V/μs

<b>THERMAL AND MECHANICAL SPECIFICATIONS</b>						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Junction operating and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>				-40 to +125	°C
Maximum internal thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation			0.22	°C/W
Typical thermal resistance, case to heatsink per module	R <sub>thCS</sub>	Mounting surface flat, smooth and greased			0.1	
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound.			Nm
	busbar					
Approximate weight					75	g
					2.7	oz.
Case style		JEDEC®			AAP Gen 7 (TO-240AA)	

<b>ΔR CONDUCTION PER JUNCTION</b>										
DEVICES	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION				UNITS
	180°	120°	90°	60°	30°	180°	120°	90°	60°	
VSK.91..	0.04	0.048	0.063	0.085	0.125	0.033	0.052	0.067	0.088	0.127 °C/W

**Note**

- Table shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

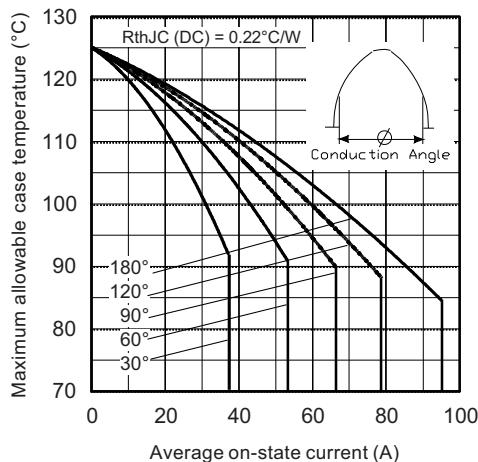


Fig. 1 - Current Ratings Characteristics

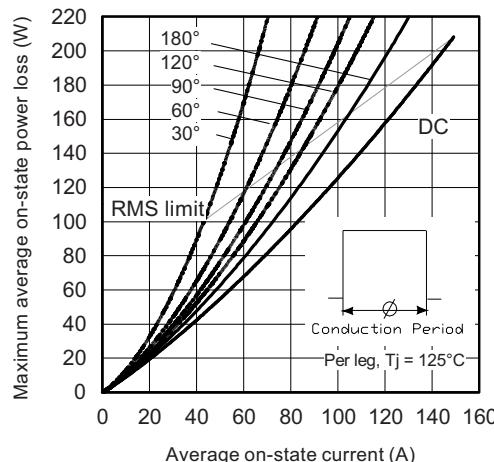


Fig. 4 - On-State Power Loss Characteristics

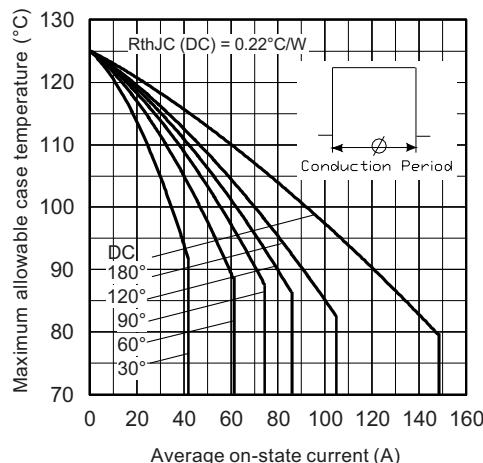


Fig. 2 - Current Ratings Characteristics

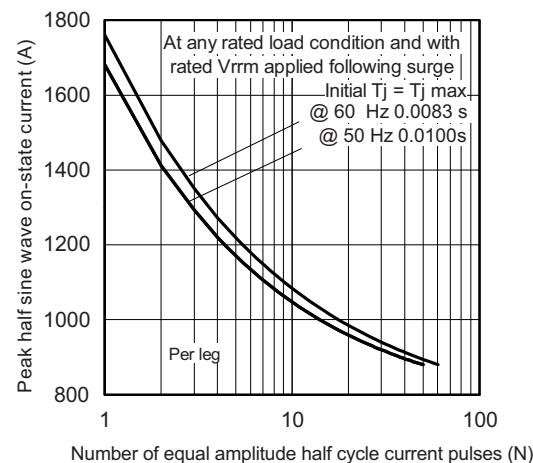


Fig. 5 - Maximum Non-Repetitive Surge Current

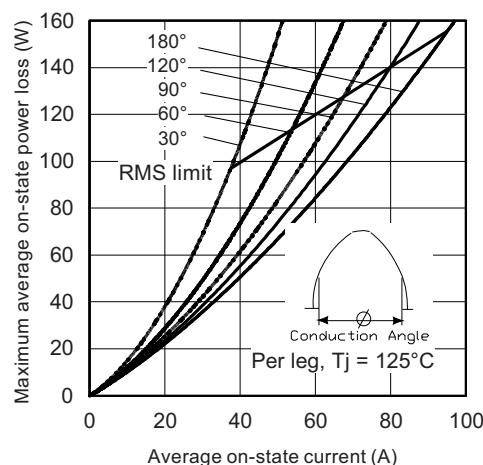


Fig. 3 - On-State Power Loss Characteristics

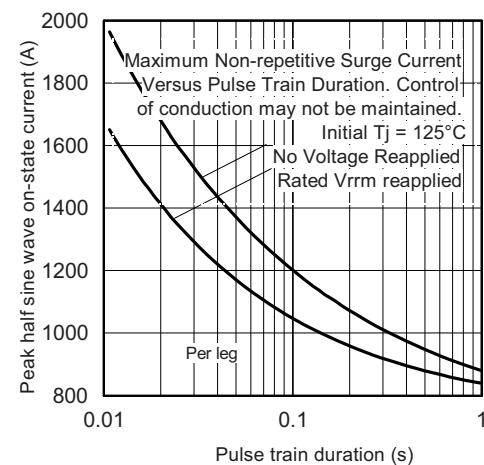


Fig. 6 - Maximum Non-Repetitive Surge Current

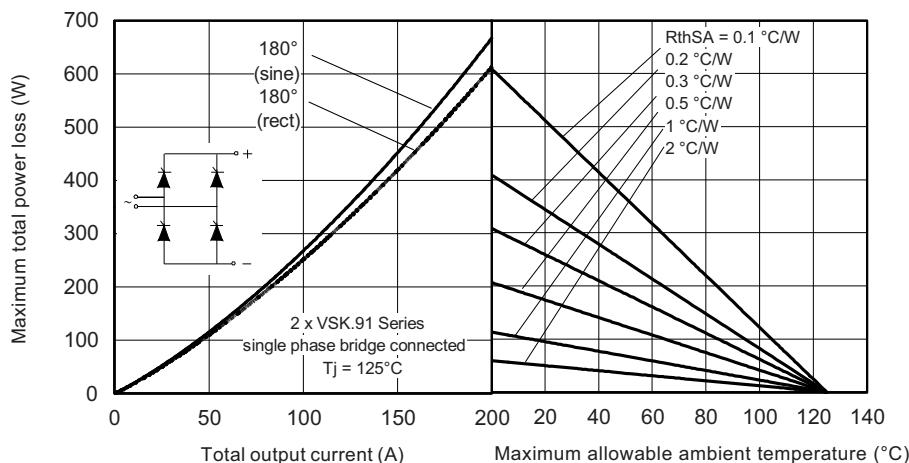


Fig. 7 - On-State Power Loss Characteristics

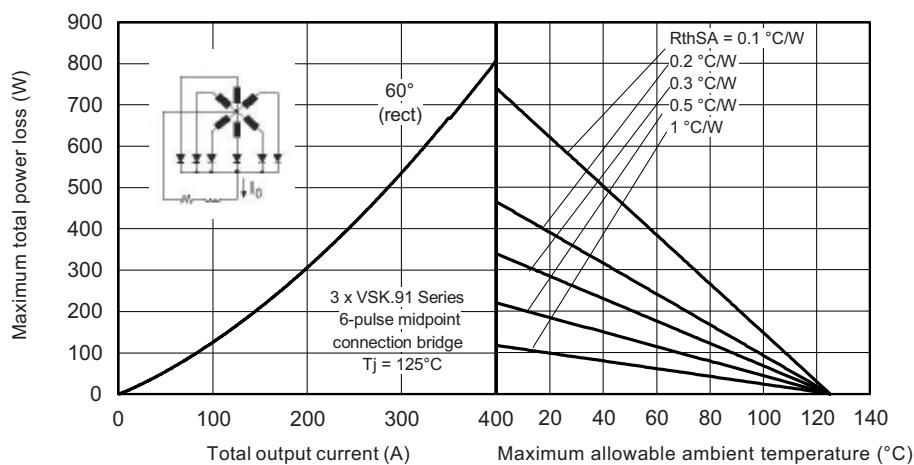


Fig. 8 - On-State Power Loss Characteristics

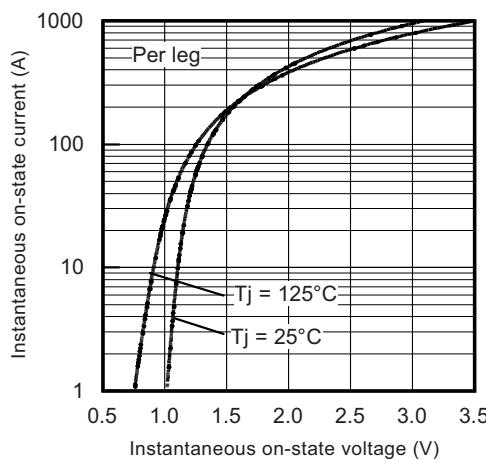


Fig. 9 - On-State Voltage Characteristics

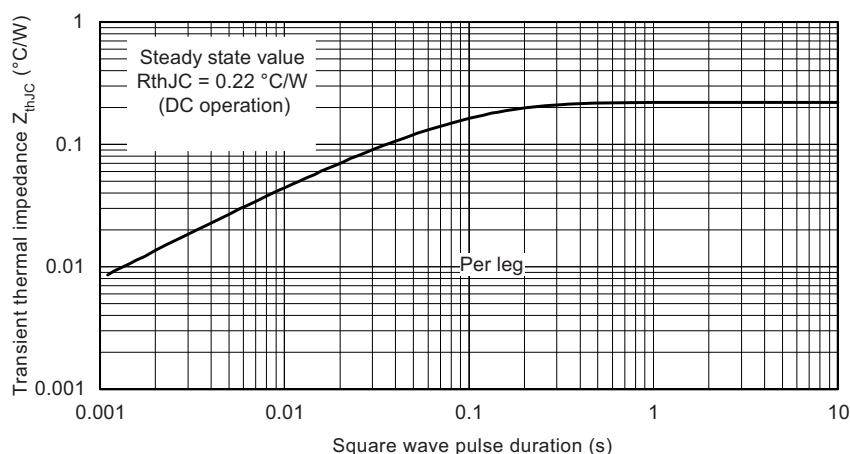


Fig. 10 - Thermal Impedance  $Z_{thJC}$  Characteristics

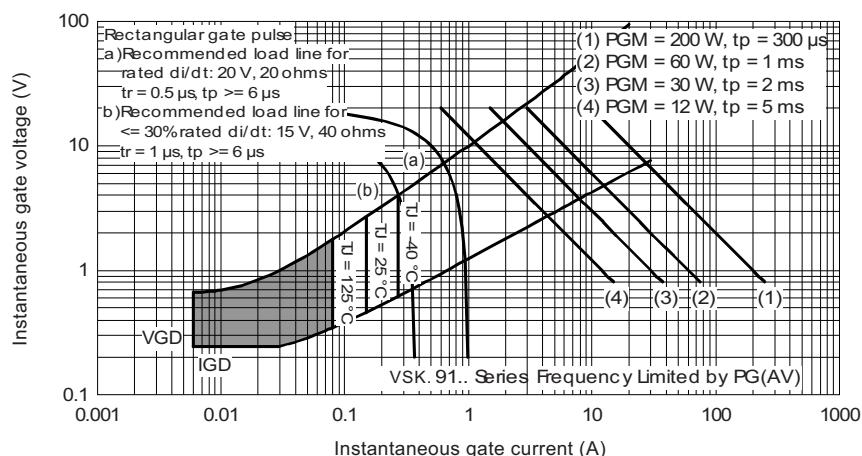


Fig. 11 - Gate Characteristics

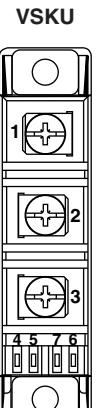
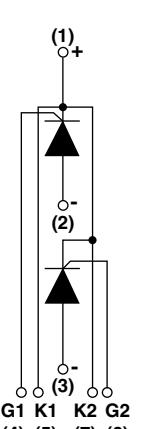
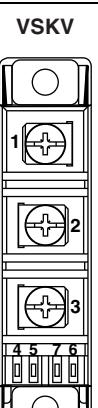
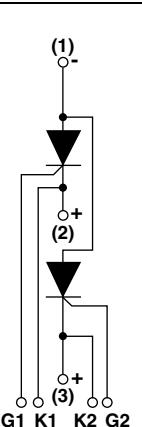
## ORDERING INFORMATION TABLE

Device code	<b>VS-VS</b>	<b>K</b>	<b>U</b>	<b>91</b>	<b>I</b>	<b>16</b>
	(1)	(2)	(3)	(4)	(5)	

- 1** - Vishay Semiconductors product
- 2** - Module type
- 3** - Circuit configuration (see Circuit Configuration table)
- 4** - Current code (95 A)
- 5** - Voltage code (see Voltage Ratings table)

### Note

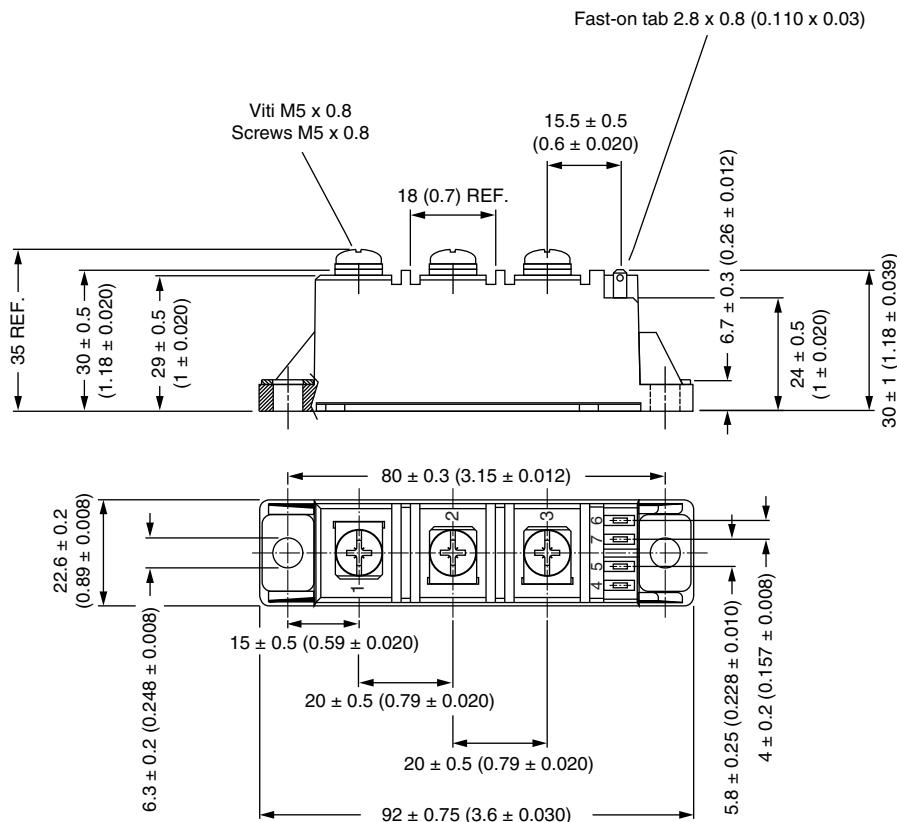
- To order the optional hardware go to [www.vishay.com/doc?95172](http://www.vishay.com/doc?95172)

CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two SCRs common cathodes	U	   <p>VSKU</p> <p>Pinout: 1, 2, 3, 4, 5, 6, 7</p> <p>Circuit: (1) +, (2) -, (3) -, G1, K1, K2, G2</p>
Two SCRs common anodes	V	   <p>VSKV</p> <p>Pinout: 1, 2, 3, 4, 5, 6, 7</p> <p>Circuit: (1) -, (2) +, (3) +, G1, K1, K2, G2</p>

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95368">www.vishay.com/doc?95368</a>

### ADD-A-PAK Generation VII - Thyristor

#### DIMENSIONS in millimeters (inches)



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