

P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY

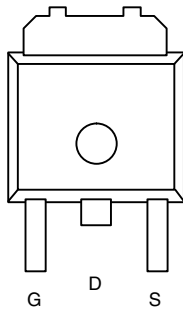
| V_{DS} (V) | $R_{DS(on)}$ (Ω) | I_D (A) | Q_g (Typ) |
|--------------|-----------------------------|-----------|-------------|
| - 60 | 0.155 at $V_{GS} = - 10$ V | - 8.4 | 12.5 |
| | 0.280 at $V_{GS} = - 4.5$ V | - 7.4 | |

FEATURES

- TrenchFET[®] Power MOSFETS
- Material categorization:
For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

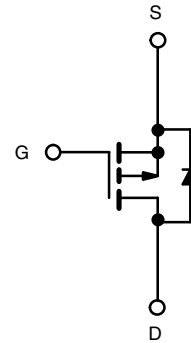
TO-252


Top View

Drain Connected to Tab

Ordering Information:

SUD08P06-155L-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--------------------------------------------------|----------------|----------------|-------------------|
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Continuous Drain Current ($T_J = 150$ °C) | I_D | $T_C = 25$ °C | - 8.2 |
| | | $T_C = 100$ °C | - 5.2 |
| Pulsed Drain Current | I_{DM} | - 18 | A |
| Continuing Source Current (Diode Conduction) | I_S | - 8.4 | |
| Avalanche Current | I_{AS} | - 12 | |
| Single Pulse Avalanche Energy | E_{AS} | 7.2 | mJ |
| Maximum Power Dissipation | P_D | $T_C = 25$ °C | 20.8 ^a |
| | | $T_A = 25$ °C | 1.7 ^b |
| Operating Junction and Storage Temperature Range | T_J, T_{stg} | - 55 to 150 | °C |

THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Typical | Maximum | Unit |
|----------------------------------|------------|---------------|---------|------|
| Junction-to-Ambient ^b | R_{thJA} | $t \leq 10$ s | 20 | 25 |
| | | Steady State | 62 | 75 |
| Junction-to-Case | R_{thJC} | 5 | 6 | °C/W |

Notes:

- See SOA curve for voltage derating.
- Surface mounted on 1" x 1" FR-4 board.

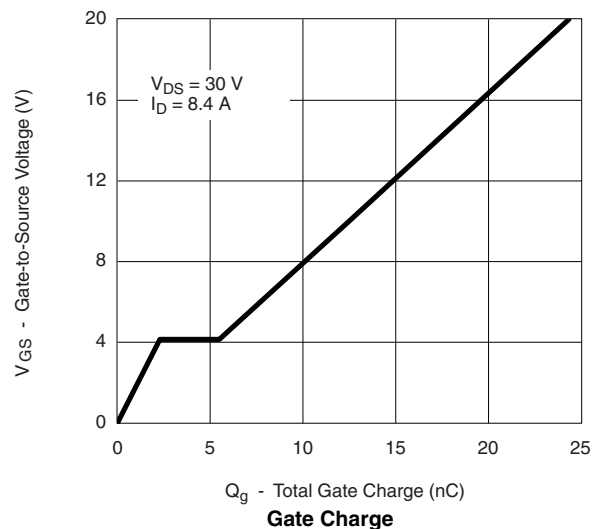
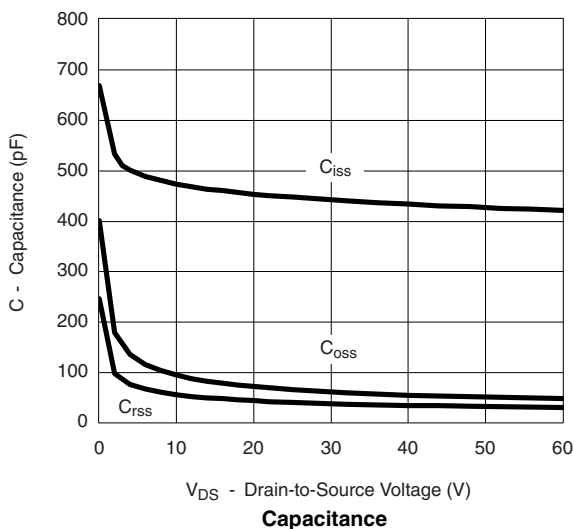
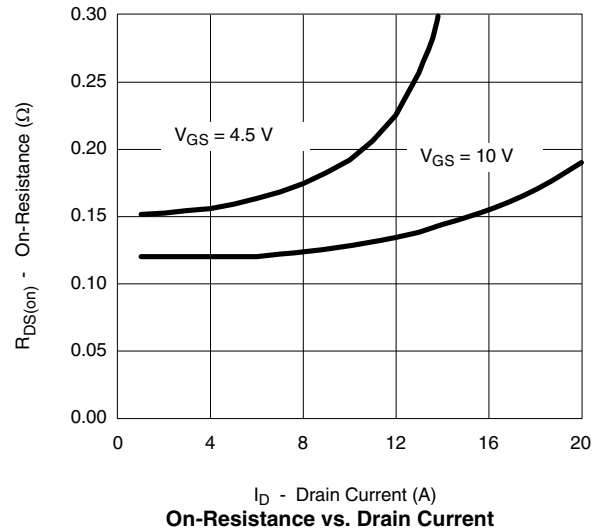
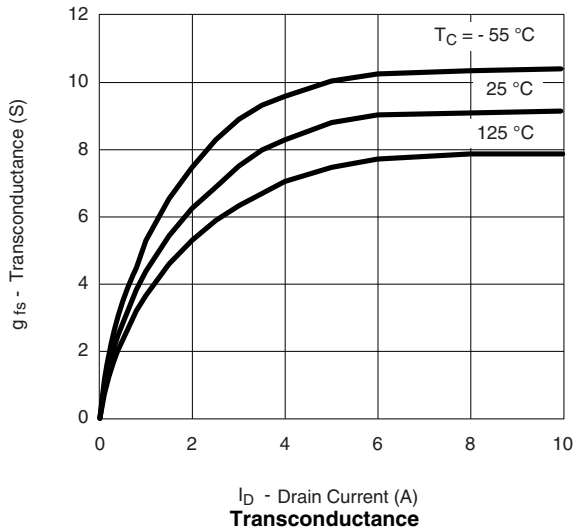
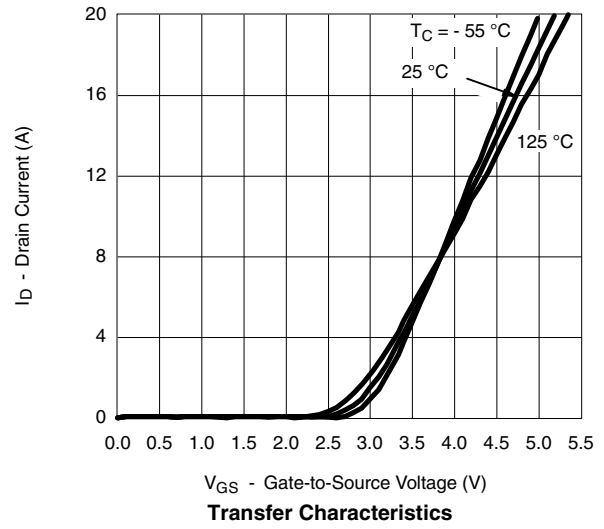
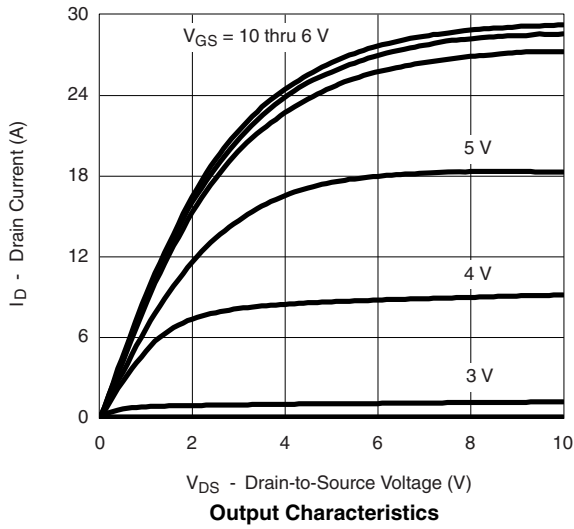
| SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted) | | | | | | |
|---------------------------------------------------------------------------------------------------------|--------------|---------------------------------------------------------------------------------------------------------------------------------|------|-------------------|-----------|---------------|
| Parameter | Symbol | Test Conditions | Min. | Typ. ^a | Max. | Unit |
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | $V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$ | - 60 | | | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$ | - 1 | - 2 | - 3 | |
| Gate-Body Leakage | I_{GSS} | $V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$ | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$ | | | - 1 | μA |
| | | $V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$ | | | - 50 | |
| | | $V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 150\text{ }^\circ\text{C}$ | | | - 150 | |
| On-State Drain Current ^b | $I_{D(on)}$ | $V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$ | - 10 | | | A |
| Drain-Source On-State Resistance ^b | $R_{DS(on)}$ | $V_{GS} = -10\text{ V}, I_D = -5\text{ A}$ | | 0.125 | 0.155 | Ω |
| | | $V_{GS} = -10\text{ V}, I_D = -5\text{ A}, T_J = 125\text{ }^\circ\text{C}$ | | | 0.280 | |
| | | $V_{GS} = -10\text{ V}, I_D = -5\text{ A}, T_J = 150\text{ }^\circ\text{C}$ | | | 0.350 | |
| | | $V_{GS} = -4.5\text{ V}, I_D = -2\text{ A}$ | | 0.158 | 0.280 | |
| Forward Transconductance ^b | g_{fs} | $V_{DS} = -15\text{ V}, I_D = -5\text{ A}$ | | 8 | | S |
| Dynamic | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS} = -25\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$ | | 450 | | pF |
| Output Capacitance | C_{oss} | | | 65 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 40 | | |
| Total Gate Charge | Q_g | $V_{DS} = -30\text{ V}, V_{GS} = -10\text{ V}, I_D = -8.4\text{ A}$ | | 12.5 | 19 | nC |
| Gate-Source Charge | Q_{gs} | | | 2.3 | | |
| Gate-Drain Charge | Q_{gd} | | | 3.2 | | |
| Gate Resistance | R_g | $f = 1\text{ MHz}$ | | 8 | | Ω |
| Turn-On Delay Time ^c | $t_{d(on)}$ | $V_{DD} = -30\text{ V}, R_L = 3.57\text{ }\Omega$ $I_D \cong -8.4\text{ A}, V_{GEN} = -10\text{ V}, R_G = 2.5\text{ }\Omega$ | | 5 | 10 | ns |
| Rise Time ^c | t_r | | | 14 | 25 | |
| Turn-Off Delay Time ^c | $t_{d(off)}$ | | | 15 | 25 | |
| Fall Time ^c | t_f | | | 7 | 12 | |
| Source-Drain Diode Ratings and Characteristics ($T_C = 25\text{ }^\circ\text{C}$) ^b | | | | | | |
| Pulsed Current | I_{SM} | | | | - 20 | A |
| Forward Voltage ^b | V_{SD} | $I_F = -2\text{ A}, V_{GS} = 0\text{ V}$ | | - 0.9 | - 1.3 | V |
| Reverse Recovery Time | t_{rr} | $I_F = -8\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$ | | 50 | 80 | ns |
| Reverse Recovery Time | Q_{rr} | | | 80 | 120 | nC |

Notes:

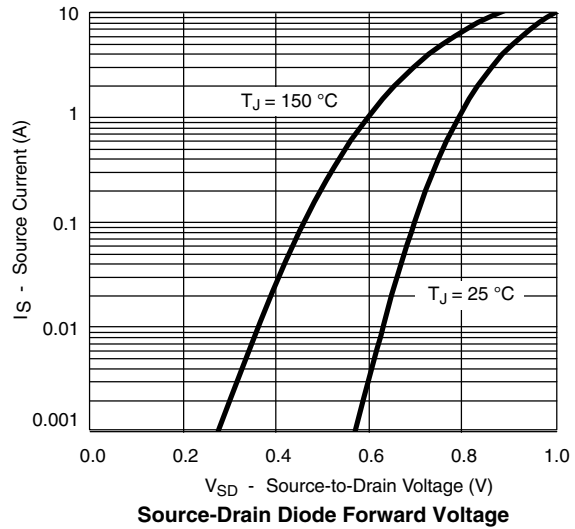
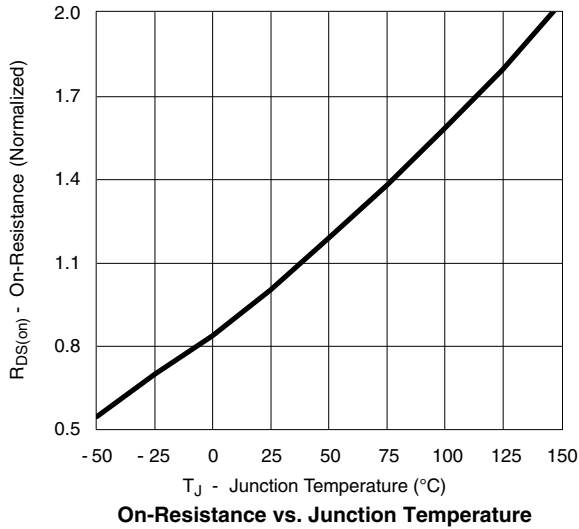
- Guaranteed by design, not subject to production testing.
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

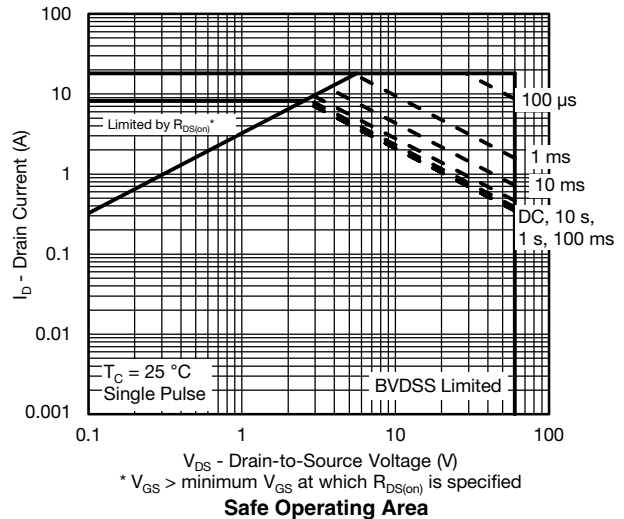
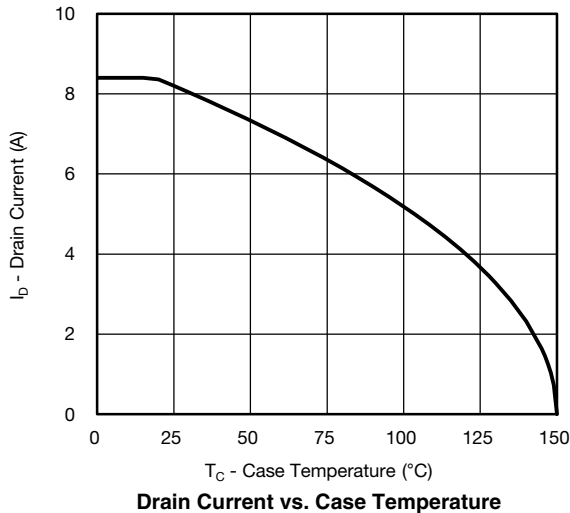
TYPICAL CHARACTERISTICS (25 °C unless noted)



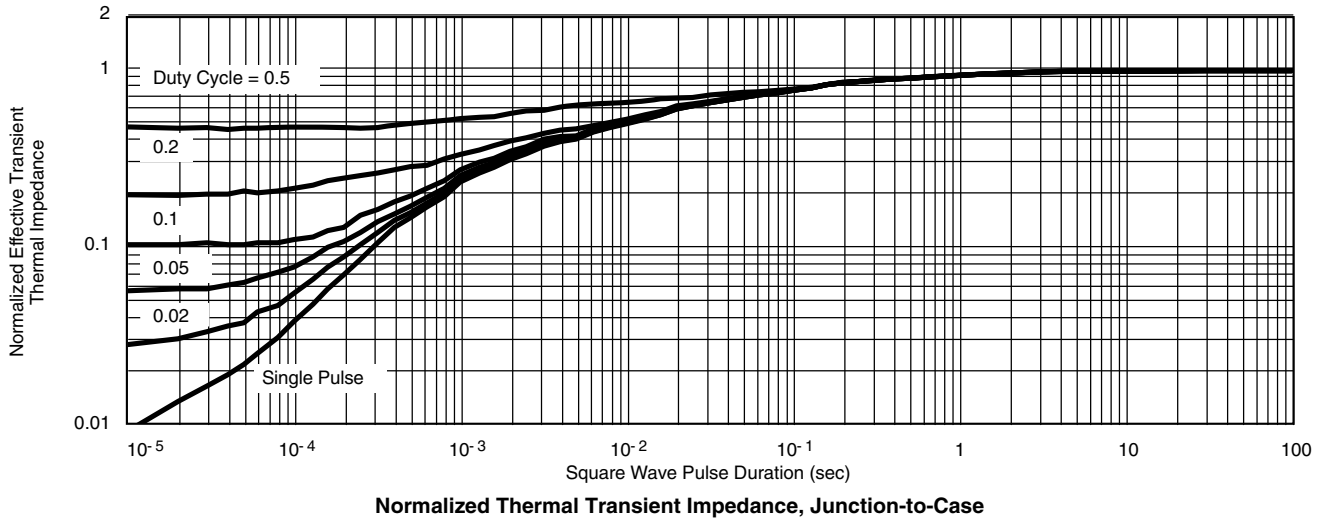
TYPICAL CHARACTERISTICS (25 °C unless noted)



THERMAL RATINGS



THERMAL RATINGS



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62843.



TO-252AA Case Outline

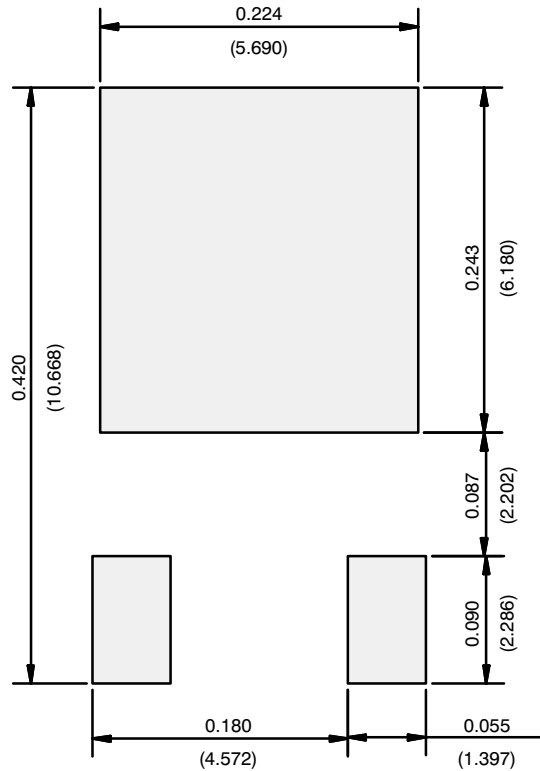


| DIM. | MILLIMETERS | | INCHES | |
|----------------------------------------------|-------------|-------|-----------|-------|
| | MIN. | MAX. | MIN. | MAX. |
| A | 2.18 | 2.38 | 0.086 | 0.094 |
| A1 | - | 0.127 | - | 0.005 |
| b | 0.64 | 0.88 | 0.025 | 0.035 |
| b2 | 0.76 | 1.14 | 0.030 | 0.045 |
| b3 | 4.95 | 5.46 | 0.195 | 0.215 |
| C | 0.46 | 0.61 | 0.018 | 0.024 |
| C2 | 0.46 | 0.89 | 0.018 | 0.035 |
| D | 5.97 | 6.22 | 0.235 | 0.245 |
| D1 | 4.10 | - | 0.161 | - |
| E | 6.35 | 6.73 | 0.250 | 0.265 |
| E1 | 4.32 | - | 0.170 | - |
| H | 9.40 | 10.41 | 0.370 | 0.410 |
| e | 2.28 BSC | | 0.090 BSC | |
| e1 | 4.56 BSC | | 0.180 BSC | |
| L | 1.40 | 1.78 | 0.055 | 0.070 |
| L3 | 0.89 | 1.27 | 0.035 | 0.050 |
| L4 | - | 1.02 | - | 0.040 |
| L5 | 1.01 | 1.52 | 0.040 | 0.060 |
| ECN: T16-0236-Rev. P, 16-May-16 DWG: 5347 | | | | |

Notes

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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