



Maxi Family 72V Input



DC-DC Converter Module

Features & Benefits

- DC input range: 43 – 110V (continuous)
- Isolated output
- Encapsulated circuitry for shock and vibration resistance
- Extended temperature range (-55 to +100°C)
- Input surge withstand: 150V for 100ms
- DC output: 3.3 – 48V
- Programmable output: 10 – 110%
- Regulation: $\pm 0.2\%$ no load to full load
- Efficiency: Up to 88%
- Maximum operating temp: 100°C, full load
- Power density: up to 80W per cubic inch
- Height above board: 0.43in [10.9mm]
- Parallelable, with N+M fault tolerance
- Low-noise ZCS/ZVS architecture
- RoHS Compliant (with F or G pin option)

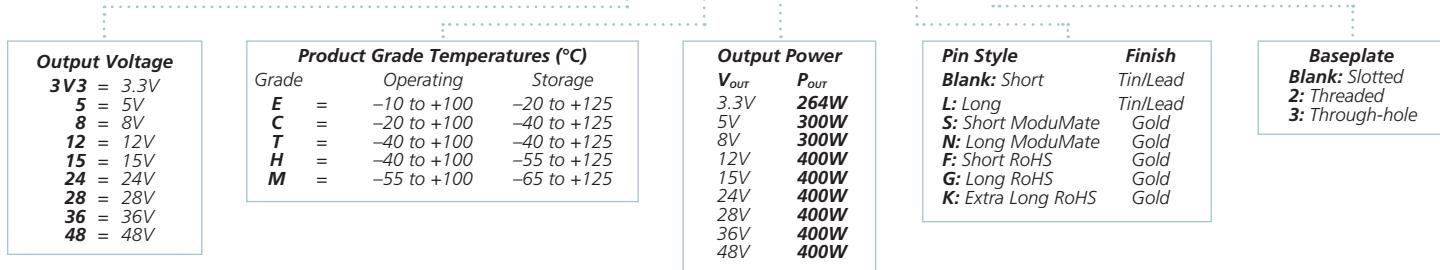
Product Overview

These DC-DC converter modules use advanced power processing, control and packaging technologies to provide the performance, flexibility, reliability and cost effectiveness of a mature power component.

High-frequency ZCS/ZVS switching provides high power density with low noise and high efficiency.

Part Numbering

e.g. V72A24C400BL



Note: Product images may not highlight current product markings.

Module Family Electrical Characteristics

Electrical characteristics apply over the full operating range of input voltage, output load (resistive) and baseplate temperature, unless otherwise specified. All temperatures refer to the operating temperature at the center of the baseplate.

Module Input Specifications

| Parameter | Min | Typ | Max | Unit | Notes |
|-------------------------|------|-------|------|-----------------|---------------------------|
| Operating input voltage | 43 | 72 | 110 | V _{DC} | Per EN50155 and GBT-25119 |
| Input surge withstand | | | 150 | V _{DC} | <100ms |
| Undervoltage turn-on | | 41.7 | 42.6 | V _{DC} | |
| Undervoltage turn-off | 35.2 | 36.5 | | V _{DC} | |
| Oversupply turn-off/on | 111 | 115.5 | 121 | V _{DC} | |
| Disabled input current | | | 1.5 | mA | PC pin low |

Module Output Specifications

| Parameter | Min | Typ | Max | Unit | Notes |
|--|-----|--------|--------------|-----------------|---|
| Output voltage set point | | ±1 | | % | Of nominal output voltage. Nominal input; full load; 25°C |
| Line regulation | | ±0.02 | ±0.20 | % | Low line to high line; full load |
| Temperature regulation | | ±0.002 | ±0.005 | % / °C | Over operating temperature range |
| Power sharing accuracy | | ±2 | ±5 | % | 10 – 100% of full load |
| Programming range | 10 | | 110 | % | Of nominal output voltage. For trimming below 90% of nominal, a minimum load of 10% of maximum rated power may be required. |
| +OUT to -OUT, +Sense to -OUT — Absolute Maximum Ratings | | | | | |
| 3.3V | | | -0.5 to 4.7 | V _{DC} | Externally applied |
| 5V | | | -0.5 to 7.0 | V _{DC} | Externally applied |
| 8V | | | -0.5 to 10.9 | V _{DC} | Externally applied |
| 12V | | | -0.5 to 16.1 | V _{DC} | Externally applied |
| 15V | | | -0.5 to 20.0 | V _{DC} | Externally applied |
| 24V | | | -0.5 to 31.7 | V _{DC} | Externally applied |
| 28V | | | -0.5 to 36.9 | V _{DC} | Externally applied |
| 36V | | | -0.5 to 47.4 | V _{DC} | Externally applied |
| 48V | | | -0.5 to 62.9 | V _{DC} | Externally applied |

Note: The permissible load current must never be exceeded during normal, abnormal or test conditions. For additional output related application information, please refer to output connections on page 6.

Thermal Resistance and Capacity

| Parameter | Min | Typ | Max | Unit |
|--|-----|------|-----|-------------|
| Baseplate to sink; flat, greased surface | | 0.08 | | °C/Watt |
| Baseplate to sink; thermal pad (P/N 20263) | | 0.07 | | °C/Watt |
| Baseplate to ambient | | 4.9 | | °C/Watt |
| Baseplate to ambient; 1000LFM | | 1.1 | | °C/Watt |
| Thermal capacity | | 165 | | Watt-sec/°C |

Module Family Electrical Characteristics (Cont.)

Electrical characteristics apply over the full operating range of input voltage, output load (resistive) and baseplate temperature, unless otherwise specified. All temperatures refer to the operating temperature at the center of the baseplate.

Module Control Specifications

| Parameter | Min | Typ | Max | Unit | Notes |
|---|-------------|-------------|-------------|-----------------------|--|
| Primary Side (PC = Primary Control; PR = Parallel) | | | | | |
| PC bias voltage current limit | 5.50 1.5 | 5.75 2.1 | 6.00 3.0 | V _{DC} mA | PC current = 1.0mA PC voltage = 5.5V During normal operation |
| PC module disable | 2.3 | 2.6 | 2.9 | V _{DC} | Switch must be able to sink ≥4mA. See Figure 2 |
| PC module enable delay | | 4 | 7 | ms | |
| PC module alarm | | | 0.5 | V _{Avg} | UV, OV, OT, module fault. See Figures 3 and 5 |
| PC resistance | 0.9 | 1.0 | 1.1 | MΩ | See Figure 3, converter off or fault mode |
| PR emitter amplitude | 5.7 | 5.9 | 6.1 | Volts | PR load >30Ω, <30pF |
| PR emitter current | 150 | | | mA | |
| PR receiver impedance | 375 | 500 | 625 | Ω | 25°C |
| PR receiver threshold | 2.4 | 2.5 | 2.6 | Volts | Minimum pulse width: 20ns |
| PR drive capability | | | 12 | modules | Without PR buffer amplifier |
| Secondary Side (SC = Secondary Control) | | | | | |
| SC bandgap voltage | 1.21 | 1.23 | 1.25 | V _{DC} | Referenced to -Sense |
| SC resistance | 990 | 1000 | 1010 | Ω | |
| SC capacitance | | 0.033 | | μF | |
| SC module alarm | | 0 | | V _{DC} | With open trim; referenced to -Sense. See Figure 7 |

Module General Specifications

| Parameter | Min | Typ | Max | Unit | Notes |
|---|-------------------|----------------|----------------|-------------------|--|
| Remote sense (total drop) | | | 0.5 | V _{DC} | 0.25V per leg (sense leads must be connected to respective, output terminals) |
| Isolation test voltage (IN to OUT) ^[a] | 3000 | | | V _{RMS} | Complies with reinforced insulation requirements |
| Isolation test voltage (IN to base) ^[a] | 1500 | | | V _{RMS} | Complies with basic insulation requirements |
| Isolation test voltage (OUT to base) ^[a] | 500 | | | V _{RMS} | Complies with operational insulation requirements |
| Isolation resistance | | 10 | | MΩ | IN to OUT, IN to baseplate, OUT to baseplate |
| Weight (E, C, T grade) | 6.5 [184.3] | 7.3 [207.5] | 8.1 [230.7] | ounces [grams] | |
| Weight (H, M grade) | 7.4 [209.3] | 8.2 [232.5] | 9.0 [255.7] | ounces [grams] | |
| Temperature limiting | 100 | 115 | | °C | See Figures 3 and 5. Do not operate converter >100°C. |
| Agency approvals | cURus, cTÜVus, CE | | | | UL60950-1, EN60950-1, CSA60950-1, IEC60950-1. With appropriate fuse in series with the +Input |

^[a] Isolation test voltage, 1 minute or less.

Note: Specifications are subject to change without notice.

Module-Specific Operating Specifications

3.3V_{OUT}, 264W (e.g. V72A3V3C264BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|------|-------|---|
| Efficiency | 79 | 80.4 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 289 | 361 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 4.14 | 4.3 | 4.46 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 10.2 | 11.7 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 80 | Amps | |
| Current limit | 81.6 | 92 | 108 | Amps | Output voltage 95% of nominal |
| Short circuit current | 56 | 92 | 108 | Amps | Output voltage <250mV |

5V_{OUT}, 300W (e.g. V72A5C300BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|------|-------|---|
| Efficiency | 83.0 | 85.5 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 100 | 125 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 6.03 | 6.25 | 6.47 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 10.4 | 11 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 60 | Amps | |
| Current limit | 61.2 | 69 | 81 | Amps | Output voltage 95% of nominal |
| Short circuit current | 22 | 69 | 81 | Amps | Output voltage <250mV |

8V_{OUT}, 300W (e.g. V72A8C300BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|------|-------|---|
| Efficiency | 83.6 | 84.8 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 215 | 269 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 9.36 | 9.7 | 10.1 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 10.2 | 10.7 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 37.5 | Amps | |
| Current limit | 38.2 | 43.1 | 50.7 | Amps | Output voltage 95% of nominal |
| Short circuit current | 26.2 | 43.1 | 50.7 | Amps | Output voltage <250mV |

12V_{OUT}, 400W (e.g. V72A12C400BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|-------|-------|---|
| Efficiency | 86.0 | 87.3 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 320 | 400 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 13.7 | 14.3 | 14.9 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 12.4 | 13.5 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 33.33 | Amps | |
| Current limit | 33.9 | 38.3 | 45 | Amps | Output voltage 95% of nominal |
| Short circuit current | 23.3 | 38.3 | 45 | Amps | Output voltage <250mV |

15V_{OUT}, 400W (e.g. V72A15C400BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|-------|-------|---|
| Efficiency | 85.2 | 86.7 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 300 | 375 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 17.1 | 17.8 | 18.5 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 12.8 | 13.2 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 26.67 | Amps | |
| Current limit | 27.2 | 30.7 | 36.1 | Amps | Output voltage 95% of nominal |
| Short circuit current | 18.6 | 30.7 | 36.1 | Amps | Output voltage <250mV |

Module-Specific Operating Specifications (Cont.)

24V_{OUT}, 400W (e.g. V72A24C400BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|-------|-------|---|
| Efficiency | 86.5 | 87.9 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 205 | 275 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 27.1 | 28.1 | 29.1 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 11 | 12.5 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 16.67 | Amps | |
| Current limit | 17 | 19.2 | 23.4 | Amps | Output voltage 95% of nominal |
| Short circuit current | 11.6 | 19.2 | 22.6 | Amps | Output voltage <250mV |

28V_{OUT}, 400W (e.g. V72A28C400BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|-------|-------|---|
| Efficiency | 86.3 | 88.4 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 190 | 260 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 31.5 | 32.7 | 33.9 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 15.6 | 17.4 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 14.29 | Amps | |
| Current limit | 14.5 | 16.4 | 19.4 | Amps | Output voltage 95% of nominal |
| Short circuit current | 10 | 16.4 | 19.4 | Amps | Output voltage <250mV |

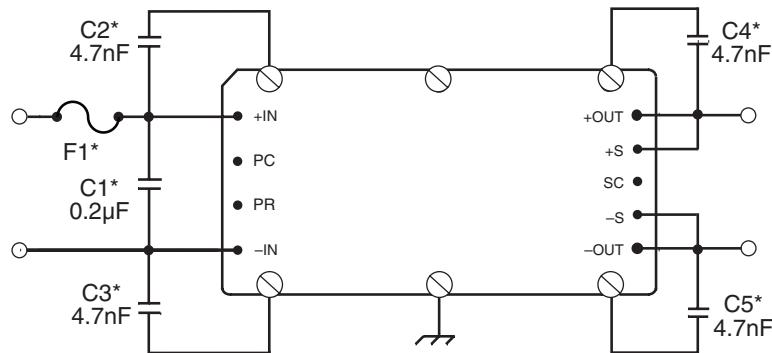
36V_{OUT}, 400W (e.g. V72A36C400BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|-------|-------|---|
| Efficiency | 86.0 | 88.0 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 200 | 250 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 40.4 | 41.9 | 43.4 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 15.3 | 16.0 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 11.11 | Amps | |
| Current limit | 11.3 | 12.8 | 15 | Amps | Output voltage 95% of nominal |
| Short circuit current | 7.77 | 12.8 | 15 | Amps | Output voltage <250mV |

48V_{OUT}, 400W (e.g. V72A48C400BL)

| Parameter | Min | Typ | Max | Unit | Notes |
|-----------------------|------|-------|------|-------|---|
| Efficiency | 85.5 | 87.5 | | % | Nominal input; 75% load; 25°C |
| Ripple and noise | | 200 | 280 | mV | P-P; Nominal input; full load; 20MHz bandwidth |
| Output OVP set point | 53.7 | 55.7 | 57.7 | Volts | 25°C; recycle input voltage or PC to restart (>100ms off) |
| Dissipation, standby | | 14.9 | 15.4 | Watts | No load |
| Load regulation | | ±0.02 | ±0.2 | % | No load to full load; nominal input |
| Output Current | 0 | | 8.33 | Amps | |
| Current limit | 8.49 | 9.58 | 11.3 | Amps | Output voltage 95% of nominal |
| Short circuit current | 5.83 | 9.58 | 11.3 | Amps | Output voltage <250mV |

Basic Module Operation



For C1 – C5, keep leads and connections short.

Figure 1 — Basic module operation requires fusing, grounding, bypassing capacitors.* See Maxi, Mini, Micro Design Guide.

Output Connections and Considerations

The permissible load current must never be exceeded during normal, abnormal or test conditions. Converters subject to dynamic loading exceeding 25% of rated current must be reviewed by Vicor Applications Engineering to ensure that the converter will operate properly.

Under dynamic-load, light-load or no-load conditions, the converter may emit audible noise. Converters that utilize remote sense may require compensation circuitry to offset the phase lag caused by the external output leads and load impedance. Remote-Sense

leads must be protected for conditions such as lead reversal, noise pickup, open circuit, or excessive output lead resistance between the sense point and the converters output terminals. For applications that may draw more than the rated current, a fast-acting electronic circuit breaker must be utilized to protect the converter. Under no circumstance should the rated current be exceeded. Utilizing or testing of current limit or short circuit current will damage the converter. Ensure that the total output capacitance connected to the converter does not exceed the limits on Page 16, "Maximum Output Capacitance", of the design guide.

Comprehensive Online Application Information



The Design Guide and Applications Manual includes:

- Application circuits
- Design requirements
- EMC considerations
- Current sharing in power arrays
- Thermal performance information
- Recommended soldering methods
- Accessory modules – filtering, rectification, front-ends
- Mounting options
- ...and more.

[CLICK HERE TO VIEW
DESIGN GUIDE](#)

Also at vicorpowers.com

- PowerBench online configurators
- Over 20 Application Notes
- Online calculators – thermal, trimming, hold-up
- PDF data sheets for ALL Vicor products

Primary Control – PC Pin

Module Enable/Disable

The module may be disabled by pulling PC below 2.3V with respect to the –Input. This may be done with an open collector transistor, relay, or optocoupler. Multiple converters may be disabled with a single transistor or relay either directly or via “OR’ing” diodes. See Figure 2.

Primary Auxiliary Supply

At 5.7V, PC can source up to 1.5mA. In the example shown in Figure 4, PC powers a module enabled LED.

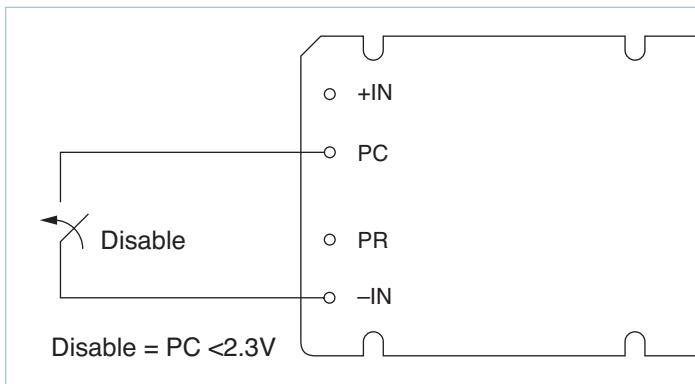


Figure 2 — Module enable/disable



The module contains "watchdog" circuitry which monitors input voltage, operating temperature and internal operating parameters. In the event that any of these parameters are outside of their allowable operating range, the module will shut down and PC will go low. PC will periodically go high and the module will check to see if the fault (as an example, overtemperature) has cleared. If the fault has not been cleared, PC will go low again and the cycle will restart. The SC pin will go low in the event of a fault and return to its normal state after the fault has been cleared. See Figures 3 and 5.

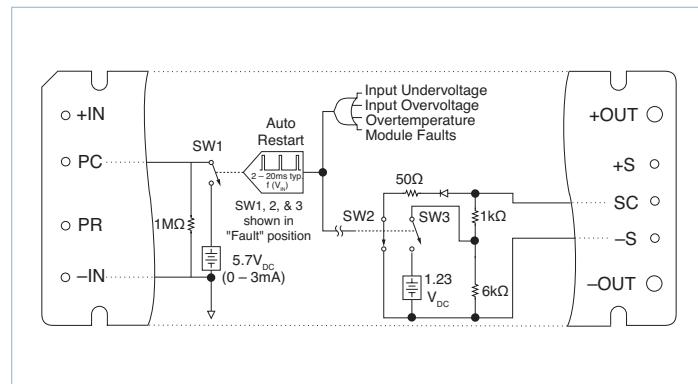


Figure 3 — PC/SC module alarm logic

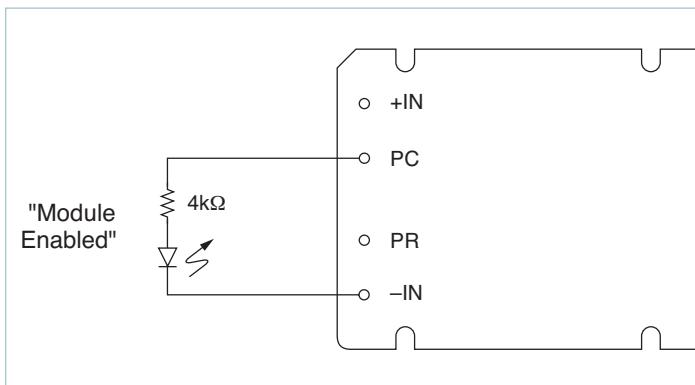


Figure 4 — LED on-state indicator

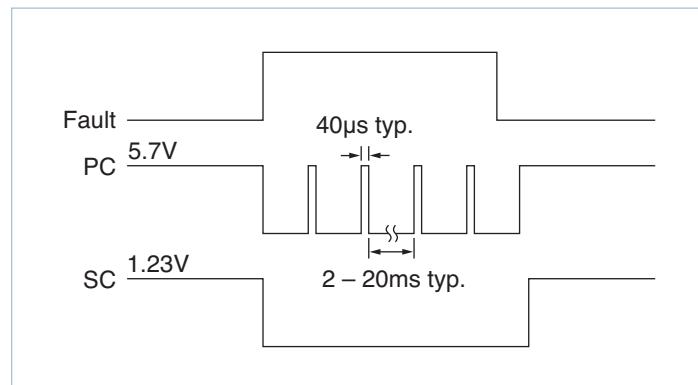


Figure 5 — PC/SC module alarm timing

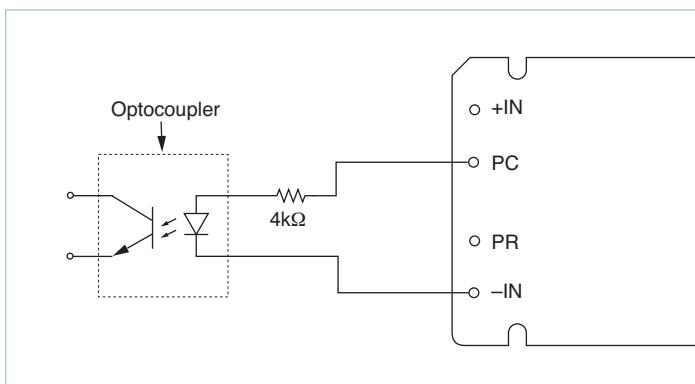


Figure 6 — Isolated on-state indicator

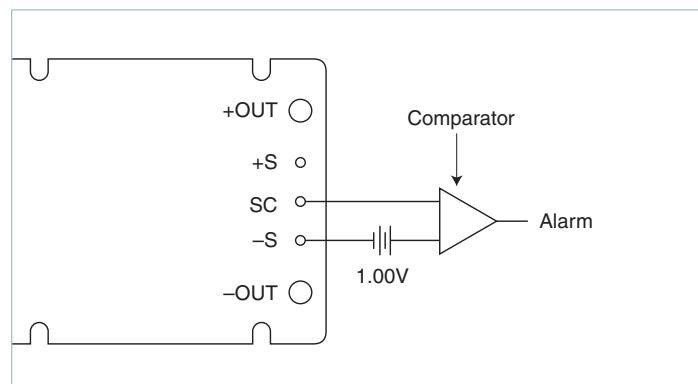
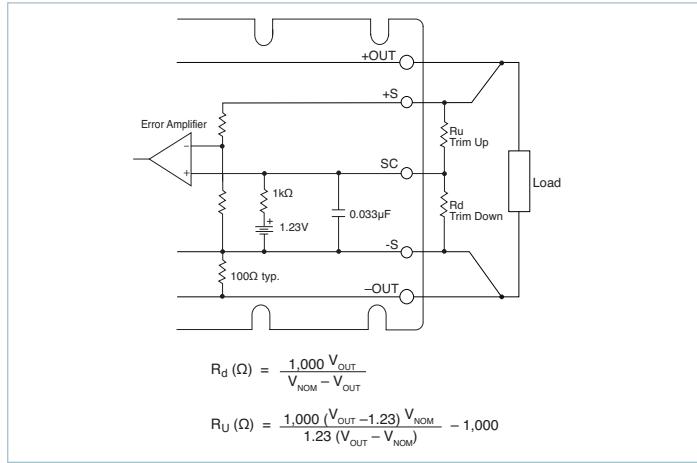


Figure 7 — Secondary-side on-state indicator

Secondary Control – SC Pin

Output Voltage Programming

The output voltage of the converter can be adjusted or programmed via fixed resistors, potentiometers or voltage DACs. See Figure 8.



Trim Down

1. This converter is not a constant-power device – it has a constant current limit. Hence, available output power is reduced by the same percentage that output voltage is trimmed down. Do not exceed maximum rated output current.
2. The trim-down resistor must be connected between the SC and -S pins. Do not bypass the SC pin directly with a capacitor.

Trim Up

1. The converter is rated for a maximum delivered power. To ensure that maximum rated power is not exceeded, reduce maximum output current by the same percentage increase in output voltage.
2. The trim-up resistor must be connected between the SC and +S pins. Do not bypass the SC pin directly with a capacitor.
3. Do not trim the converter above maximum trim range (typically +10%) or the output overvoltage protection circuitry may be activated.

Trim resistor values calculated automatically:

On-line calculators for trim resistor values are available on the vicor website at:

asp.vicorpowers.com/calculators/calculators.asp?calc=1

Resistor values can be calculated for fixed trim up, fixed trim down and for variable trim up or down.

Parallel Bus – PR Pin

Parallel Operation

The PR pin supports paralleling for increased power with N+1 (N+M) redundancy. Modules of the same input voltage, output voltage, and power level will current share if all PR pins are suitably interfaced.

Compatible interface architectures include the following:

AC-coupled single-wire interface. All PR pins are connected to a single communication bus through 0.001 μ F (500V) capacitors. This interface supports current sharing and is fault tolerant except for the communication bus. Up to three converters may be paralleled by this method. See Figure 9.

Transformer-coupled interface. For paralleling four or more converters a transformer-coupled interface is required, and under certain conditions a PR buffer circuit.

For details on parallel operation please refer to the [Design Guide & Applications Manual for Maxi, Mini, Micro Family](#).

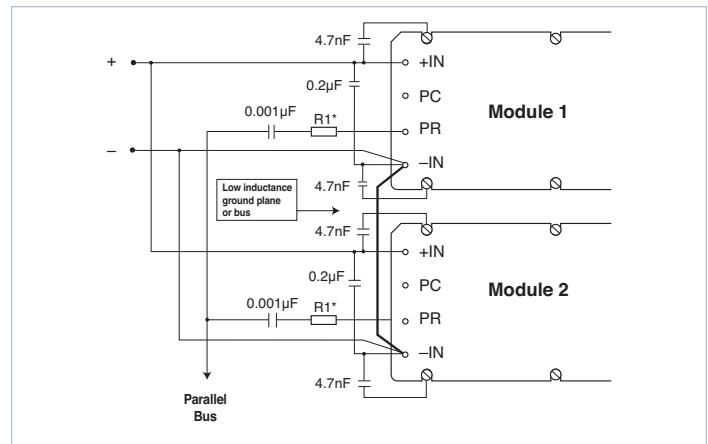


Figure 9 — AC-coupled single-wire interface

* See [Maxi, Mini, Micro Design Guide](#)

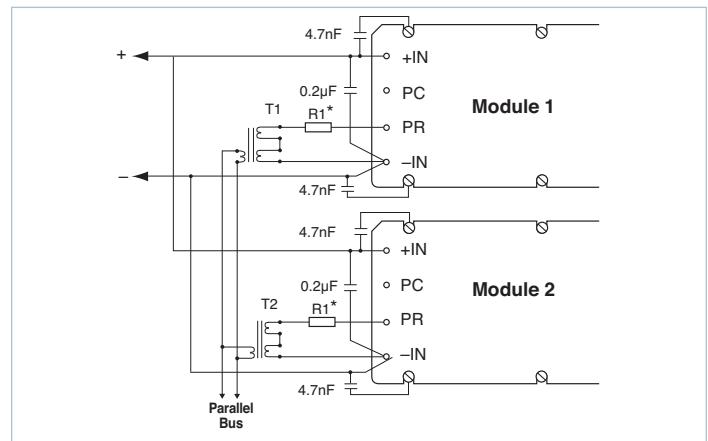
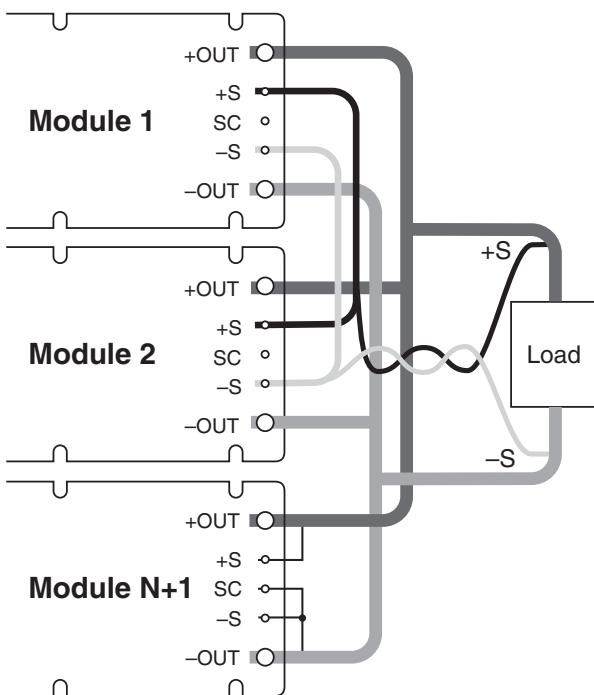


Figure 10 — Transformer-coupled interface

| Number of Converters in Parallel | *R1 value Ω |
|----------------------------------|---|
| 2 | 75 |
| 3 | 50 |
| 4 | 33 |
| 5 or more | refer to application note: Designing High-Power Arrays using Maxi, Mini, Micro Family DC-DC Converters |

Parallel Bus Output



- The +OUT and -OUT power buses should be designed to minimize and balance parasitic impedance from each module output to the load.
- The +Sense pins must be tied together to form a +Sense bus. This must be Kelvin connected to +OUT at a single point. The -Sense pins should be tied together to form a -Sense bus. This must be Kelvin connected to -OUT at a single point.
- At the discretion of the power system designer, a subset of all modules within an array may be configured as slaves by connecting SC to -S.
- OR'ing diodes may be inserted in series with the +OUT pins of each module to provide module output fault tolerance.
- The +Sense and -Sense leads should be routed in close proximity to each other on the printed circuit board. If wires are used to connect the converters on a PCB to an external load, the Sense leads should be twisted together to reduce noise pickup.

Figure 11 — N+1 module array output connections

Pin Styles*

| Designator | Description | Finish | Notes |
|------------|-----------------|----------|---|
| (None) | Short | Tin/Lead | Requires inboard mounting |
| L | Long | Tin/Lead | Onboard mounting for 0.065in boards |
| S | Short ModuMate | Gold | SurfMate or inboard socket mounting |
| N | Long ModuMate | Gold | Onboard socket mounting |
| F | Short RoHS | Gold | Select for RoHS-compliant inboard solder, socket or SurfMate mounting |
| G | Long RoHS | Gold | Select for RoHS-compliant onboard solder or socket mounting |
| K | Extra Long RoHS | Gold | Select for RoHS compliance onboard mounting for thicker PCBs (not intended for socket or Surfmate mounting) |

* Pin style designator follows the "B" after the output power and precedes the baseplate designator.

Ex. V48A12T500B**N**2 — Long ModuMate Pins

Storage

Vicor products, when not installed in customer units, should be stored in ESD safe packaging in accordance with ANSI/ESD S20.20, "Protection of Electrical and Electronic Parts, Assemblies and Equipment" and should be maintained in a temperature controlled factory/ warehouse environment not exposed to outside elements controlled between the temperature ranges of 15°C and 38°C. Humidity shall not be condensing, no minimum humidity when stored in an ESD compliant package.

Mechanical Drawings

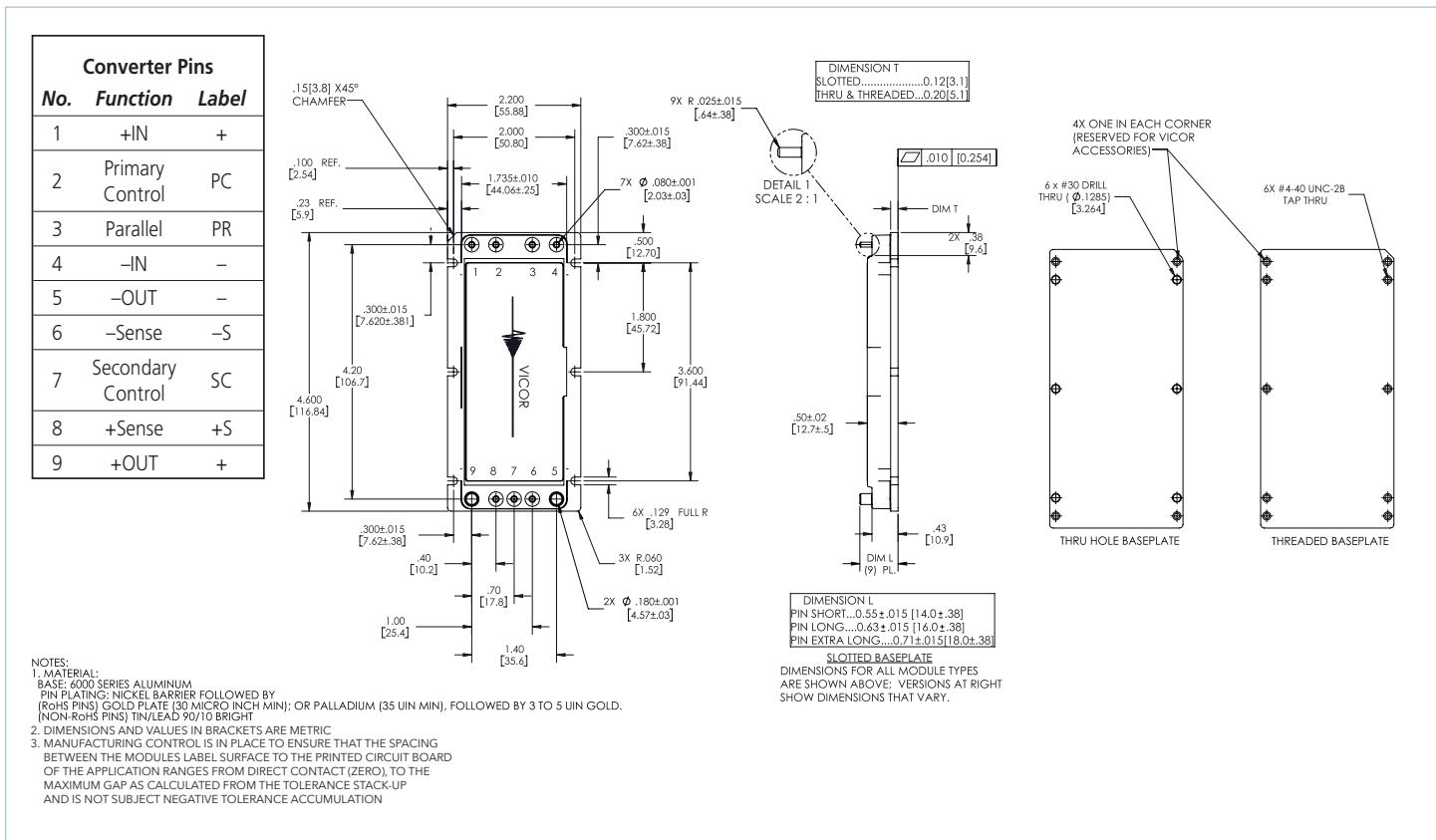


Figure 12 — Module outline

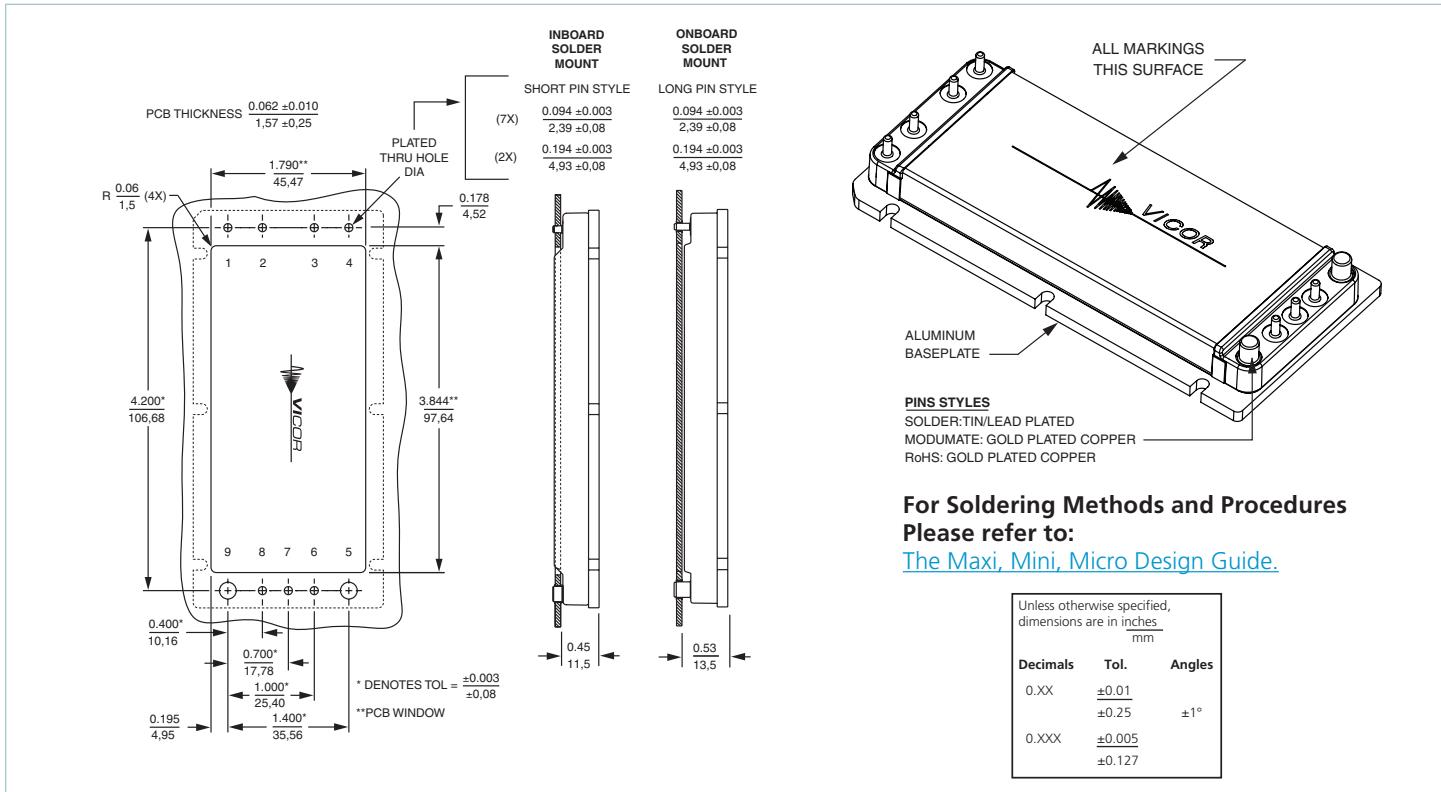


Figure 13 — PCB mounting specifications

Vicor's comprehensive line of power solutions includes high density AC-DC and DC-DC modules and accessory components, fully configurable AC-DC and DC-DC power supplies, and complete custom power systems.

Information furnished by Vicor is believed to be accurate and reliable. However, no responsibility is assumed by Vicor for its use. Vicor makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication. Vicor reserves the right to make changes to any products, specifications, and product descriptions at any time without notice. Information published by Vicor has been checked and is believed to be accurate at the time it was printed; however, Vicor assumes no responsibility for inaccuracies. Testing and other quality controls are used to the extent Vicor deems necessary to support Vicor's product warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

Specifications are subject to change without notice.

Visit <http://www.vicorpowers.com/dc-dc-converters-board-mount/high-density-dc-dc-converters> for the latest product information.

Vicor's Standard Terms and Conditions and Product Warranty

All sales are subject to Vicor's Standard Terms and Conditions of Sale, and Product Warranty which are available on Vicor's webpage (<http://www.vicorpowers.com/termsconditionswarranty>) or upon request.

Life Support Policy

VICOR'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS PRIOR WRITTEN APPROVAL OF THE CHIEF EXECUTIVE OFFICER AND GENERAL COUNSEL OF VICOR CORPORATION. As used herein, life support devices or systems are devices which (a) are intended for surgical implant into the body, or (b) support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in a significant injury to the user. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system or to affect its safety or effectiveness. Per Vicor Terms and Conditions of Sale, the user of Vicor products and components in life support applications assumes all risks of such use and indemnifies Vicor against all liability and damages.

Intellectual Property Notice

Vicor and its subsidiaries own Intellectual Property (including issued U.S. and Foreign Patents and pending patent applications) relating to the products described in this data sheet. No license, whether express, implied, or arising by estoppel or otherwise, to any intellectual property rights is granted by this document. Interested parties should contact Vicor's Intellectual Property Department.

Contact Us: <http://www.vicorpowers.com/contact-us>

Vicor Corporation

25 Frontage Road
Andover, MA, USA 01810
Tel: 800-735-6200
Fax: 978-475-6715
www.vicorpowers.com

email

Customer Service: custserv@vicorpowers.com
Technical Support: apps@vicorpowers.com

©2017 – 2019 Vicor Corporation. All rights reserved. The Vicor name is a registered trademark of Vicor Corporation.
All other trademarks, product names, logos and brands are property of their respective owners.