



#### 60V N-CHANNEL ENHANCEMENT MODE VERTICAL DMOSFET

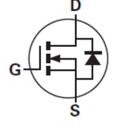
#### **Features**

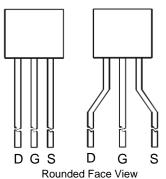
- BV<sub>DSS</sub> > 60V
- $R_{DS(on)} \le 5\Omega$  @  $V_{GS} = 10V$
- I<sub>D</sub> = 270mA Maximum Continuous Drain Current
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

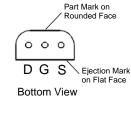
#### **Mechanical Data**

- Case: E-Line (TO-92 Compatible)
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Rating 94V-0
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.159 grams (Approximate)









Flat Face View

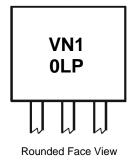
Device Symbol

### **Ordering Information** (Note 4)

Product	Marking	Package	Leads	Quantity
VN10LP	VN10LP	E-Line	Straight	4,000 Loose in a Box
VN10LPSTZ	VN10LP	E-Line	Joggled	2,000 Taped per Ammo Box

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and
- <1000ppm antimony compounds.</p>
  4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

### **Marking Information**



VN10LP = Product Type Marking Code



VN10LP

### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	±20	V
Continuous Drain Current	I <sub>D</sub>	270	mA
Pulsed Drain Current	I <sub>DM</sub>	3	Α

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Power Dissipation	(Note 5)	P <sub>D</sub>	625	mW	
Thermal Resistance, Junction to Ambient	(Note 5)	R <sub>0JA</sub>	200	°C/W	
Thermal Resistance, Junction to Leads	(Note 6)	R <sub>0JL</sub>	71	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		

Notes:

- 5. For a through-hole device mounted on the minimum recommended pad layout with 12mm lead length from the bottom of package to the single-sided FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
- 6. Thermal resistance from junction to solder-point at the seating plane (2.5mm from the bottom of package along the drain lead).

#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current		_	_	10	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
On state Drain Current (Note 7)	I <sub>D(on)</sub>	750	_	_	mA	V <sub>DS</sub> =15 V, V <sub>GS</sub> =10V	
Gate Threshold Voltage	$V_{GS(th)}$	0.8	_	2.5	V	$I_D = 1 \text{mA}, V_{DS} = V_{GS}$	
Static Drain-Source On-Resistance (Note 7)	7	) —	_	5.0	()	$V_{GS} = 10V, I_D = 500mA$	
Static Drain-Source On-Resistance (Note 1)	R <sub>DS</sub> (ON)			7.5		$V_{GS} = 5V, I_D = 200mA$	
Forward Transconductance (Notes 7 & 9)	g <sub>fs</sub>	100	_	_	mS	V <sub>DS</sub> = 15V, I <sub>D</sub> = 500mA	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>	_	_	60		.,	
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 25V$ , $V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_		5			
Turn-On Time (Note 8)	t <sub>(on)</sub>	_	_	10	V 45V 1 200 A		
Turn-Off Time (Note 8)	t <sub>(off)</sub>	_		10	ns	$V_{DD} = 15V, I_D = 600mA$	

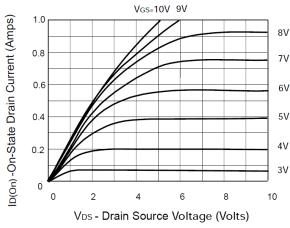
Notes:

- 7. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%.
- 8. Switching characteristics are independent of operating junction temperature. Switching times are measured with 50ohm source impedance and <5ns rise time on a pulse generator.</p>
  9. For design aid only, not subject to production testing.

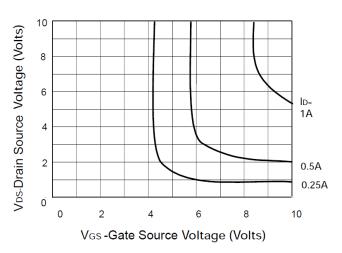


VN10LP

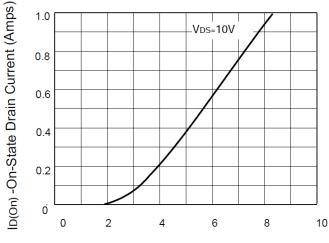
### Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)



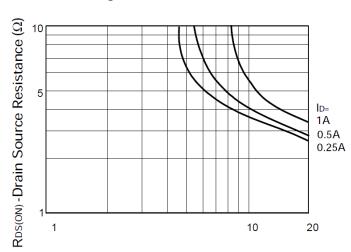
#### **Saturation Characteristics**



#### **Voltage Saturation Characteristics**

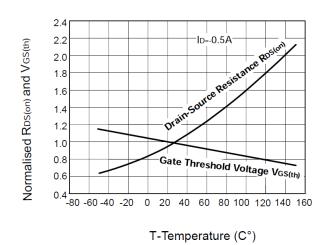


Vgs-Gate Source Voltage (Volts)



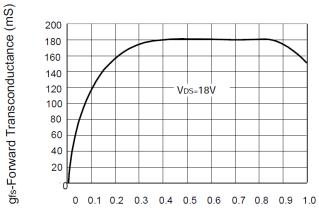
Vgs-Gate Source Voltage (Volts)

### **Transfer Characteristics**



Normalised RDS(on) and VGS(th) vs Temperature

## On-resistance vs gate-source voltage



ID(on) - Drain Current (Amps)

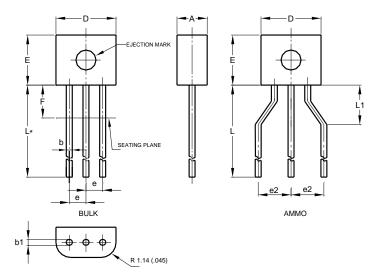
#### Transconductance v drain current





### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



E-Line					
Dim	Min	Max	Тур		
Α	2.16	2.41	-		
b	0.41	0.495	_		
b1	0.41	0.495	-		
D	4.37	4.77	_		
Е	3.61	4.01	-		
е	-	-	1.27		
e2	_	_	2.54		
F	_	2.50	_		
L	13.00	13.97	_		
L1	2.50	3.50	_		
All Dimensions in mm					





VN10LP

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