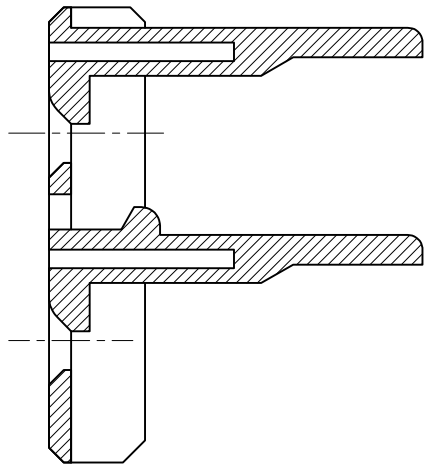
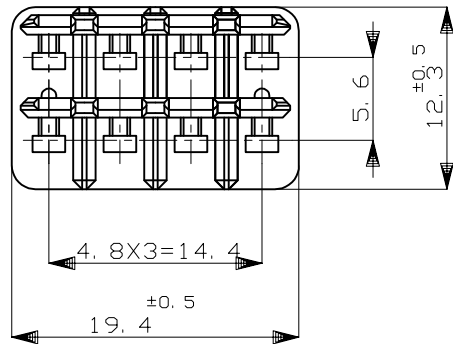
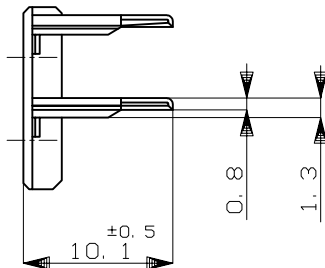
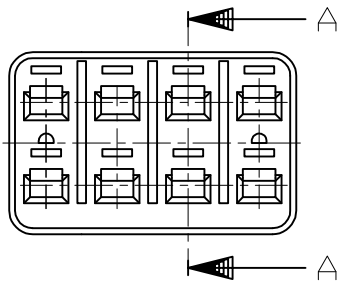
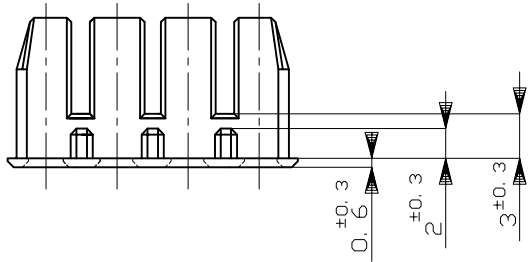


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RELEASED FOR PUBLICATION

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LOC J	DIST —	REVISIONS						
		P	LTR	DESCRIPTION	DATE	DWN	APVD	
			C1	REVISED	ECR-15-000191	27JAN2015	TS	KB



SECTION
断面 A-A
(尺度 5:1)

GENERAL TOLERANCE
0 < * ≤ 10 : ±0.2
10 < * ≤ 30 : ±0.25
30 < * ≤ 100 : ±0.3
ANGLES : ±3°

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN K.ODA 12/18'87	TE Connectivity			
DIMENSIONS: mm		CHK M.NOGUCHI 12/18'87				
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD M.NOGUCHI 11/4'88	NAME 8P. ECONOSEAL J-MARKII+ CONNECTOR DOUBLE LOCK PLATE FOR PLUG HOUSING			
0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± -		PRODUCT SPEC SEE NOTE 2	SIZE A3			
MATERIAL PBT		APPLICATION SPEC —	CAGE CODE 00779			
FINISH —		WEIGHT —	DRAWING NO C-174983			
		CUSTOMER DRAWING	RESTRICTED TO —			
			SCALE 2:1			
			SHEET 1 of 1			
			REV C1			

1. 本品は、プラグハウジング型番174981に装着される。
2. 製品規格；108-5262

1. TO BE MATED WITH PLUG HOUSING (PN 174981)
2. PRODUCT SPECIFICATION；108-5262

Mouser Electronics

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