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## **FQA90N15**

# May 2014

# N-Channel QFET $^{\circledR}$ MOSFET 150 V, 90 A, 18 m $\Omega$

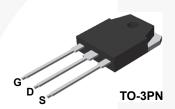
#### **Features**

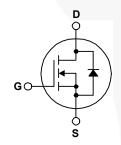
- $R_{DS(on)}$  = 18 m $\Omega$  (Max.) @  $V_{GS}$  = 10 V,  $I_D$  = 45 A
- · Low Gate Charge (Typ. 220 nC)
- · Low Crss (Typ. 200 pF)
- · 100% Avalanche Tested
- 175°C Maximum Junction Memperature Rating

### Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for low voltage applications such as audio amplifier, high efficiency switching for DC/DC converters, and DC motor control, uninterrupted power supply.





### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

9					
Symbol		Parameter		FQA90N15	Unit
V <sub>DSS</sub>	Drain-Source Voltage		/-	150	V
I <sub>D</sub>	Drain Current	- Continuous (T <sub>C</sub> = 25°C - Continuous (T <sub>C</sub> = 100°		90 63.5	A A
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	360	Α
V <sub>GSS</sub>	Gate-Source voltage			±25	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		(Note 2)	1400	mJ
I <sub>AR</sub>	Avalanche Current (Note 1		(Note 1)	90	Α
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		(Note 1)	37.5	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		(Note 3)	6.0	V/ns
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C) - Derate Above 25°C			375 2.5	W W/°C
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +175	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		oose,	300	°C

#### **Thermal Characteristics**

Symbol	Parameter	FQA90N15	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.4	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W

## **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA90N15	FQA90N15	TO-3PN	Tube	N/A	N/A	30 units

## **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
Off Charac	teristics			ı		I
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250 \mu A$	150			V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C	-	0.15		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 150V, V <sub>GS</sub> = 0V V <sub>DS</sub> = 120V, T <sub>C</sub> = 150°C			1 10	μA μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 25V, V <sub>DS</sub> = 0V	-		100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -25V, V <sub>DS</sub> = 0V	-		-100	nA
On Charac	teristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 45A		0.014	0.018	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40V, I <sub>D</sub> = 45A	-	68		S
Dynamic C	Characteristics			•	•	
C <sub>iss</sub>	Input Capacitance $V_{DS} = 25V, V_{GS} = 0V,$		\	6700	8700	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0MHz	-	1400	1800	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	200	260	pF
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 75V, I <sub>D</sub> = 90A		105	220	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 25\Omega$		760	1500	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	470	950	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4)	-	410	830	ns
Qg	Total Gate Charge	V <sub>DS</sub> = 120V, I <sub>D</sub> = 90A	/	220	285	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10V	/	43		nC
$Q_{gd}$	Gate-Drain Charge (Note 4)		74	110		nC
Drain-Sour	rce Diode Characteristics and Maximur	n Ratings	1	I	Ż	
I <sub>S</sub> Maximum Continuous Drain-Source Diode Forward Current					90	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current				360	Α
$V_{SD}$	Drain-Source Diode Forward Voltage V <sub>GS</sub> = 0V, I <sub>S</sub> = 90A				1.5	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 90A		175		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dl <sub>F</sub> /dt =100A/μs		0.97		μС

#### NOTES

<sup>1.</sup> Repetitive rating: pulse-width limited by maximum junction temperature.

<sup>2.</sup> L = 0.29 mH, I  $_{AS}$  = 90 A, V  $_{DD}$  = 25 V,  $R_{G}$  = 25  $\Omega,$  starting T  $_{J}$  = 25°C.

 $<sup>3.</sup>I_{SD} \leq 90$  A, di/dt  $\leq 300$  A/µs,  $V_{DD} \leq BV_{DSS}$ , starting  $T_J$  = 25°C.

<sup>4.</sup> Essentially independent of operating temperature typical characteristics.

## **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

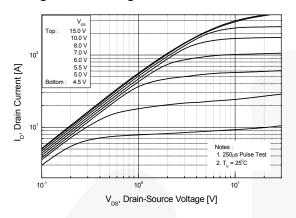


Figure 2. Transfer Characteristics

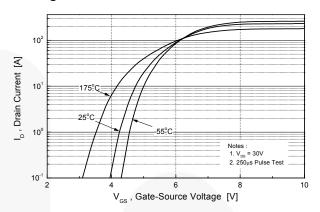


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

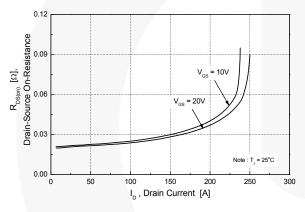
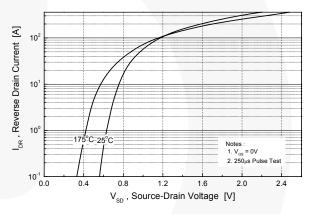


Figure 4. Body Diode Forward Voltage Variation vs. Source Current



**Figure 5. Capacitance Characteristics** 

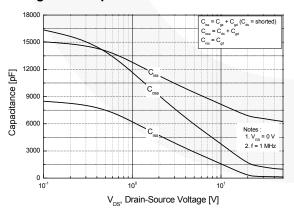
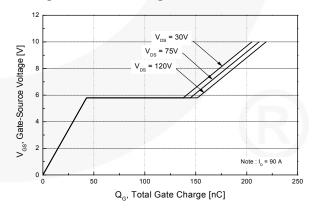


Figure 6. Gate Charge Characteristics



## Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

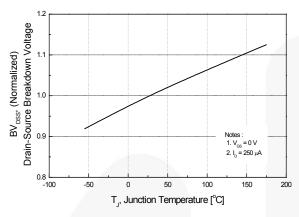


Figure 8. On-Resistance Variation vs. Temperature

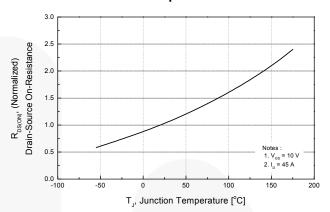


Figure 9. Maximum Safe Operating Area

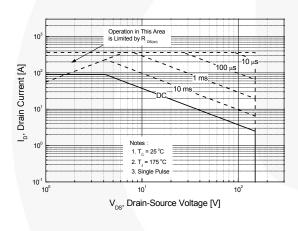


Figure 10. Maximum Drain Current vs. Case Temperature

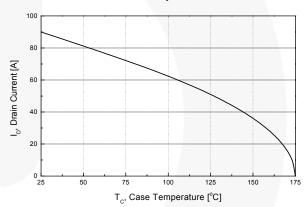
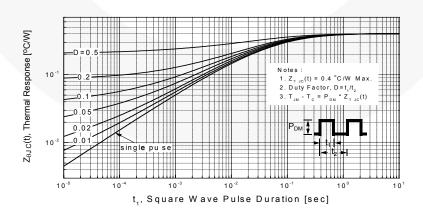


Figure 11. Transient Thermal Response Curve



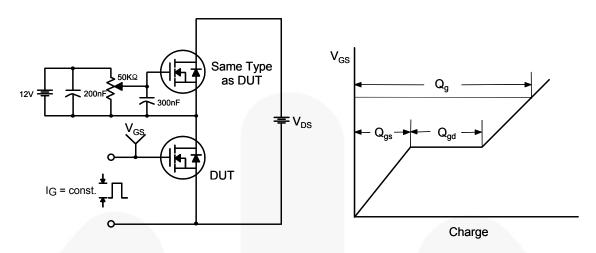


Figure 12. Gate Charge Test Circuit & Waveform

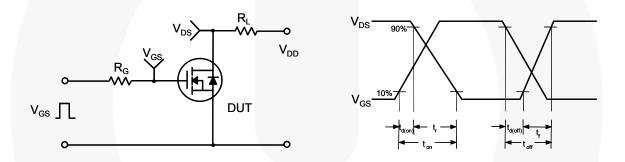


Figure 13. Resistive Switching Test Circuit & Waveforms

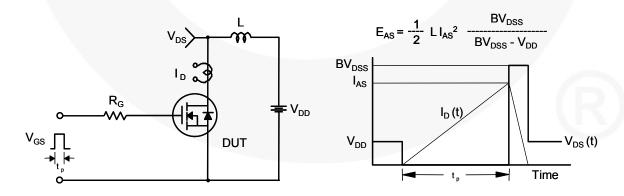


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

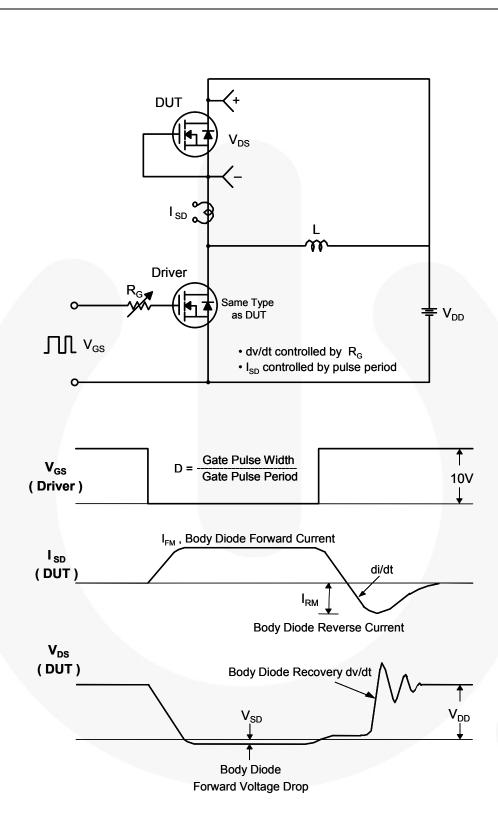
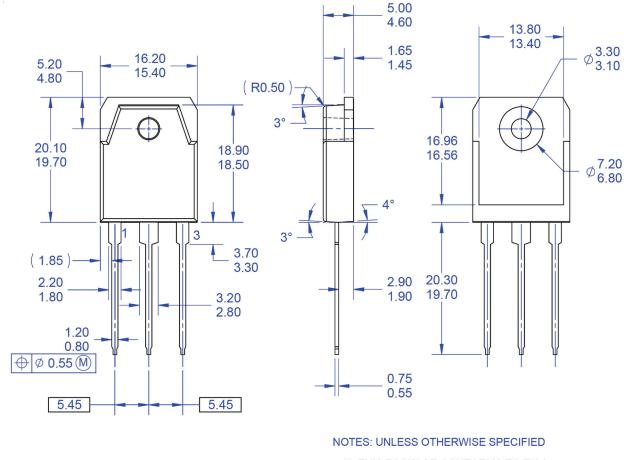
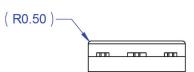


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
   B) ALL DIMENSIONS ARE IN MILLIMETERS.
- DIMENSION AND TOLERANCING PER ASME14.5-2009.
- D) DIMENSIONS ARE EXCLUSSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSSIONS.
  E) DRAWING FILE NAME: TO3PN03AREV1.
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### Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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