New Product



Si4004DY

RoHS

COMPLIANT

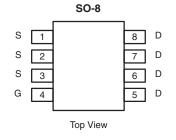
HALOGEN

FREE

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N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ.)			
20	0.0138 at V _{GS} = 10 V	12	10.6 nC			
	0.0192 at V _{GS} = 4.5 V	12	10.0110			

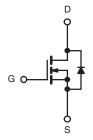


FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

DC/DC Converters



N-Channel MOSFET

Ordering Information: Si4004DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20	v	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	12 ^e		
	T _C = 70 °C		12 ^e		
	T _A = 25 °C		11 ^{b, c}		
	T _A = 70 °C		8.8 ^{b, c}	_	
Pulsed Drain Current		I _{DM}	70	A	
	T _C = 25 °C		4.2		
Continuous Source-Drain Diode Current	T _A = 25 °C	۱ _S	2.1 ^{b, c}		
Single Pulse Avalanche Current L = 0.1 mH Avalanche Energy L = 0.1 mH		I _{AS}	25		
		E _{AS}	31	mJ	
	T _C = 25 °C		5.0		
Maximum Power Dissipation	T _C = 70 °C	– P _D	3.2	w	
	T _A = 25 °C		2.5 ^{b, c}		
	T _A = 70 °C	1	1.6 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 s$	R _{thJA}	43	50	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	19	25	0/11	

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under steady state conditions is 92 °C/W.
- e. Package limited.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 050 4		22		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.2		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0 V$			1	μA	
		V_{DS} = 20 V, V_{GS} = 0 V, T_{J} = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 10$ V	30			А	
		V _{GS} = 10 V, I _D = 11 A		0.0115	0.0138	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 9.3 A		0.0160	0.0192		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 11 A		25		S	
Dynamic ^b				1			
Input Capacitance	C _{iss}			1280			
Output Capacitance	C _{oss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz		440		pF	
Reverse Transfer Capacitance	C _{rss}			195			
		V _{DS} = 10 V, V _{GS} = 10 V, I _D = 11 A		21.6	33		
Total Gate Charge	Qg			10.6	16	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 11 A		4.2			
Gate-Drain Charge	Q _{gd}			3.1			
Gate Resistance	R _g	f = 1 MHz	0.7	3.6	7.2	Ω	
Turn-On Delay Time	t _{d(on)}			15	25	-	
Rise Time	t _r	V_{DD} = 10 V, R_L = 1.1 Ω		12	20		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D} \cong$ 8.8 A, V_GEN = 4.5 V, R_g = 1 Ω		17	26		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			7	14	ns	
Rise Time	t _r	V_{DD} = 10 V, R_L = 1.1 Ω		10	20	-	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ 8.8 A, V_GEN = 10 V, R_g = 1 Ω		23	35		
Fall Time	t _f			9	18		
Drain-Source Body Diode Characterist	ics				1	1	
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			4.2	^	
Pulse Diode Forward Current ^a	I _{SM}				70	A	
Body Diode Voltage	V _{SD}	I _S = 8.8 A		0.84	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			26	39	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			15	23	nC	
Reverse Recovery Fall Time	t _a	$I_F = 8.8 \text{ A}, \text{ dl/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		13		ns	
Reverse Recovery Rise Time	t _b			13	İ		

Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

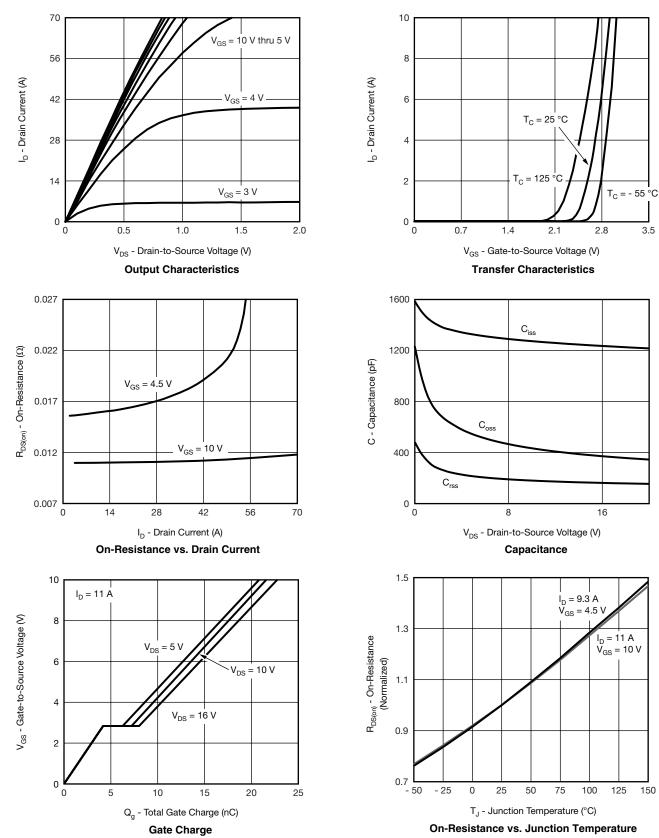
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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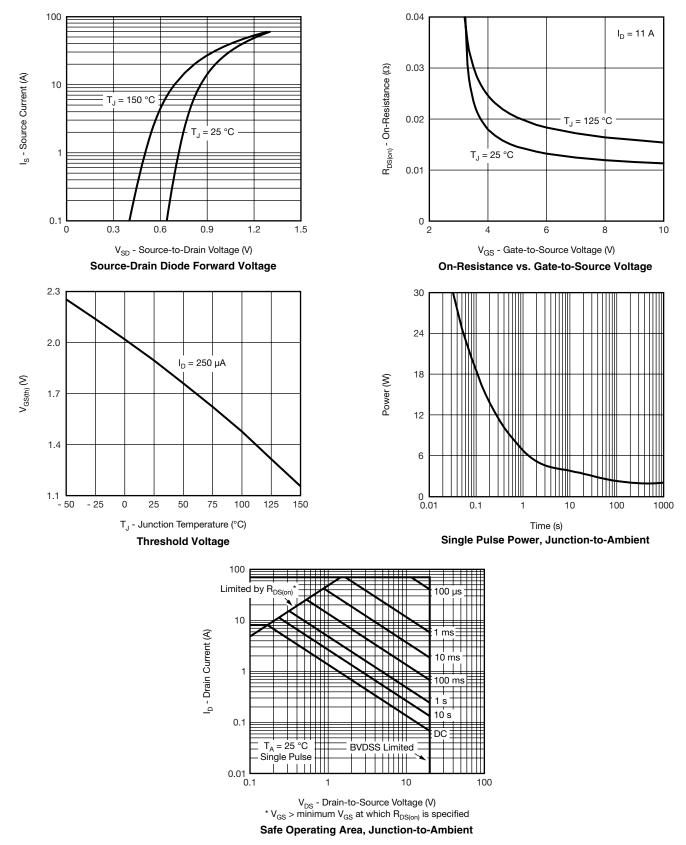


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

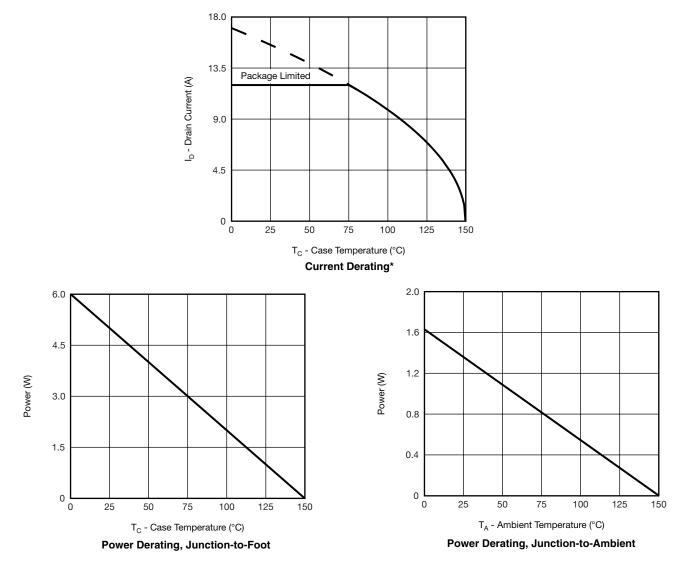


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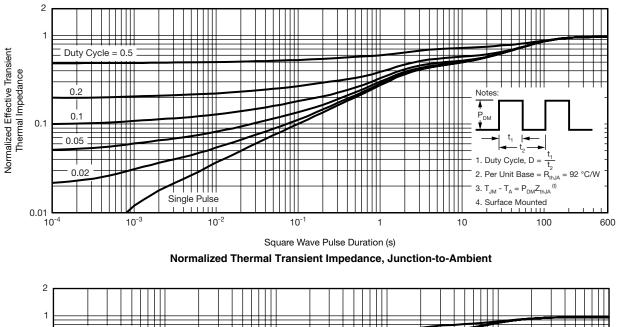


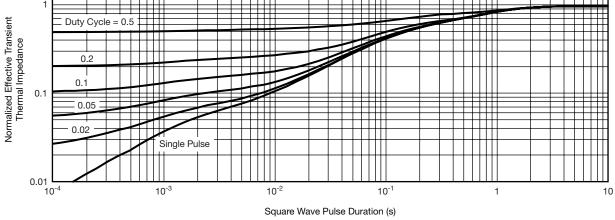
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?70338.



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