





#### **DUAL SURFACE MOUNT SWITCHING DIODE**

#### **Features**

- Fast Switching Speed
- Surface Mount Package Ideally Suited for Automated Insertion
- For General Purpose Switching Applications
- High Conductance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3 & 4)
- Qualified to AEC-Q101 Standards for High Reliability

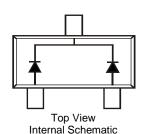
### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208<sup>3</sup>
- · Polarity: See Diagram
- Weight: 0.008 grams (Approximate)









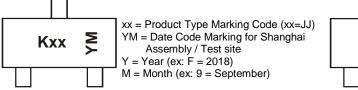
### **Ordering Information** (Note 5)

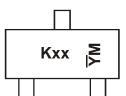
Part Number	Compliance	Case	Packaging
BAV70-7-F	Standard	SOT23	3000/Tape & Reel
BAV70-13-F	Standard	SOT23	10,000/Tape & Reel
BAV70Q-7-F	Automotive	SOT23	3000/Tape & Reel
BAV70Q-13-F	Automotive	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Products manufactured with Date Code 9W (week 39, 2009) and newer are built with Green Molding Compound. Products manufactured prior to Date Code 9W are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> Fire Retardants.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**





 $\begin{array}{l} xx = \text{Product Type Marking Code } (xx = \text{JJ}) \\ \overline{Y}M = \text{Date Code Marking for Chengdu} \\ \text{Assembly / Test site} \\ \overline{Y} = \text{Year } (\text{ex: F} = 2018) \\ \end{array}$ 

M = Month (ex: 9 = September)

Date Code Key

Date Code Rey													
Year	2000	2001		2013	2014	2015	2016	201	7 201	8 2019	2020	2021	2022
Code	L	М		Α	В	С	D	Е	F	G	Н	I	J
Month	Jan	Feb	Mar	Apr	Ма	ıy J	un	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5		6	7	8	9	0	N	D



# 

Characteristic		Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage		$V_{RM}$	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	75	V
RMS Reverse Voltage		V <sub>R(RMS)</sub>	53	V
Forward Continuous Current (Note 6)		I <sub>FM</sub>	300	mA
Average Rectified Output Current (Note 6)		lo	150	mA
Repetitive Peak Forward Current		I <sub>FRM</sub>	450	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0µs @ t = 1.0s	I <sub>FSM</sub>	2.0 1.0	А

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P <sub>D</sub>	350	mW
Thermal Resistance Junction to Ambient Air (Note 6)	$R_{\theta JA}$	357	°C/W
Operating and Storage Temperature Range	$T_J,T_STG$	-65 to +150	°C

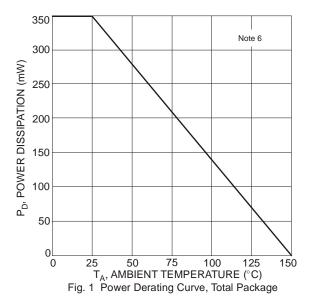
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

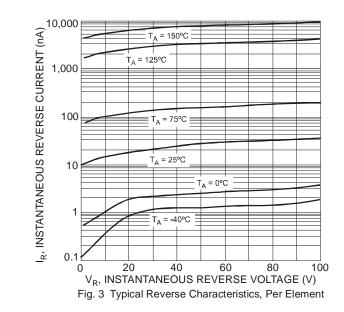
Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	75	_	>	$I_R = 2.5 \mu A$
Forward Voltage	VF		0.715 0.855 1.0 1.25	٧	I <sub>F</sub> = 1.0mA I <sub>F</sub> = 10mA I <sub>F</sub> = 50mA I <sub>F</sub> = 150mA
Reverse Current (Note 7)	I <sub>R</sub>	_	2.5 50 30 25	μA	$V_R = 75V$ $V_R = 75V$ , $T_J = +150$ °C $V_R = 25V$ , $T_J = +150$ °C $V_R = 20V$
Total Capacitance	Ст	_	2.0	pF	V <sub>R</sub> = 0, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	_	4.0	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \times I_R, R_L = 100 \Omega$

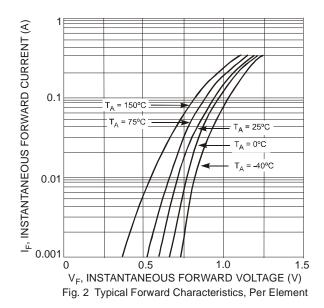
Notes:

- 6. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com.
- 7. Short duration pulse test used to minimize self-heating effect.









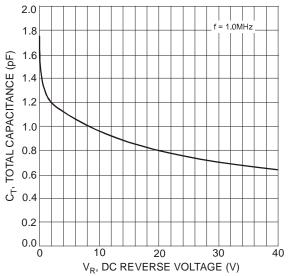
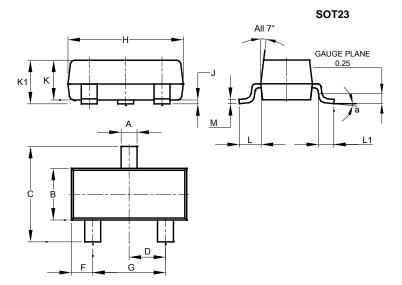


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element



# **Package Outline Dimensions**

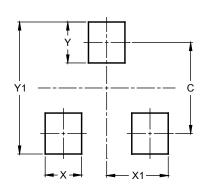
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
<b>K</b> 1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



### SOT23

Dimensions	Value (in mm)
C	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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