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November 1996 **Revised December 2005** 

# NC7SZ384 **1-Bit Low Power Bus Switch**

### **General Description**

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The NC7SZ384 provides 1-bit of ultra high-speed CMOS TTLcompatible bus switch. The low On Resistance of the switch allows inputs to be connected to outputs with minimal propagation delay and without generating additional ground bounce noise. The device is organized as a 1-bit switch with a bus enable  $(\overline{OE})$  signal. When  $\overline{OE}$  is LOW, the switch is on and Port A is connected to Port B. When  $\overline{OE}$  is HIGH, the switch is open and a high-impedance state exists between the two ports.

#### **Features**

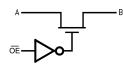
- Space saving SOT23 or SC70 5-lead package
- Ultra small MicroPak<sup>™</sup> leadless package
- $\blacksquare$  5 $\Omega$  switch connection between two ports
- Minimal propagation delay through the switch
- Low I<sub>CC</sub>
- Zero bounce in flow-through mode
- Control inputs compatible with TTL level

### **Ordering Code:**

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As
NC7SZ384M5X	MA05B	8Z84	5-Lead SOT23, JEDEC MO-178, 1.6mm	3k Units on Tape and Reel
NC7SZ384P5X	MAA05A	Z84	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel
NC7SZ384L6X	MAC06A	C3	Pb-Free 6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

Pb-Free package per JEDEC J-STD-020B.

#### Logic Diagram



### **Pin Description**

Pin Name	Description
OE	Bus Switch Enable
A	Bus A
В	Bus B
NC	No Connect

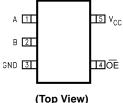
#### **Truth Table**

OE	B <sub>O</sub>	Function
L	A <sub>O</sub>	Connect
Н	HIGH-Z State	Disconnect

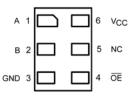
MicroPak™ is a trademark of Fairchild Semiconductor Corporation.

### **Connection Diagrams**

#### Pin Assignments for SOT23 and SC70



#### Pad Assignments for MicroPak



(Top Through View)

## Absolute Maximum Ratings(Note 1)

$\mathbf{O}$ and $\mathbf{O}$ (1) (1) (1) (1)	
Supply Voltage (V <sub>CC</sub> )	-0.5V to +7.0V
DC Switch Voltage (V <sub>S</sub> )	-0.5V to +7.0V
DC Input Voltage (V <sub>IN</sub> ) (Note 2)	-0.5V to +7.0V
DC Input Diode Current	
(I <sub>IK</sub> ) V <sub>IN</sub> < 0V	–50 mA
DC Output (I <sub>OUT</sub> ) Sink Current	128 mA
DC V <sub>CC</sub> /GND Current	
(I <sub>CC</sub> /I <sub>GND</sub> )	±100 mA
Storage Temperature Range	
(T <sub>STG</sub> )	$-65^{\circ}C$ to $+150^{\circ}C$
Junction Temperature	
under Bias (T <sub>J</sub> )	+150°C
Junction Lead Temperature (T <sub>L</sub> )	
(Soldering, 10 Seconds)	+260°C
Power Dissipation (P <sub>D</sub> ) @ +85°C	
SOT23-5	200 mW
SC70-5	150 mW

# Recommended Operating Conditions (Note 3)

Power Supply Operating (V <sub>CC</sub> )	4.0V to 5.5V
Input Voltage (V <sub>IN</sub> )	0V to 5.5V
Output Voltage (V <sub>OUT</sub> )	0V to 5.5V
Input Rise and Fall Time (t <sub>r</sub> , t <sub>f</sub> )	
Switch Control Input	0 ns/V to 5 ns
Switch I/O	0 ns/V to DC
Operating Temperature (T <sub>A</sub> )	-40°C to +85°C
Thermal Resistance ( $\theta_{JA}$ )	
SOT23-5	300°C/W
SC70-5	425°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

# **DC Electrical Characteristics**

Symbol	Parameter	V <sub>CC</sub>	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			Units	Conditions
Cymbol		(V)	Min	Тур	Max	Unita	Conditions
V <sub>IK</sub>	Clamp Diode Voltage	4.5			-1.2	-V	I <sub>IN</sub> = -18 mA
V <sub>IH</sub>	HIGH Level Input Voltage	4.5-5.5	2.0			V	
V <sub>IL</sub>	LOW Level Input Voltage	4.5-5.5			0.8	V	
I <sub>IN</sub>	Input Leakage Current	5.5			±1.0	μA	$0 \le V_{IN} \le 5.5V$
I <sub>OFF</sub>	"OFF" Leakage Current	5.5			±10.0	μA	$0 \le A, B \le V_{CC}$
R <sub>ON</sub>	Switch On Resistance	4.5		3	7	Ω	$V_{IN} = 0V, I_{IN} = 64 \text{ mA}$
	(Note 4)	4.5		3	7	Ω	V <sub>IN</sub> = 0V, I <sub>IN</sub> = 30 mA
		4.5		6	15	Ω	V <sub>IN</sub> = 2.4V, I <sub>IN</sub> = 15 mA
		4.0		10	20	Ω	$V_{IN} = 2.4V, I_{IN} = 15 \text{ mA}$
I <sub>CC</sub>	Quiescent Supply Current	5.5			10	μA	$V_{IN} = V_{CC}$ or GND
							I <sub>O</sub> = 0
$\Delta I_{CC}$	Increase in I <sub>CC</sub> per Input (Note 5)	5.5		0.9	2.5	mA	$V_{IN} = 3.4V$ , $I_O = 0$ , Control Input only

Note 4: Measured by the voltage drop between A and B pins at the indicated current through the switch. On Resistance is determined by the lower of the voltages on the two (A or B) pins.

Note 5: Per TTL driven input ( $V_{IN}$  = 3.4V, control input only). A and B pins do not contribute to I<sub>CC</sub>.

# **AC Electrical Characteristics**

				= -40°C to +85				
Symbol	Parameter	V <sub>cc</sub>	$\mathbf{C_L} = 50 \ \mathbf{pF}, \ \mathbf{RU} = \mathbf{RD} = 500 \Omega$			Units	Conditions	Figure
		(V)	Min	Typ (Note 6)	Max			Number
t <sub>PHL</sub> ,	Propagation Delay Bus to Bus	4.0-5.5			0.25	ns	V <sub>I</sub> = OPEN	Figures
t <sub>PLH</sub>	(Note 7)							1, 2
t <sub>PZL</sub> ,	Output Enable Time	4.5–5.5	1.0	2.5	5.0	ns	$V_I = 7V$ for $t_{PZL}$	Figures
t <sub>PZH</sub>		4.0	1.0		5.5	ns	V <sub>I</sub> = OPEN for t <sub>PZH</sub>	1, 2
t <sub>PLZ</sub> ,	Output Disable Time	4.5-5.5	1.0	2.5	5.0	ns	$V_I = 7V$ for $t_{PLZ}$	Figures
t <sub>PHZ</sub>		4.0	1.0		5.5	ns	V <sub>I</sub> = OPEN for t <sub>PHZ</sub>	1, 2

Note 6: All typical values are  $V_{CC}=5.0V,\,T_A=25\,^{\circ}C.$ 

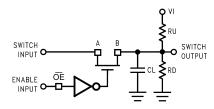
Note 7: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical On Resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage source (zero output impedance).

### Capacitance (Note 8)

Symbol	Parameter	Тур	Max	Units	Conditions
CIN	Control Pin Input Capacitance	2	6	pF	$V_{CC} = 5.0V$
C <sub>I/O</sub>	Input/Output Capacitance	4.5	10	pF	$V_{CC}, \overline{BE} = 5.0V$

Note 8:  $T_A = 25^{\circ}C$ , f = 1 MHz.

# AC Loading and Waveforms

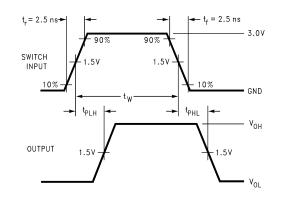


Input driven by  $50\Omega$  source terminated in  $50\Omega$ 

 $\rm C_L$  includes load and stray capacitance

Input PRR = 1.0 MHz;  $t_W$  = 500 ns

#### FIGURE 1. AC Test Circuit



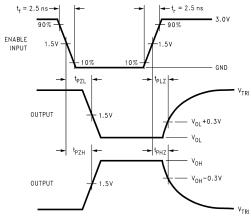


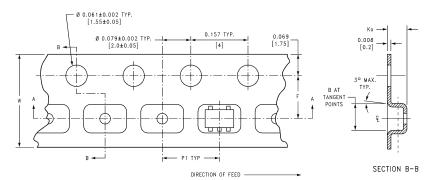
FIGURE 2. AC Waveforms

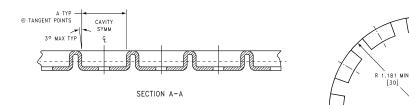
# Tape and Reel Specification

TAPE FORMAT for SOT23 and SC70

Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
	Leader (Start End)	125 (typ)	Empty	Sealed
M5X, P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

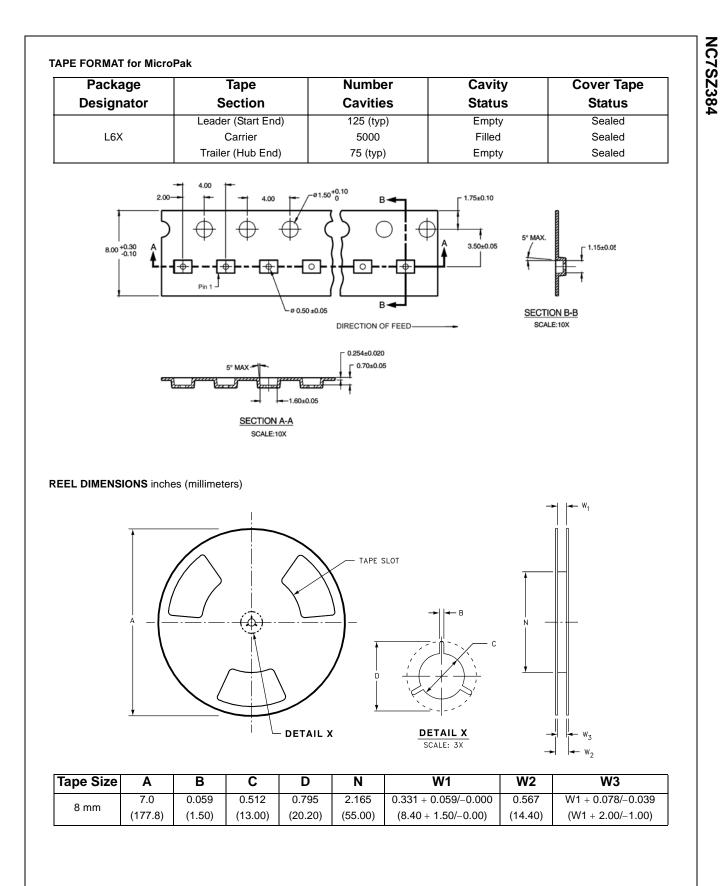
#### TAPE DIMENSIONS inches (millimeters)



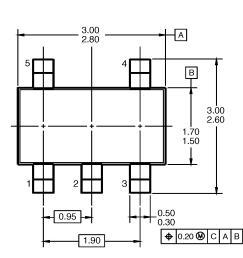


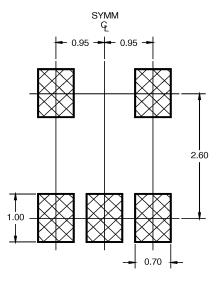
BEND RADIUS NOT TO SCALE

Package	Tape Size	DIM A	DIM B	DIM F	DIM K <sub>o</sub>	DIM P1	DIM W
SC70-5	0	0.093	0.096	$0.138 \pm 0.004$	$0.053 \pm 0.004$	0.157	$0.315\pm0.004$
5070-5	8 mm	(2.35)	(2.45)	$(\textbf{3.5}\pm\textbf{0.10})$	$(1.35 \pm 0.10)$	(4)	$(8\pm0.1)$
SOT23-5	0	0.130	0.130	$0.138 \pm 0.002$	$0.055 \pm 0.004$	0.157	$0.315\pm0.012$
50123-5	8 mm	(3.3)	(3.3)	$(3.5\pm0.05)$	$(1.4\pm0.11)$	(4)	(8 ± 0.3)

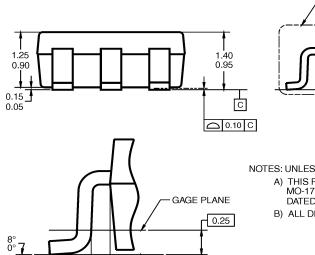


### Physical Dimensions inches (millimeters) unless otherwise noted





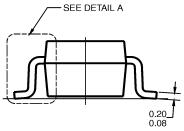
LAND PATTERN RECOMMENDATION



0.55 0.35

0.60 REF

DETAIL A



NOTES: UNLESS OTHERWISE SPECIFIED A) THIS PACKAGE CONFORMS TO JEDEC MO-178, ISSUE B, VARIATION AA, DATED JANUARY 1999.

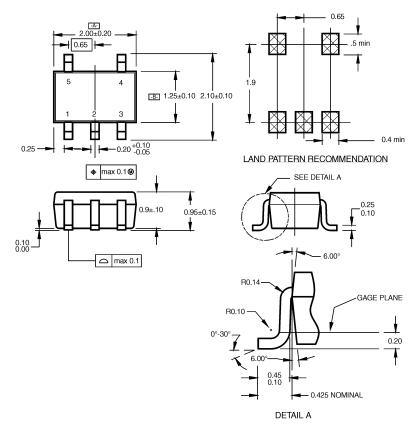
MA05BRevC

5-Lead SOT23, JEDEC MO-178, 1.6mm Package Number MA05B

SEATING PLANE

B) ALL DIMENSIONS ARE IN MILLIMETERS.





#### NOTES:

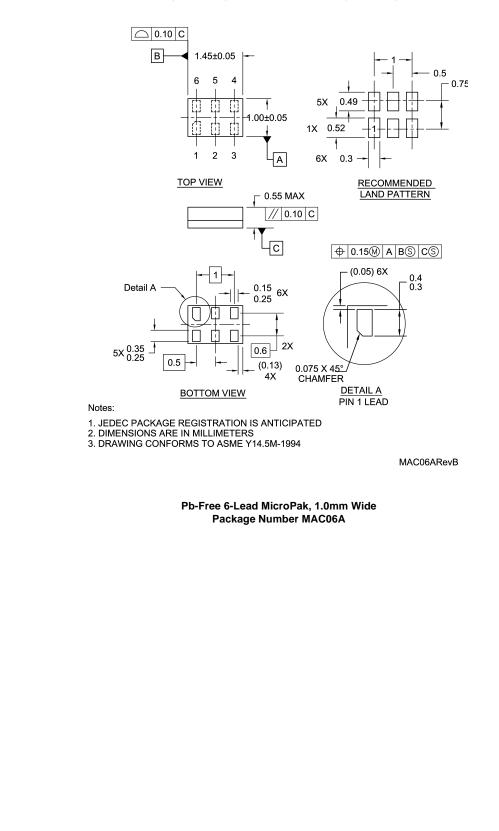
A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A. B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.

MAA05ARevC

C. DIMENSIONS ARE IN MILLIMETERS.

5-Lead SC70, EIAJ SC-88a, 1.25mm Wide Package Number MAA05A NC7SZ384





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