



Pin Definition:

1. Gate
2. Source
3. Drain

Key Parameter Performance

Parameter		Value	Unit
V_{DS}		20	V
$R_{DS(on)}$ (max)	$V_{GS} = 4.5V$	33	mΩ
	$V_{GS} = 2.5V$	40	
	$V_{GS} = 1.8V$	51	
Q_g		11	nC

Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

Application

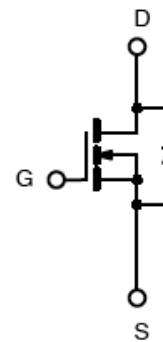
- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing
TSM2312CX RFG	SOT-23	3,000pcs / 7" Reel

Note: "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings ($T_C = 25^\circ C$, unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	±8	V
Continuous Drain Current		I _D	4.9	A
Pulsed Drain Current ^(Note 1)		I _{DM}	15	A
Continuous Source Current (Diode Conduction) ^(Note 2)		I _S	1.0	A
Maximum Power Dissipation	Ta = 25°C	P _D	0.75	W
	Ta = 75°C		0.48	
Operating Junction and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Thermal Performance

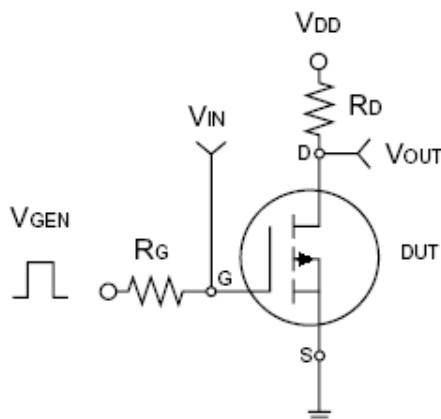
Parameter	Symbol	Limit	Unit
Thermal Resistance Junction to Lead	$R_{\theta JL}$	75	°C/W
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	140	°C/W

Electrical Specifications

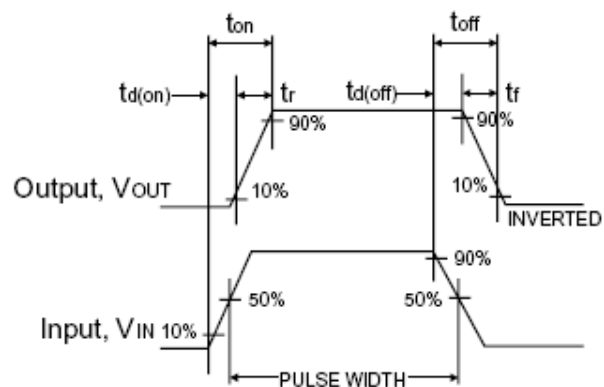
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static ^(Note 3)						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250uA	BV _{DSS}	20	--	--	V
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250uA	V _{GS(TH)}	0.45	0.65	1.0	V
Gate Body Leakage	V _{GS} = ±8V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V _{DS} = 16V, V _{GS} = 0V	I _{DSS}	--	--	1.0	μA
On-State Drain Current	V _{DS} =10V, V _{GS} = 4.5V	I _{D(ON)}	15	--	--	A
Drain-Source On-State Resistance	V _{GS} = 4.5V, I _D = 4.9A	R _{DS(ON)}	--	27	33	mΩ
	V _{GS} = 2.5V, I _D = 4.4A		--	33	40	
	V _{GS} = 1.8V, I _D = 3.9A		--	42	51	
Forward Transconductance	V _{DS} = 15V, I _D = 5.0A	g _{fs}	--	40	--	S
Diode Forward Voltage	I _S = 1.0A, V _{GS} = 0V	V _{SD}	--	0.8	1.2	V
Dynamic ^(Note 4)						
Total Gate Charge	V _{DS} = 10V, I _D = 5.0A, V _{GS} = 4.5V	Q _g	--	11	14	nC
Gate-Source Charge		Q _{gs}	--	1.5	--	
Gate-Drain Charge		Q _{gd}	--	2.1	--	
Input Capacitance	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz	C _{iss}	--	500	--	pF
Output Capacitance		C _{oss}	--	300	--	
Reverse Transfer Capacitance		C _{rss}	--	140	--	
Switching ^(Note 5)						
Turn-On Delay Time	V _{DD} = 10V, R _L = 10Ω, I _D = 1A, V _{GEN} = 4.5V, R _G = 6Ω	t _{d(on)}	--	15	25	ns
Turn-On Rise Time		t _r	--	40	60	
Turn-Off Delay Time		t _{d(off)}	--	48	70	
Turn-Off Fall Time		t _f	--	31	45	

Notes:

1. Pulse width limited by the maximum junction temperature
2. Surface Mounted on FR4 Board $t \leq 5$ sec.
3. Pulse test: $PW \leq 300\mu s$, duty cycle $\leq 2\%$
4. For DESIGN AID ONLY, not subject to production testing.
5. Switching time is essentially independent of operating temperature.



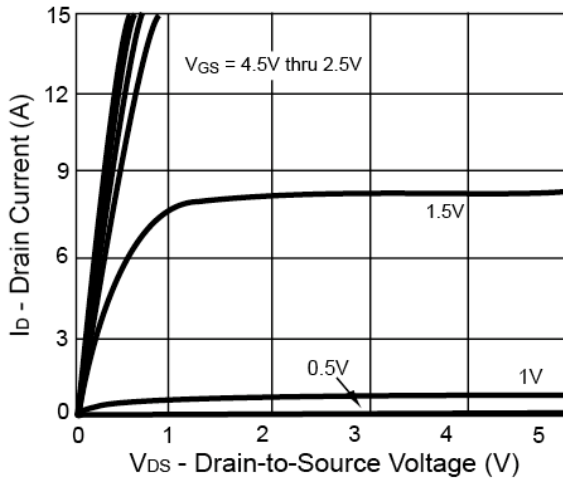
Switching Test Circuit



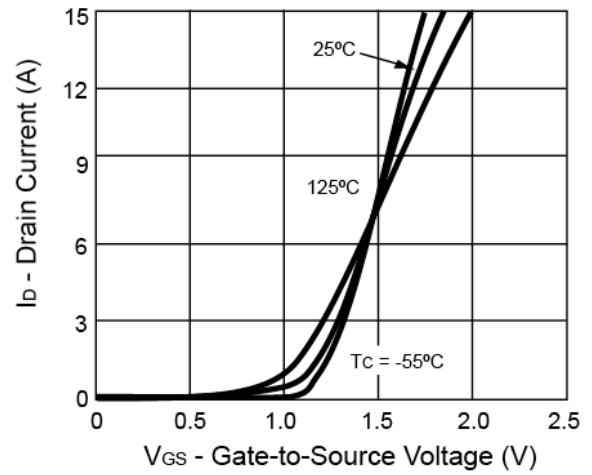
Switchin Waveforms

Electrical Characteristics Curve

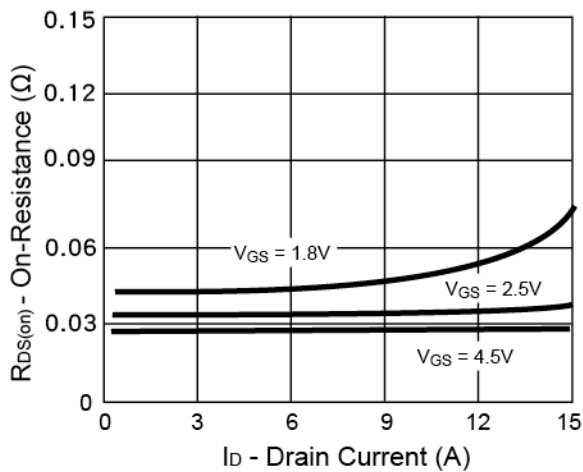
Output Characteristics



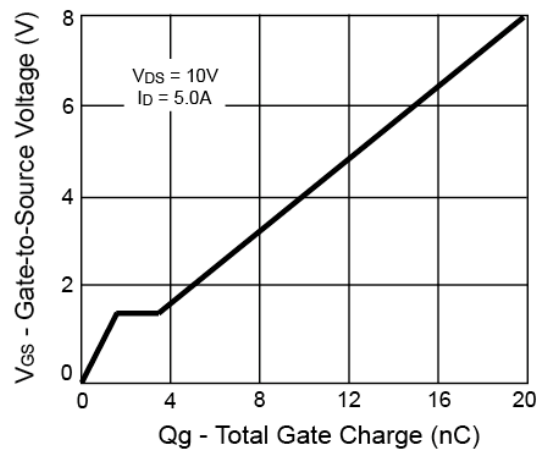
Transfer Characteristics



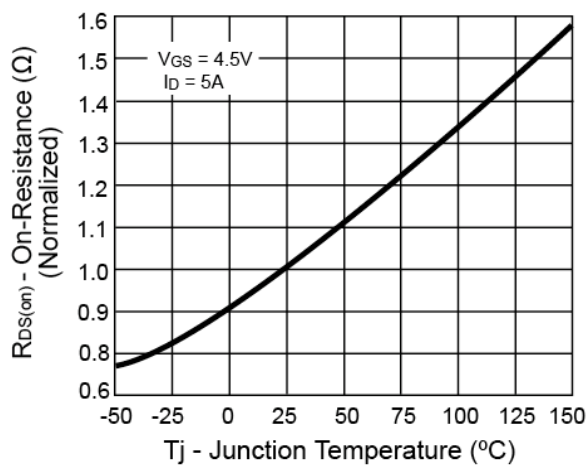
On-Resistance vs. Drain Current



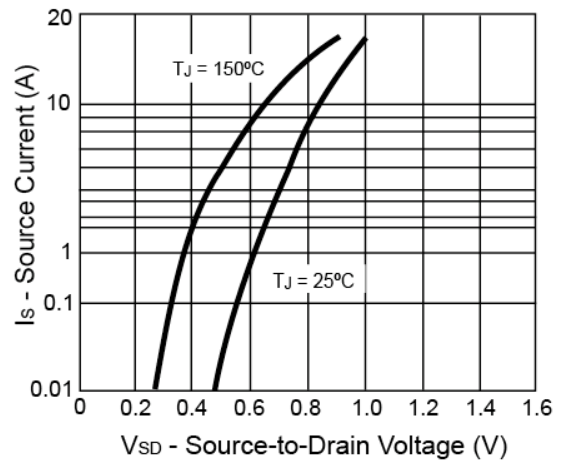
Gate Charge



On-Resistance vs. Junction Temperature

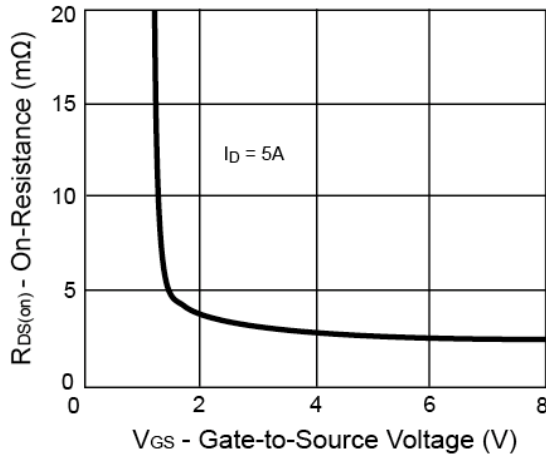


Source-Drain Diode Forward Voltage

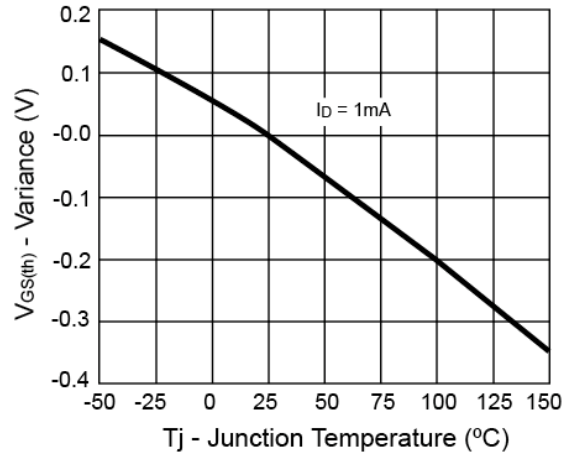


Electrical Characteristics Curve

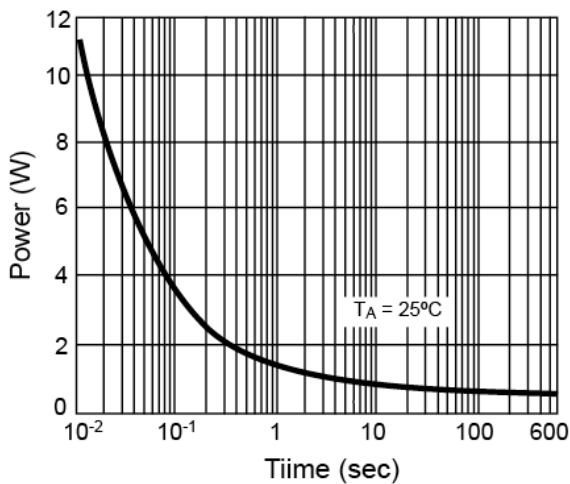
On-Resistance vs. Gate-Source Voltage



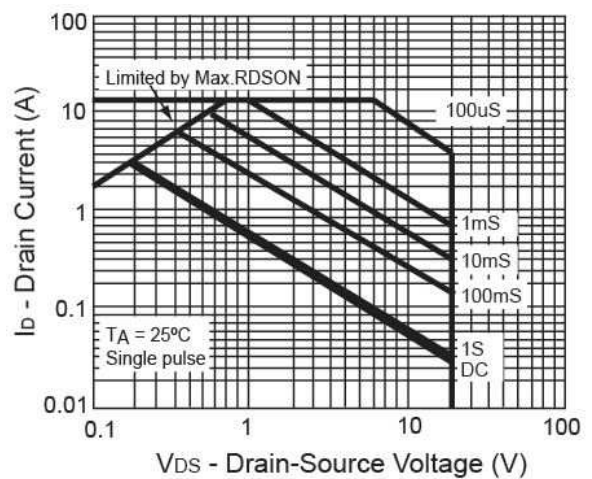
Threshold Voltage



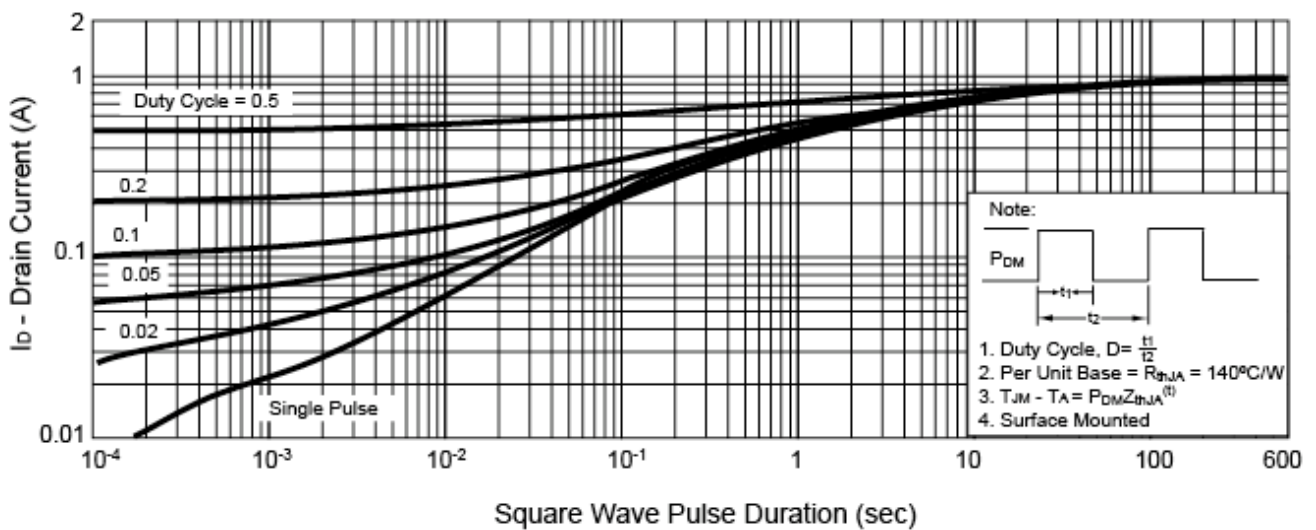
Single Pulse Power



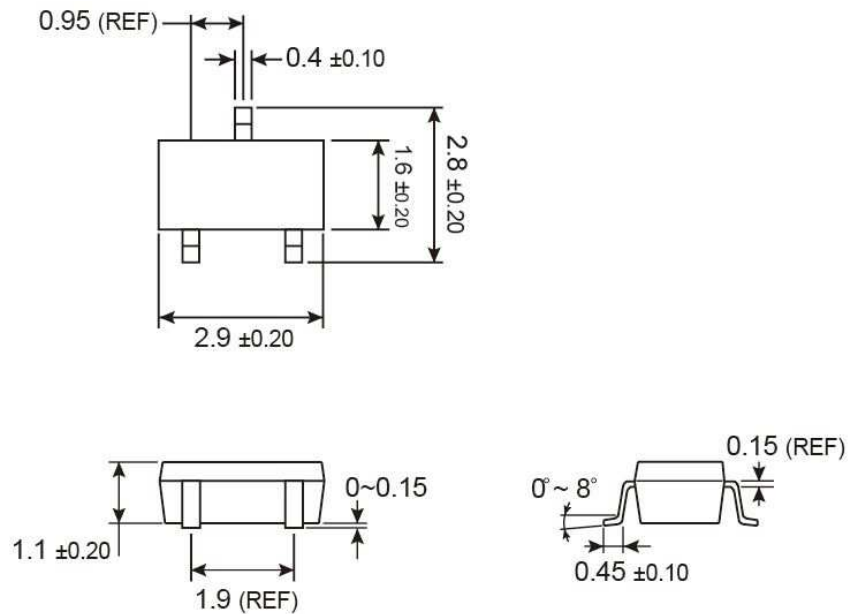
Safety Operation Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

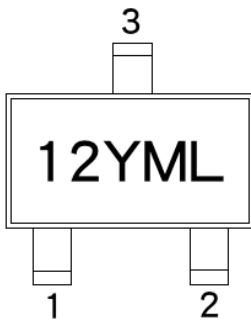


SOT-23 Mechanical Drawing



Unit: Millimeters

Marking Diagram



12 = Device Code
Y = Year Code
M = Month Code for Halogen Free Product
 O =Jan **P** =Feb **Q** =Mar **R** =Apr
 S =May **T** =Jun **U** =Jul **V** =Aug
 W =Sep **X** =Oct **Y** =Nov **Z** =Dec
L = Lot Code

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