



## Precision Monolithic Low-Voltage CMOS Analog Switches

### DESCRIPTION

The DG417L, DG418L, DG419L are low voltage pin-for-pin compatible companion devices to the industry standard DG417, DG418, DG419 with improved performance. Using BiCMOS wafer fabrication technology allows the DG417L, DG418L, DG419L to operate on single and dual supplies. Single supply voltage ranges from 3 V to 12 V while dual supply operation is recommended with  $\pm 3$  V to  $\pm 6$  V. Combining high speed ( $t_{ON}$ : 28 ns), flat  $R_{ON}$  over the analog signal range (6  $\Omega$ ), minimal insertion lose (up to 100 MHz), and excellent crosstalk and off-isolation performance (-70 dB at 1 MHz), the DG417L, DG418L, DG419L are ideally suited for audio and video signal switching. The DG417L and DG418L respond to opposite control logic as shown in the truth table. The DG419L has an SPDT configuration.

### FEATURES

- 2.7 V- thru 12 V single supply or  $\pm 3$ - thru  $\pm 6$  dual supply
- On-resistance -  $R_{ON}$ : 14  $\Omega$
- Fast switching -  $t_{ON}$ : 28 ns  
-  $t_{OFF}$ : 13 ns
- TTL, CMOS compatible
- Low leakage: < 100 pA
- Material categorization: For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



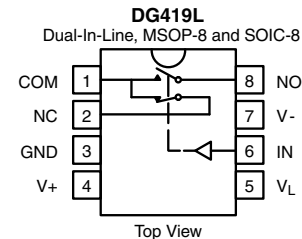
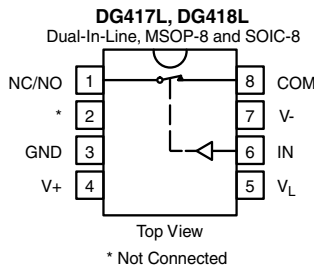
### APPLICATIONS

- Precision automatic test equipment
- Precision data acquisition
- Communication systems
- Battery powered systems
- Computer peripherals
- SDSL, DSLAM
- Audio and video signal routing

### BENEFITS

- Widest dynamic range
- Low signal errors and distortion
- Break-before-make switching action
- Simple interfacing

### FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



| TRUTH TABLE |        |        |
|-------------|--------|--------|
| Logic       | DG417L | DG418L |
| 0           | ON     | OFF    |
| 1           | OFF    | ON     |

| TRUTH TABLE (DG419L) |     |     |
|----------------------|-----|-----|
| Logic                | NC  | NO  |
| 0                    | ON  | OFF |
| 1                    | OFF | ON  |

| ORDERING INFORMATION (DG417L, DG418L) |                   |                |
|---------------------------------------|-------------------|----------------|
| Temp. Range                           | Package           | Part Number    |
|                                       | 8-Pin Narrow SOIC | DG417LDY       |
|                                       |                   | DG417LDY-E3    |
| DG417LDY-T1                           |                   |                |
| DG417LDY-T1-E3                        |                   |                |
|                                       | 8-Pin MSOP        | DG418LDY       |
|                                       |                   | DG418LDY-E3    |
| DG418LDY-T1                           |                   |                |
| DG418LDY-T1-E3                        |                   |                |
|                                       | 8-Pin MSOP        | DG417LDQ-T1-E3 |
|                                       |                   | DG418LDQ-T1-E3 |

| ORDERING INFORMATION (DG419L) |                   |                |
|-------------------------------|-------------------|----------------|
| Temp. Range                   | Package           | Part Number    |
| - 40 °C to 85 °C              | 8-Pin Narrow SOIC | DG419LDY       |
|                               |                   | DG419LDY-E3    |
|                               |                   | DG419LDY-T1    |
|                               |                   | DG419LDY-T1-E3 |
|                               | 8-Pin MSOP        | DG419LDQ-T1-E3 |

\* Pb containing terminations are not RoHS compliant, exemptions may apply

| ABSOLUTE MAXIMUM RATINGS                            |   |             |    |
|---|---|-------------|----|
| Parameter   | Limit   | Unit        |    |
| V+ to V-  | - 0.3 to 13   | V           |    |
| GND to V-   | 7   |             |    |
| V <sub>L</sub>                                      | (GND - 0.3) to (V+) + 0.3                               |             |    |
| I <sub>N</sub> , COM, NC, NO <sup>a</sup>           | - 0.3 to (V+ + 0.3)<br>or 30 mA, whichever occurs first |             |    |
| Continuous Current (Any Terminal)                   | 30  | mA          |    |
| Peak Current, S or D (Pulsed 1 ms, 10 % Duty Cycle) | 100   |             |    |
| Storage Temperature                                 | (AK, DQ, DY Suffix)                                     | - 65 to 150 | °C |
| Power Dissipation (Packages) <sup>b</sup>           | 8-Pin MSOP <sup>c</sup>                                 | 320         | mW |
|   | 8-Pin SOIC <sup>c</sup>                                 | 400         |    |
|   | 8-Pin CerDIP <sup>d</sup>                               | 600         |    |

**Notes:**

- a. Signals on NC, NO, COM, or IN exceeding V+ or V- will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- b. All leads welded or soldered to PC board.
- c. Derate 6.5 mW/°C above 25 °C.
- d. Derate 12 mW/°C above 75 °C.

| SPECIFICATIONS (Single Supply 12 V)       |  |   |                    |                   |                                      |                   |                                     |                   |      |
|---|--|---|--------------------|-------------------|--------------------------------------|-------------------|-------------------------------------|-------------------|------|
| Parameter                                 | Symbol                                       | Test Conditions<br>Unless Otherwise Specified<br>V+ = 12 V, V- = 0 V<br>V <sub>L</sub> = 5 V, V <sub>IN</sub> = 2.4 V, 0.8 V <sup>f</sup> | Temp. <sup>b</sup> | Typ. <sup>c</sup> | A Suffix Limits<br>- 55 °C to 125 °C |                   | D Suffix Limits<br>- 40 °C to 85 °C |                   | Unit |
|   |  |   |                    |                   | Min. <sup>d</sup>                    | Max. <sup>d</sup> | Min. <sup>d</sup>                   | Max. <sup>d</sup> |      |
| <b>Analog Switch</b>                      |  |   |                    |                   |                                      |                   |                                     |                   |      |
| Analog Signal Range <sup>e</sup>          | V <sub>ANALOG</sub>                          |   | Full               |                   | 0                                    | 12                | 0                                   | 12                | V    |
| On-Resistance                             | R <sub>ON</sub>                              | V+ = 10.8 V, V- = 0 V<br>I <sub>NO</sub> , I <sub>NC</sub> = 5 mA, V <sub>COM</sub> = 2 V / 9 V   | Room<br>Full       | 13                |                                      | 20<br>32          |                                     | 20<br>23.5        | Ω    |
| Switch Off Leakage Current                | I <sub>NO(off)</sub><br>I <sub>NC(off)</sub> | V <sub>COM</sub> = 1 V / 11 V<br>V <sub>NO</sub> , V <sub>NC</sub> = 11 V / 1 V   | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           | nA   |
|   | I <sub>COM(off)</sub>                        |   | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           |      |
| Channel On Leakage Current                | I <sub>COM(on)</sub>                         | V <sub>NO</sub> , V <sub>NC</sub> = V <sub>COM</sub> = 11 V / 1 V   | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           |      |
| <b>Digital Control</b>                    |  |   |                    |                   |                                      |                   |                                     |                   |      |
| Input Current                             | I <sub>INL</sub> or I <sub>INH</sub>         |   | Full               | 0.01              | - 1.5                                | 1.5               | - 1                                 | 1                 | μA   |
| <b>Dynamic Characteristics</b>            |  |   |                    |                   |                                      |                   |                                     |                   |      |
| Turn-On Time                              | t <sub>ON</sub>                              | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF<br>V <sub>NO</sub> , V <sub>NC</sub> = 5 V, see figure 2                                   | Room<br>Full       | 28                |                                      | 43<br>50          |                                     | 43<br>46          | ns   |
| Turn-Off Time                             | t <sub>OFF</sub>                             |   | Room<br>Full       | 13                |                                      | 31<br>35          |                                     | 31<br>32          |      |
| Break-Before-Make Time Delay              | t <sub>D</sub>                               | DG419L only, V <sub>NC</sub> , V <sub>NO</sub> = 5 V<br>R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                                    | Room               | 13                |                                      |                   |                                     |                   |      |
| Charge Injection <sup>e</sup>             | Q <sub>INJ</sub>                             | V <sub>g</sub> = 0 V, R <sub>g</sub> = 0 Ω, C <sub>L</sub> = 1 nF   | Room               | 1                 |                                      |                   |                                     |                   | pC   |
| Off-Isolation <sup>e</sup>                | OIRR   | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz   | Room               | - 71              |                                      |                   |                                     |                   | dB   |
| Channel-to-Channel Crosstalk <sup>e</sup> | X <sub>TALK</sub>                            |   | Room               | - 71              |                                      |                   |                                     |                   |      |
| Source Off Capacitance <sup>e</sup>       | C <sub>NO(off)</sub><br>C <sub>NC(off)</sub> | V <sub>IN</sub> = 0 or V+, f = 1 MHz  | Room               | 5                 |                                      |                   |                                     |                   | pF   |
| Channel-On Capacitance <sup>e</sup>       | C <sub>ON</sub>                              |   | Room               | 15                |                                      |                   |                                     |                   |      |
| <b>Power Supplies</b>                     |  |   |                    |                   |                                      |                   |                                     |                   |      |
| Positive Supply Current                   | I+   | V <sub>IN</sub> = 0 or V <sub>L</sub>   | Room<br>Full       | 0.02              |                                      | 1<br>7.5          |                                     | 1<br>5            | μA   |
| Negative Supply Current                   | I-   |   | Room<br>Full       | - 0.002           | - 1<br>- 7.5                         |                   | - 1<br>- 5                          |                   |      |
| Logic Supply Current                      | I <sub>L</sub>                               |   | Room<br>Full       | 0.002             |                                      | 1<br>7.5          |                                     | 1<br>5            |      |
| Ground Current                            | I <sub>GND</sub>                             |   | Room<br>Full       | - 0.002           | - 1<br>- 7.5                         |                   | - 1<br>- 5                          |                   |      |



| SPECIFICATIONS (Dual Supply $\pm 5$ V)       |                                |   |                    |                   |                                      |                   |                                     |                   |          |
|--|--------------------------------|---|--------------------|-------------------|--------------------------------------|-------------------|-------------------------------------|-------------------|----------|
| Parameter                                    | Symbol                         | Test Conditions<br>Unless Otherwise Specified<br>$V_+ = 5$ V, $V_- = -5$ V<br>$V_L = 5$ V, $V_{IN} = 2.4$ V, $0.8$ V <sup>f</sup> | Temp. <sup>b</sup> | Typ. <sup>c</sup> | A Suffix Limits<br>- 55 °C to 125 °C |                   | D Suffix Limits<br>- 40 °C to 85 °C |                   | Unit     |
|  |                                |   |                    |                   | Min. <sup>d</sup>                    | Max. <sup>d</sup> | Min. <sup>d</sup>                   | Max. <sup>d</sup> |          |
| <b>Analog Switch</b>                         |                                |   |                    |                   |                                      |                   |                                     |                   |          |
| Analog Signal Range <sup>e</sup>             | $V_{ANALOG}$                   |   | Full               |                   | - 5                                  | 5                 | - 5                                 | 5                 | V        |
| On-Resistance                                | $R_{ON}$                       | $V_+ = 5$ V, $V_- = -5$ V<br>$I_{NO}$ , $I_{NC} = 5$ mA, $V_{COM} = \pm 3.5$ V  | Room<br>Full       | 14                |                                      | 18.5<br>30        |                                     | 18.5<br>21        | $\Omega$ |
| Switch Off<br>Leakage Current <sup>a</sup>   | $I_{NO(off)}$<br>$I_{NC(off)}$ | $V_+ = 5.5$ V, $V_- = -5.5$ V<br>$V_{COM} = \pm 4.5$ V  | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           | nA       |
|  | $I_{COM(off)}$                 | $V_{NO}$ , $V_{NC} = \pm 4.5$ V   | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           |          |
| Channel On<br>Leakage Current <sup>a</sup>   | $I_{COM(on)}$                  | $V_+ = 5.5$ V, $V_- = -5.5$ V<br>$V_{NO}$ , $V_{NC} = V_{COM} = \pm 4.5$ V  | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           |          |
| <b>Digital Control</b>                       |                                |   |                    |                   |                                      |                   |                                     |                   |          |
| Input Current <sup>a</sup>                   | $I_{INL}$ or $I_{INH}$         |   | Full               | 0.05              | - 1.5                                | 1.5               | - 1                                 | 1                 | $\mu$ A  |
| <b>Dynamic Characteristics</b>               |                                |   |                    |                   |                                      |                   |                                     |                   |          |
| Turn-On Time <sup>e</sup>                    | $t_{ON}$                       | $R_L = 300 \Omega$ , $C_L = 35$ pF  | Room<br>Full       | 30                |                                      | 41<br>50          |                                     | 41<br>44          | ns       |
| Turn-Off Time <sup>e</sup>                   | $t_{OFF}$                      | $V_{NO}$ , $V_{NC} = \pm 3.5$ V, see figure 2   | Room<br>Full       | 16                |                                      | 32<br>36          |                                     | 32<br>33          |          |
| Break-Before-Make Time<br>Delay <sup>e</sup> | $t_D$                          | DG419L only, $V_{NO}$ , $V_{NC} = 3.5$ V<br>$R_L = 300 \Omega$ , $C_L = 35$ pF  | Room               | 10                |                                      |                   |                                     |                   |          |
| Transition Time                              | $t_{TRANS}$                    | $R_L = 300 \Omega$ , $C_L = 35$ pF<br>$V_{S1} = \pm 3.5$ V, $V_{S2} = \pm 3.5$ V  | Room               | 33                |                                      | 47                |                                     | 47                |          |
| Charge Injection <sup>e</sup>                | $Q_{INJ}$                      | $V_g = 0$ V, $R_g = 0 \Omega$ , $C_L = 1$ nF  | Room               | 3                 |                                      |                   |                                     |                   | pC       |
| Off-Isolation <sup>e</sup>                   | OIRR                           | $R_L = 50 \Omega$ , $C_L = 5$ pF, $f = 1$ MHz   | Room               | - 71              |                                      |                   |                                     |                   | dB       |
| Channel-to-Channel<br>Crosstalk <sup>e</sup> | $X_{TALK}$                     |   | Room               | - 76              |                                      |                   |                                     |                   |          |
| Source Off Capacitance <sup>e</sup>          | $C_{NO(off)}$<br>$C_{NC(off)}$ | $f = 1$ MHz   | Room               | 5.2               |                                      |                   |                                     |                   | pF       |
| Channel-On Capacitance <sup>e</sup>          | $C_{ON}$                       |   | Room               | 15                |                                      |                   |                                     |                   |          |
| <b>Power Supplies</b>                        |                                |   |                    |                   |                                      |                   |                                     |                   |          |
| Positive Supply Current <sup>e</sup>         | $I_+$                          | $V_{IN} = 0$ or $V_L$   | Room<br>Full       | 0.03              |                                      | 1<br>7.5          |                                     | 1<br>5            | $\mu$ A  |
| Negative Supply Current <sup>e</sup>         | $I_-$                          |   | Room<br>Full       | - 0.002           | - 1<br>- 7.5                         |                   | - 1<br>- 5                          |                   |          |
| Logic Supply Current <sup>e</sup>            | $I_L$                          |   | Room<br>Full       | 0.002             |                                      | 1<br>7.5          |                                     | 1<br>5            |          |
| Ground Current <sup>e</sup>                  | $I_{GND}$                      |   | Room<br>Full       | - 0.002           | - 1<br>- 7.5                         |                   | - 1<br>- 5                          |                   |          |

| <b>SPECIFICATIONS</b> (Single Supply 5 V)    |              |   |                    |                   |                                      |                   |                                     |                   |               |
|--|--------------|---|--------------------|-------------------|--------------------------------------|-------------------|-------------------------------------|-------------------|---------------|
| Parameter                                    | Symbol       | Test Conditions<br>Unless Otherwise Specified<br>$V_+ = 5\text{ V}$ , $V_- = 0\text{ V}$<br>$V_L = 5\text{ V}$ , $V_{IN} = 2.4\text{ V}$ , $0.8\text{ V}^f$ | Temp. <sup>b</sup> | Typ. <sup>c</sup> | A Suffix Limits<br>- 55 °C to 125 °C |                   | D Suffix Limits<br>- 40 °C to 85 °C |                   | Unit          |
|  |              |   |                    |                   | Min. <sup>d</sup>                    | Max. <sup>d</sup> | Min. <sup>d</sup>                   | Max. <sup>d</sup> |               |
| <b>Analog Switch</b>                         |              |   |                    |                   |                                      |                   |                                     |                   |               |
| Analog Signal Range <sup>e</sup>             | $V_{ANALOG}$ |   | Full               |                   |                                      | 5                 |                                     | 5                 | V             |
| On-Resistance <sup>e</sup>                   | $R_{ON}$     | $V_+ = 4.5\text{ V}$ , $I_{NO}$ , $I_{NC} = 5\text{ mA}$<br>$V_{COM} = 1\text{ V}$ , $3.5\text{ V}$   | Room<br>Full       | 26                |                                      | 36.5<br>50        |                                     | 36.5<br>40.5      | $\Omega$      |
| <b>Dynamic Characteristics</b>               |              |   |                    |                   |                                      |                   |                                     |                   |               |
| Turn-On Time <sup>e</sup>                    | $t_{ON}$     | $R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$  | Room<br>Full       | 37                |                                      | 49<br>60          |                                     | 49<br>54          | ns            |
| Turn-Off Time <sup>e</sup>                   | $t_{OFF}$    | $V_{NO}$ , $V_{NC} = 3.5\text{ V}$ , see figure 2   | Room<br>Full       | 16                |                                      | 31<br>35          |                                     | 31<br>32          |               |
| Break-Before-Make Time<br>Delay <sup>e</sup> | $t_D$        | DG419L only, $V_{NO}$ , $V_{NC} = 3.5\text{ V}$<br>$R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$   | Room               | 19                |                                      |                   |                                     |                   |               |
| Charge Injection <sup>e</sup>                | $Q_{INJ}$    | $V_g = 0\text{ V}$ , $R_g = 0\ \Omega$ , $C_L = 1\text{ nF}$  | Room               | 0.4               |                                      |                   |                                     |                   | pC            |
| <b>Power Supplies</b>                        |              |   |                    |                   |                                      |                   |                                     |                   |               |
| Positive Supply Current <sup>e</sup>         | $I_+$        | $V_{IN} = 0\text{ or }V_L$  | Room<br>Full       | 0.02              |                                      | 1<br>7.5          |                                     | 1<br>5            | $\mu\text{A}$ |
| Negative Supply Current <sup>e</sup>         | $I_-$        |   | Room<br>Full       | - 0.002           | - 1<br>- 7.5                         |                   | - 1<br>- 5                          |                   |               |
| Logic Supply Current <sup>e</sup>            | $I_L$        |   | Room<br>Full       | 0.002             |                                      | 1<br>7.5          |                                     | 1<br>5            |               |
| Ground Current <sup>e</sup>                  | $I_{GND}$    |   | Room<br>Full       | - 0.002           | - 1<br>- 7.5                         |                   | - 1<br>- 5                          |                   |               |



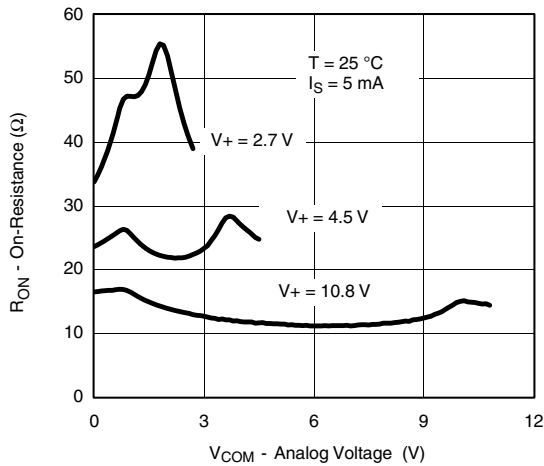
| SPECIFICATIONS (Single Supply 3 V)           |                                |   |                    |                   |                                      |                   |                                     |                   |               |
|--|--------------------------------|---|--------------------|-------------------|--------------------------------------|-------------------|-------------------------------------|-------------------|---------------|
| Parameter                                    | Symbol                         | Test Conditions<br>Unless Otherwise Specified<br>$V_+ = 3\text{ V}$ , $V_- = 0\text{ V}$<br>$V_L = 3\text{ V}$ , $V_{IN} = 2\text{ V}$ , $0.4\text{ V}^f$ | Temp. <sup>b</sup> | Typ. <sup>c</sup> | A Suffix Limits<br>- 55 °C to 125 °C |                   | D Suffix Limits<br>- 40 °C to 85 °C |                   | Unit          |
|  |                                |   |                    |                   | Min. <sup>d</sup>                    | Max. <sup>d</sup> | Min. <sup>d</sup>                   | Max. <sup>d</sup> |               |
| <b>Analog Switch</b>                         |                                |   |                    |                   |                                      |                   |                                     |                   |               |
| Analog Signal Range <sup>e</sup>             | $V_{ANALOG}$                   |   | Full               |                   | 0                                    | 3                 | 0                                   | 3                 | V             |
| On-Resistance                                | $R_{ON}$                       | $V_+ = 2.7\text{ V}$ , $V_- = 0\text{ V}$<br>$I_{NO}$ , $I_{NC} = 5\text{ mA}$ , $V_{COM} = 0.5\text{ V}$ , $2.2\text{ V}$                                | Room<br>Full       | 47                |                                      | 70<br>80          |                                     | 70<br>75          | $\Omega$      |
| Switch Off<br>Leakage Current <sup>a</sup>   | $I_{NO(off)}$<br>$I_{NC(off)}$ | $V_+ = 3.3\text{ V}$ , $V_- = 0\text{ V}$<br>$V_{COM} = 1, 2\text{ V}$ , $V_{NO}$ , $V_{NC} = 2, 1\text{ V}$  | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           | nA            |
|  | $I_{COM(off)}$                 |   | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           |               |
| Channel On<br>Leakage Current <sup>a</sup>   | $I_{COM(on)}$                  | $V_+ = 3.3\text{ V}$ , $V_- = 0\text{ V}$<br>$V_{NO}$ , $V_{NC} = V_{COM} = 1\text{ V}$ , $2\text{ V}$  | Room<br>Full       |                   | - 1<br>- 15                          | 1<br>15           | - 1<br>- 10                         | 1<br>10           |               |
| <b>Digital Control</b>                       |                                |   |                    |                   |                                      |                   |                                     |                   |               |
| Input Current <sup>a</sup>                   | $I_{INL}$ or $I_{INH}$         |   | Full               | 0.005             | - 1.5                                | 1.5               | - 1                                 | 1                 | $\mu\text{A}$ |
| <b>Dynamic Characteristics</b>               |                                |   |                    |                   |                                      |                   |                                     |                   |               |
| Turn-On Time                                 | $t_{ON}$                       | $R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$<br>$V_{NO}$ , $V_{NC} = 1.5\text{ V}$ , see figure 2   | Room<br>Full       | 65                |                                      | 75<br>95          |                                     | 75<br>85          | ns            |
| Turn-Off Time                                | $t_{OFF}$                      |   | Room<br>Full       | 26                |                                      | 41<br>45          |                                     | 41<br>43          |               |
| Break-Before-Make Time<br>Delay              | $t_D$                          | DG419L only, $V_{NO}$ , $V_{NC} = 1.5\text{ V}$<br>$R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$   | Room               | 33                |                                      |                   |                                     |                   |               |
| Charge Injection <sup>e</sup>                | $Q_{INJ}$                      | $V_g = 0\text{ V}$ , $R_g = 0\ \Omega$ , $C_L = 10\text{ nF}$   | Room               | 1                 |                                      |                   |                                     |                   | pC            |
| Off-Isolation <sup>e</sup>                   | OIRR                           | $R_L = 50\ \Omega$ , $C_L = 5\text{ pF}$ , $f = 1\text{ MHz}$   | Room               | - 71              |                                      |                   |                                     |                   | dB            |
| Channel-to-Channel<br>Crosstalk <sup>e</sup> | $X_{TALK}$                     |   | Room               | - 77              |                                      |                   |                                     |                   |               |
| Source Off Capacitance <sup>e</sup>          | $C_{NO(off)}$<br>$C_{NC(off)}$ | $f = 1\text{ MHz}$  | Room               | 5.6               |                                      |                   |                                     |                   | pF            |
| Channel On Capacitance <sup>e</sup>          | $C_{D(on)}$                    |   | Room               | 16                |                                      |                   |                                     |                   |               |

**Notes:**

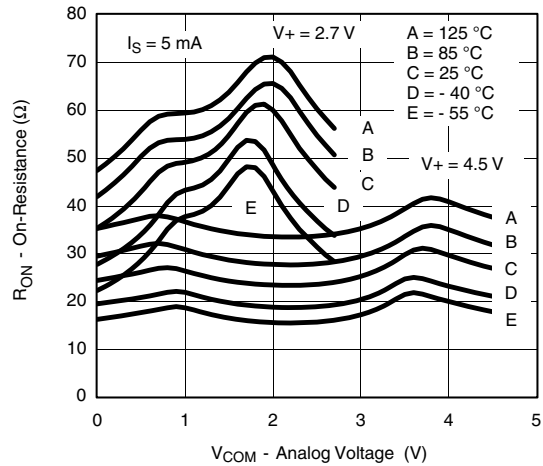
- a. Leakage parameters are guaranteed by worst case test condition and not subject to production test.
- b. Room = 25 °C, Full = as determined by the operating temperature suffix.
- c. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
- d. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- e. Guaranteed by design, not subject to production test.
- f.  $V_{IN}$  = input voltage to perform proper function.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

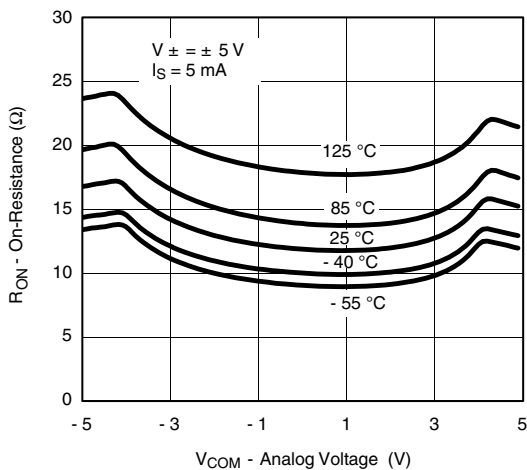
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



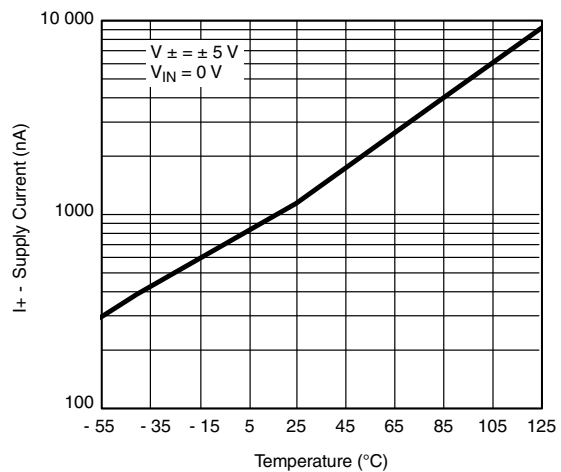
**$R_{ON}$  vs.  $V_{COM}$  and Supply Voltage**



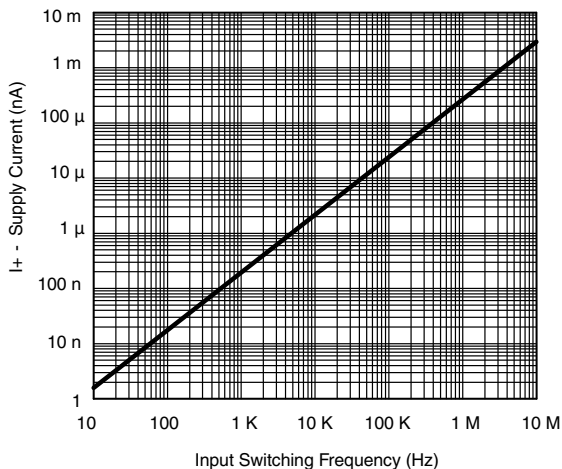
**$R_{ON}$  vs. Analog Voltage and Temperature**



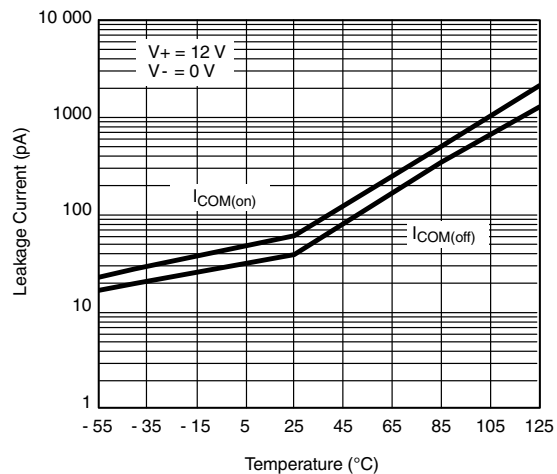
**$R_{ON}$  vs. Analog Voltage and Temperature**



**Supply Current vs. Temperature**

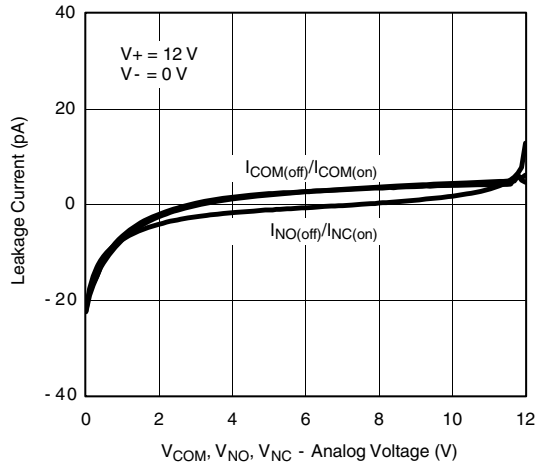


**Supply Current vs. Input Switching Frequency**

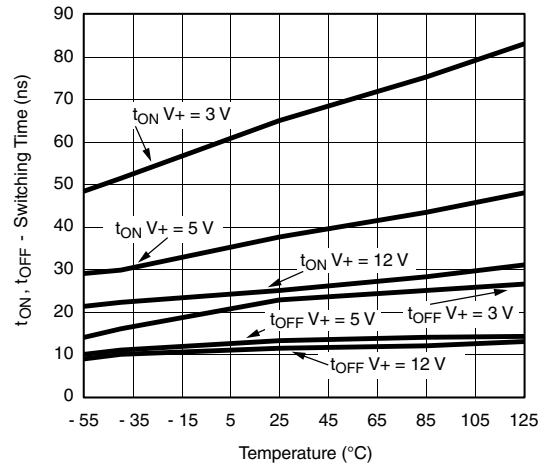


**Leakage Current vs. Temperature**

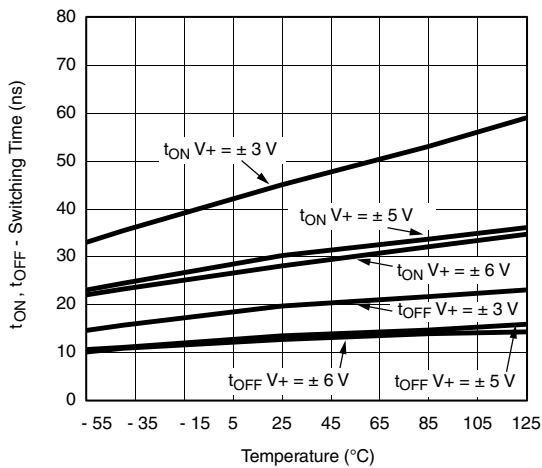
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



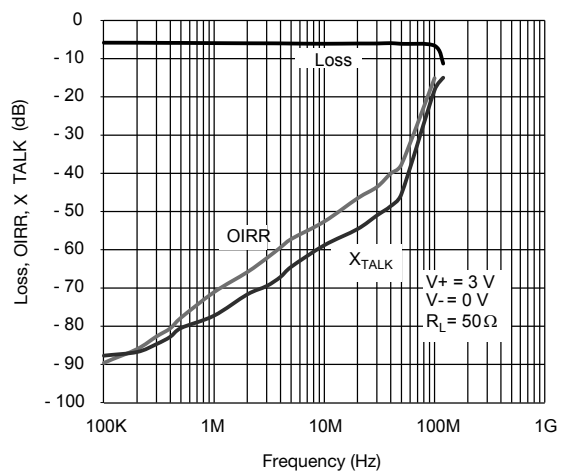
**Leakage vs. Analog Voltage**



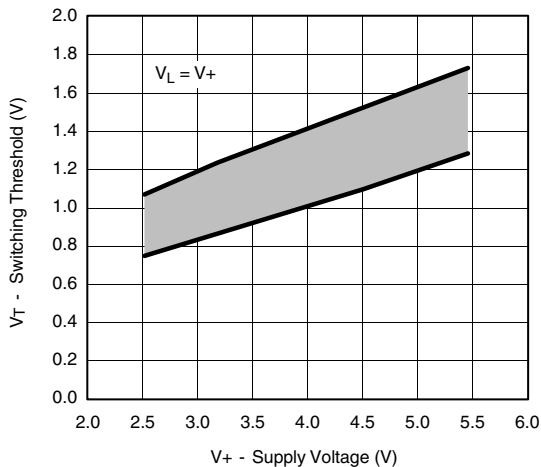
**Switching Time vs. Temperature and Single Supply Voltage**



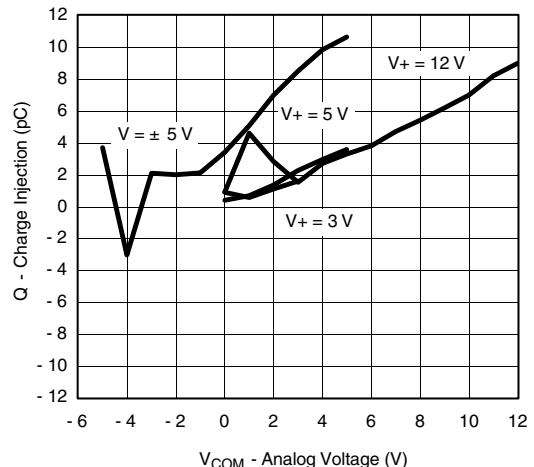
**Switching Time vs. Temperature and Dual Supply Voltage**



**Insertion Loss, Off-Isolation Crosstalk vs. Frequency**



**Switching Threshold vs. Supply Voltage**



**Charge Injection vs. Analog Voltage**

## SCHEMATIC DIAGRAM (Typical Channel)

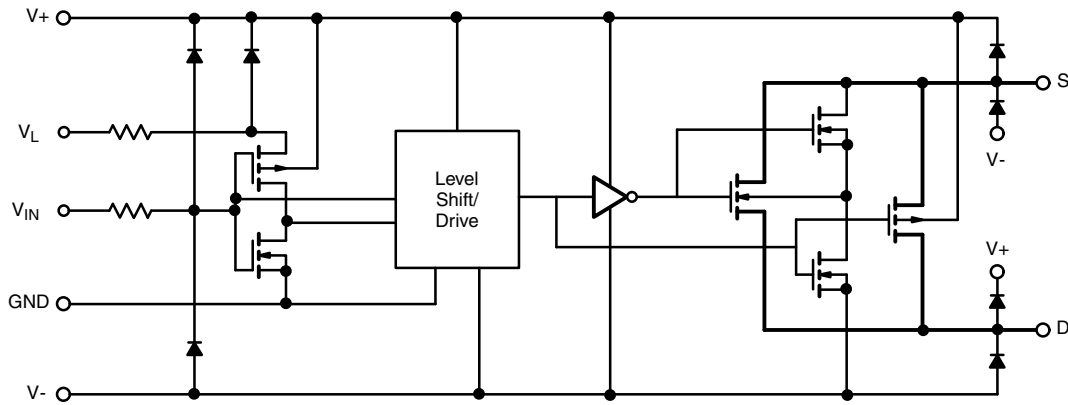
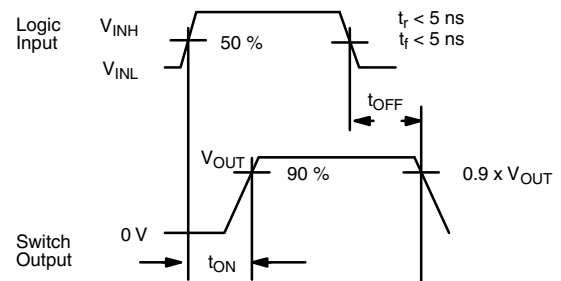
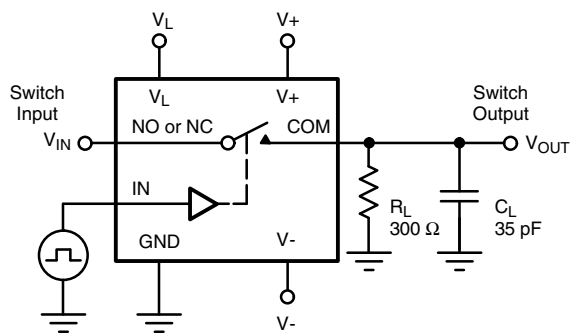


Figure 1.

## TEST CIRCUITS

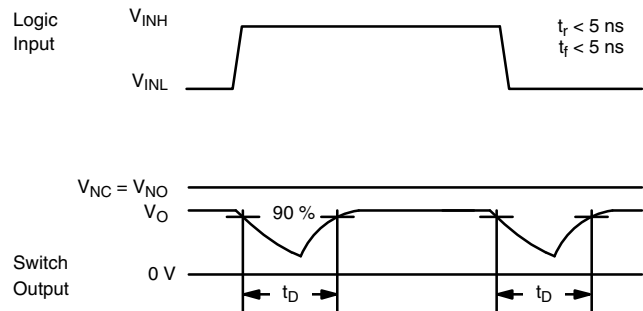
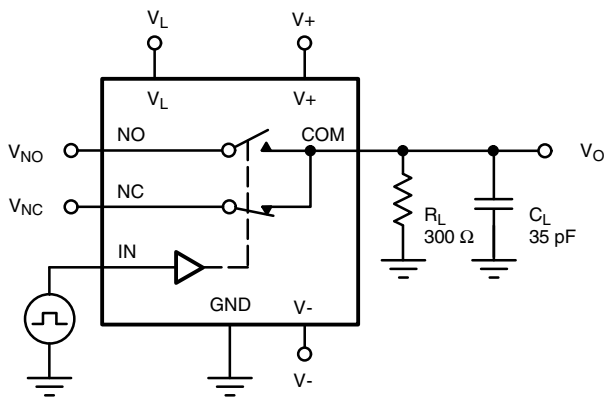


$C_L$  (includes fixture and stray capacitance)

$$V_{OUT} = V_{IN} \frac{R_L}{R_L + R_{ON}}$$

Note: Logic input waveform is inverted for switches that have the opposite logic sense control

Figure 2. Switching Time

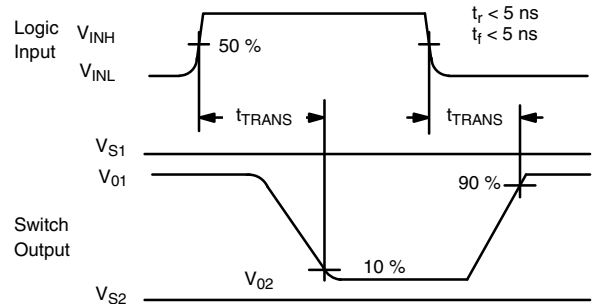
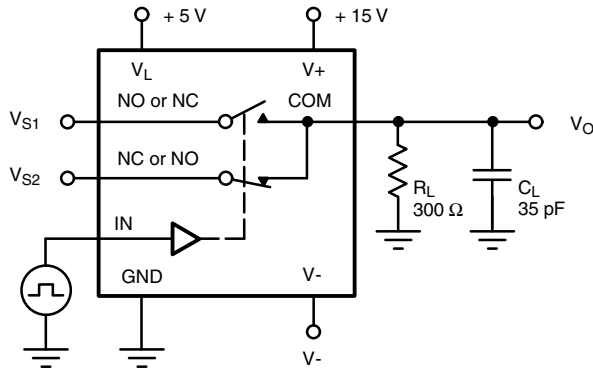


$C_L$  (includes fixture and stray capacitance)

Figure 3. Break-Before-Make (DG419L)



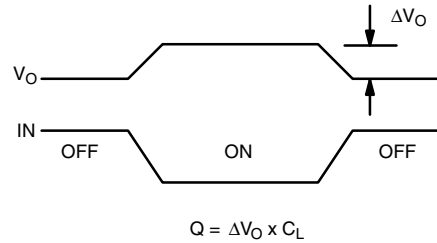
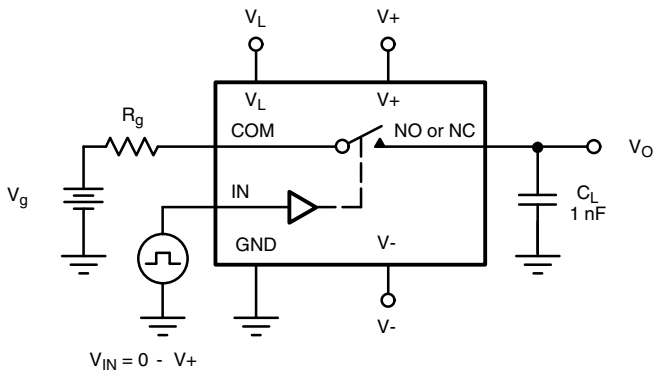
## TEST CIRCUITS



$C_L$  (includes fixture and stray capacitance)

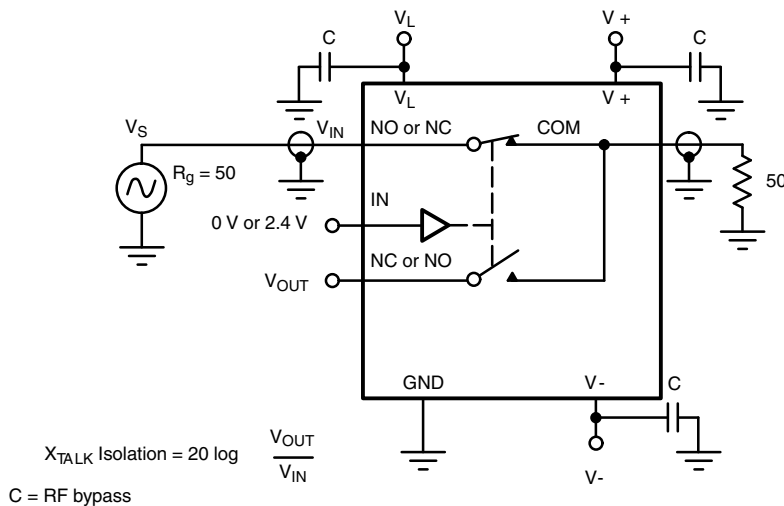
$$V_O = V_S \frac{R_L}{R_L + R_{ON}}$$

Figure 4. Transition Time (DG419L)



IN dependent on switch configuration Input polarity determined by sense of switch.

Figure 5. Charge Injection



$X_{TALK} \text{ Isolation} = 20 \log \frac{V_{OUT}}{V_{IN}}$   
 $C = \text{RF bypass}$

Figure 6. Crosstalk (DG419L)

## TEST CIRCUITS

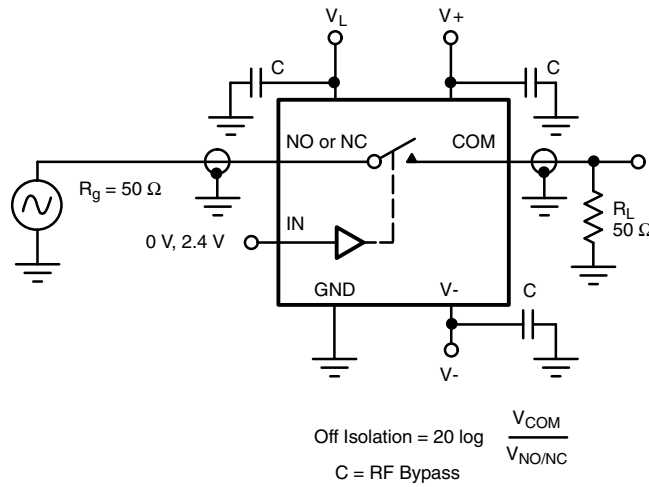


Figure 7. Off Isolation

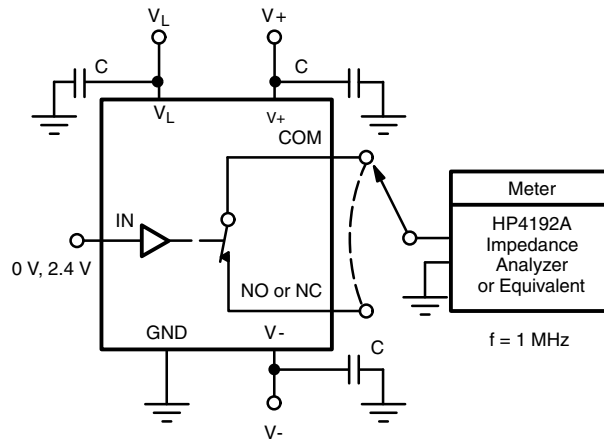


Figure 8. Source/Drain Capacitances

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## SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012

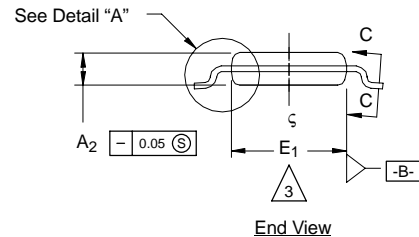
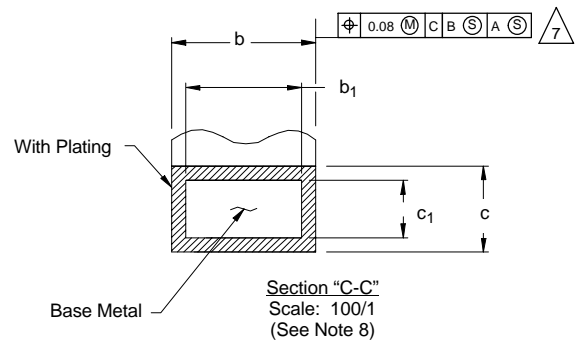
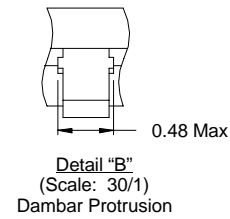
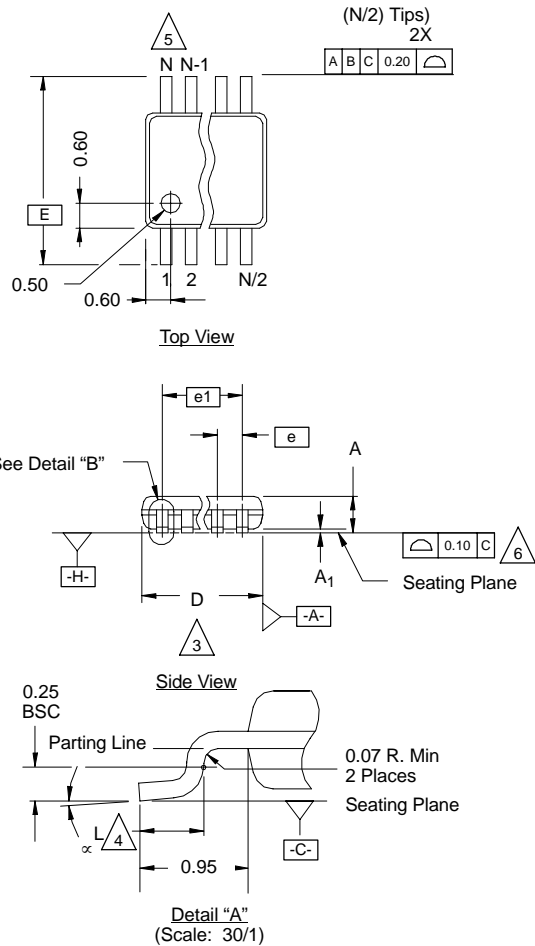


| DIM                            | MILLIMETERS |      | INCHES    |       |
|--------------------------------|-------------|------|-----------|-------|
|                                | Min         | Max  | Min       | Max   |
| A                              | 1.35        | 1.75 | 0.053     | 0.069 |
| A <sub>1</sub>                 | 0.10        | 0.20 | 0.004     | 0.008 |
| B                              | 0.35        | 0.51 | 0.014     | 0.020 |
| C                              | 0.19        | 0.25 | 0.0075    | 0.010 |
| D                              | 4.80        | 5.00 | 0.189     | 0.196 |
| E                              | 3.80        | 4.00 | 0.150     | 0.157 |
| e                              | 1.27 BSC    |      | 0.050 BSC |       |
| H                              | 5.80        | 6.20 | 0.228     | 0.244 |
| h                              | 0.25        | 0.50 | 0.010     | 0.020 |
| L                              | 0.50        | 0.93 | 0.020     | 0.037 |
| q                              | 0°          | 8°   | 0°        | 8°    |
| S                              | 0.44        | 0.64 | 0.018     | 0.026 |
| ECN: C-06527-Rev. I, 11-Sep-06 |             |      |           |       |
| DWG: 5498                      |             |      |           |       |



### MSOP: 8-LEADS

JEDEC Part Number: MO-187, (Variation AA and BA)



**NOTES:**

- Die thickness allowable is  $0.203 \pm 0.0127$ .
- Dimensioning and tolerances per ANSI.Y14.5M-1994.
- Dimensions "D" and "E<sub>1</sub>" do not include mold flash or protrusions, and are measured at Datum plane [-H-], mold flash or protrusions shall not exceed 0.15 mm per side.
- Dimension is the length of terminal for soldering to a substrate.
- Terminal positions are shown for reference only.
- Formed leads shall be planar with respect to one another within 0.10 mm at seating plane.
- The lead width dimension does not include Dambar protrusion. Allowable Dambar protrusion shall be 0.08 mm total in excess of the lead width dimension at maximum material condition. Dambar cannot be located on the lower radius or the lead foot. Minimum space between protrusions and an adjacent lead to be 0.14 mm. See detail "B" and Section "C-C".
- Section "C-C" to be determined at 0.10 mm to 0.25 mm from the lead tip.
- Controlling dimension: millimeters.
- This part is compliant with JEDEC registration MO-187, variation AA and BA.
- Datums [-A-] and [-B-] to be determined Datum plane [-H-].
- Exposed pad area in bottom side is the same as teh leadframe pad size.

**N = 8L**

| Dim                  | MILLIMETERS |      |      | Note |
|----------------------|-------------|------|------|------|
|                      | Min         | Nom  | Max  |      |
| <b>A</b>             | -           | -    | 1.10 |      |
| <b>A<sub>1</sub></b> | 0.05        | 0.10 | 0.15 |      |
| <b>A<sub>2</sub></b> | 0.75        | 0.85 | 0.95 |      |
| <b>b</b>             | 0.25        | -    | 0.38 | 8    |
| <b>b<sub>1</sub></b> | 0.25        | 0.30 | 0.33 | 8    |
| <b>c</b>             | 0.13        | -    | 0.23 |      |
| <b>c<sub>1</sub></b> | 0.13        | 0.15 | 0.18 |      |
| <b>D</b>             | 3.00 BSC    |      |      | 3    |
| <b>E</b>             | 4.90 BSC    |      |      |      |
| <b>E<sub>1</sub></b> | 2.90        | 3.00 | 3.10 | 3    |
| <b>e</b>             | 0.65 BSC    |      |      |      |
| <b>e<sub>1</sub></b> | 1.95 BSC    |      |      |      |
| <b>L</b>             | 0.40        | 0.55 | 0.70 | 4    |
| <b>N</b>             | 8           |      |      | 5    |
| $\alpha$             | 0°          | 4°   | 6°   |      |

ECN: T-02080—Rev. C, 15-Jul-02  
 DWG: 5867

## RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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