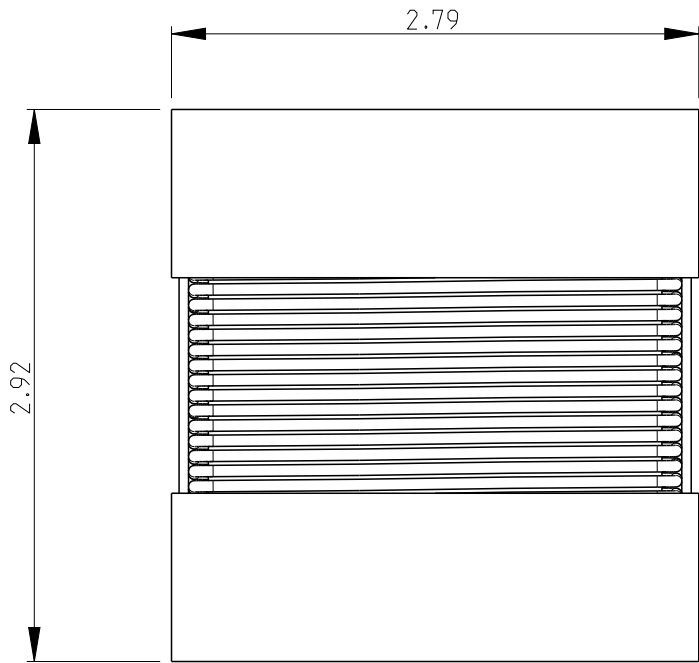


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	BA1	ECR-14-010250	16JUL2014	KR	TN

D



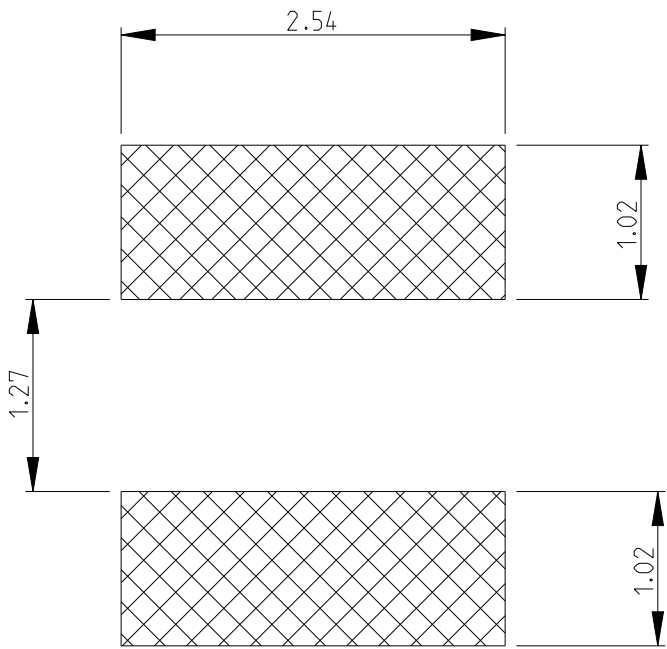
D

- 1
- 2
- TERMINAL WRAPAROUND APPROX 0.007'' (0.18mm) BOTH ENDS
- FOR FURTHER INFORMATION SEE DATA SHEET FOR 3650 SERIES-LOW INDUCTANCE, HIGH FREQUENCY CHIP INDUCTOR

C

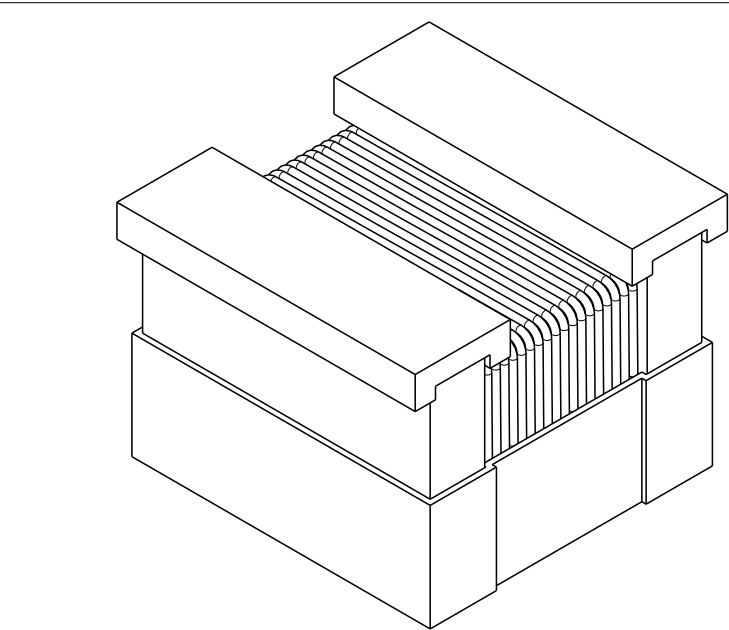
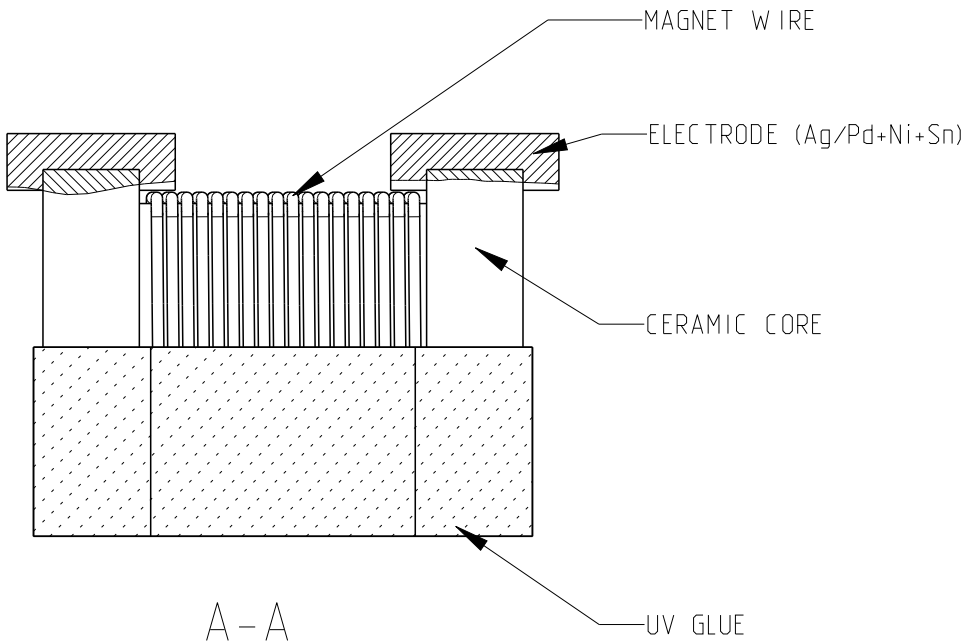
C

RECOMMENDED LAND PATTERN FOR 3650-2C SERIES



B

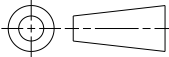
B



A

A

ROHS Compliant

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN SUBHASH.M 04-AUG-09		TE Connectivity							
		CHK ALLAN.P 04-AUG-09									
		APVD STEPHEN.P 04-AUG-09									
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.2 1 PLC ±0.1 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ±5°		NAME LOW INDUCTANCE, HIGH FREQUENCY CHIP INDUCTOR SERIES 3650 2C -							
											
MATERIAL											
-											
-											
FINISH		PRODUCT SPEC		SIZE		CAGE CODE		DRAWING NO		RESTRICTED TO	
		-		A3		00779		C-1879023		-	
		WEIGHT									
		-									
						</					

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[36502CR33JTDG](#)