

#### Is Now Part of



# ON Semiconductor®

# To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer

March 2008

### FST3125 — 4-Bit Bus Switch

#### **Features**

- $4\Omega$  Switch Connection between Two Ports
- Minimal Propagation Delay through the Switch
- Low I<sub>CC</sub>
- Zero Bounce in Flow-through Mode
- Control Inputs Compatible with TTL Level

### **Description**

Fairchild switch FST3125 provides four high-speed CMOS TTL-compatible bus switches. The low on resistance of the switch allows inputs to be connected to outputs without adding propagation delay or generating additional ground bounce noise.

The device is organized as four one1-bit switches with separate /OE inputs. When /OE is LOW, the switch is ON and port A is connected to port B. When /OE is HIGH, the switch is OPEN and a high-impedance state exists between the two ports.

### **Ordering Information**

Part Number	Operating Temperature Range	Package	Packing Method
FST3125M	-40 to 85°C	14-Lead, Small Outline Integrated Circuit (SOIC) 0.150 inch Narrow	Tube
FST3125MX	-40 to 85°C	14-Lead, Small Outline Integrated Circuit (SOIC) 0.150 inch Narrow	Tape and Reel
FST3125QSC	-40 to 85°C	16-Lead, Quarter Size Outline Package (QSOP) MO-137 0.150 inch Wide	Tube
FST3125QSCX	-40 to 85°C	16-Lead, Quarter Size Outline Package (QSOP) MO-137 0.150 inch Wide	Tape and Reel
FST3125MTC	-40 to 85°C	14-Lead, Thin Shrink Small Outline Package (TSSOP) MO-153, 4mm Wide	Tube
FST3125MTCX	-40 to 85°C	14-Lead, Thin Shrink Small Outline Package (TSSOP) MO-153, 4mm Wide	Tape and Reel

All packages are lead free per JEDEC: J-STD-020B standard.

The Fairchild switch family derives from and embodies Fairchild's proven switch technology used for several years in its 74LVX3L384 (FST3384) bus switch product.

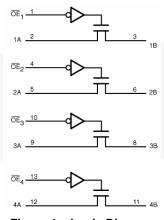


Figure 1. Logic Diagram

### **Pin Configurations**

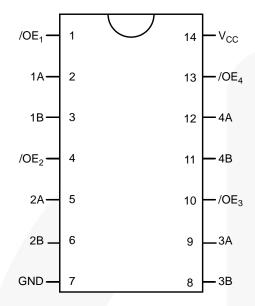


Figure 2. SOIC and TSSOP Pin Assignments

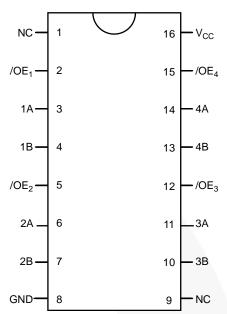


Figure 3. QSOP Pin Assignments

### **Pin Descriptions**

Pin Names	Description
/OE <sub>1</sub> , /OE <sub>2</sub> , /OE <sub>3</sub> , /OE <sub>4</sub>	Bus Switch Enables
1A, 2A, 3A, 4A	Bus A
1B, 2B, 3B, 4B	Bus B
NC	Not Connected
V <sub>cc</sub>	Supply Voltage
GND	Ground

### **Truth Table**

Inputs	Inputs/Outputs		
/OE	A, B		
LOW	A = B		
HIGH	High Impedance		

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage	-0.5	7.0	V
Vs	DC Switch Voltage	-0.5	7.0	V
V <sub>IN</sub>	DC Input Voltage <sup>(1)</sup>	-0.5	7.0	V
I <sub>IK</sub>	DC Input Current		-50	mA
I <sub>OUT</sub>	DC Output Sink Current		128	mA
I <sub>CC</sub> / I <sub>GND</sub>	DC V <sub>CC</sub> / GND Current		±100	mA
T <sub>STG</sub>	Storage Temperature Range	-65	+150	°C

#### Note:

 The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

### **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter		Min.	Max.	Unit
Vcc	Power Supply Operating		4.0	5.5	V
V <sub>IN</sub>	Input Voltage		0	5.5	V
V <sub>OUT</sub>	Output Voltage		0	5.5	V
4 4.	Input Rise and Fall Time	Switch Control Input <sup>(2)</sup>	0	5	ns/V
$t_r$ , $t_f$	Switch I/O		0	DC	115/ V
T <sub>A</sub>	Operating Temperature, Free Air		-40	+85	°C

#### Note:

2. Unused control inputs must be held HIGH or LOW. They may not float.

#### **DC Electrical Characteristics**

Typical values are at  $V_{CC} = 5.0V$  and  $T_A = 25$ °C.

Cumbal	Dozomotov Conditions		V 00	T <sub>A</sub> =-40 to +85°C			Unita
Symbol	Symbol Parameter	Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Units
V <sub>IK</sub>	Clamp Diode Voltage	I <sub>IN</sub> = -18mA	4.5			-1.2	V
V <sub>IH</sub>	High-Level Input Voltage		4.0 to 5.5	2.0			V
$V_{IL}$	Low-Level Input Voltage		4.0 to 5.5			0.8	V
I <sub>IN</sub>	Input Leakage Current	$0 \leq V_{IN} \leq 5.5$	5.5			±1.0	μΑ
l <sub>OZ</sub>	Off-state Leakage Current	$0 \le A, B \le V_{CC}$	5.5			±1.0	μΑ
		$V_{IN} = 0V$ , $I_{IN} = 64mA$	4.5		4	7	
D	Switch On Resistance <sup>(3)</sup>	V <sub>IN</sub> = 0V, I <sub>IN</sub> = 30mA	4.5		4	7	
$R_{ON}$	Switch On Resistance	$V_{IN} = 2.4V$ , $I_{IN} = 15mA$	4.5		8	15	Ω
		V <sub>IN</sub> = 2.4V, I <sub>IN</sub> = 15mA	4.0		11	20	
Icc	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$	5.5			3	μA
$\Delta I_{CC}$	Increase in I <sub>CC</sub> per Input	One Input at 3.4V, Other Inputs at V <sub>CC</sub> or GND	5.5			2.5	mA

#### Note:

3. Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the A or B pins.

#### **AC Electrical Characteristics**

 $T_A = -40 \text{ to } +85^{\circ}\text{C}, \ C_L = 50 \text{pF}, \ \text{and} \ R_U = R_D = 500 \Omega.$ 

Symbol	Parameter	Conditions	$V_{CC} = 4.5$	5 – 5.5V	V <sub>CC</sub> =	4.0V	Units	Figure
Syllibol	Farameter	Conditions	Min.	Max.	Min.	Max.	Units	riguie
t <sub>PHL</sub> , t <sub>PLH</sub>	Propagation Delay Bus-to-Bus <sup>(4)</sup>	V <sub>IN</sub> = Open		0.25		0.25	ns	Figure 4 Figure 5
t <sub>PZH</sub> ,t <sub>PZL</sub>	Output Enable Time	$V_{IN} = 7V$ for $t_{PZL}$ $V_{IN} = Open$ for $t_{PZH}$	1.0	5.0		5.5	ns	Figure 4 Figure 5
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Output Disable Time	$V_{IN} = 7V$ for $t_{PLZ}$ $V_{IN} = 0$ pen for $t_{PHZ}$	1.5	5.3		5.6	ns	Figure 4 Figure 5

#### Note

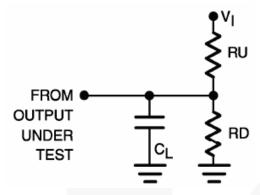
4. This parameter is guaranteed by design, but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical on resistance of the switch and the 50pF load capacitance when driven by an ideal voltage source (zero output impedance).

### Capacitance

 $T_A = +25$ °C, f = 1MHz. Capacitance is characterized, but not tested.

Symbol	Parameter	Conditions	Тур.	Units
C <sub>IN</sub>	Control Pin Input Capacitance	V <sub>CC</sub> = 5.0V	3	pF
C <sub>I/O</sub>	Input/Output Capacitance	V <sub>CC</sub> , /OE = 5.0V	2	pF

### **AC Loadings and Waveforms**



Notes: Input driven by  $50\Omega$  source terminated in  $50\Omega$ .  $C_L$  includes load and stray capacitance. Input PRR = 1.0MHz,  $t_w$  = 500ns.

Figure 4. AC Test Circuit

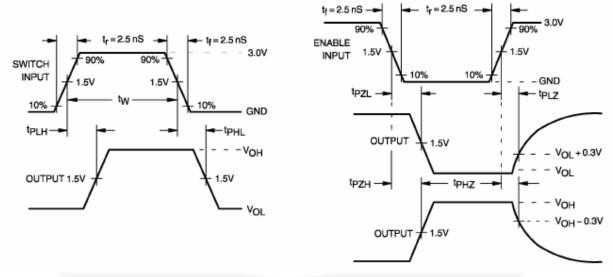


Figure 5. AC Waveforms

### **Physical Dimensions**

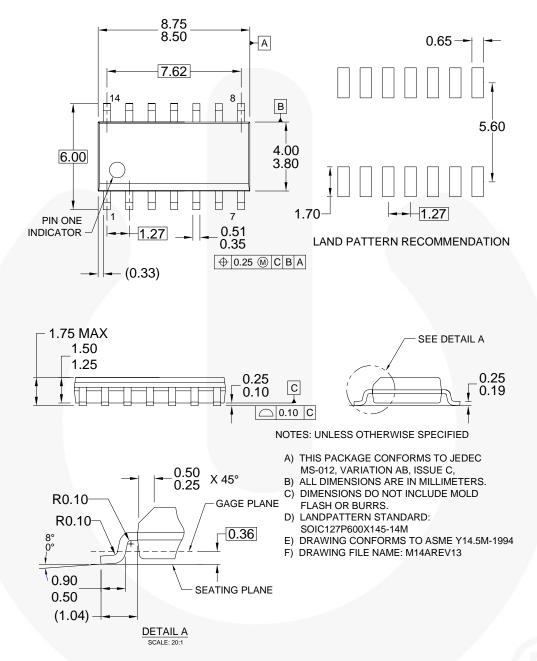


Figure 6. 14-Lead, Small-Outline Integrated Circuit (SOIC) 0.150-inch Narrow

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/

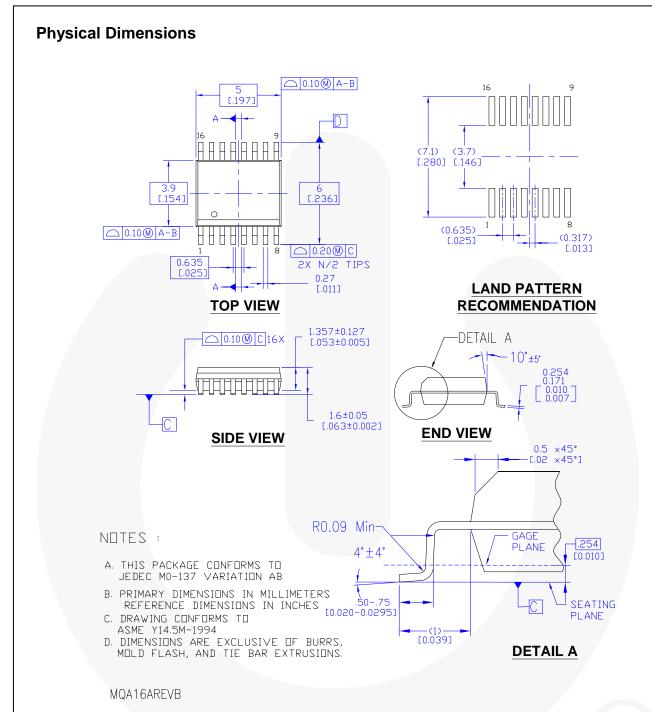


Figure 7. 16-Lead, Quarter-Size Outline Package (QSOP), MO-1370.150-inch Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/

### **Physical Dimensions**

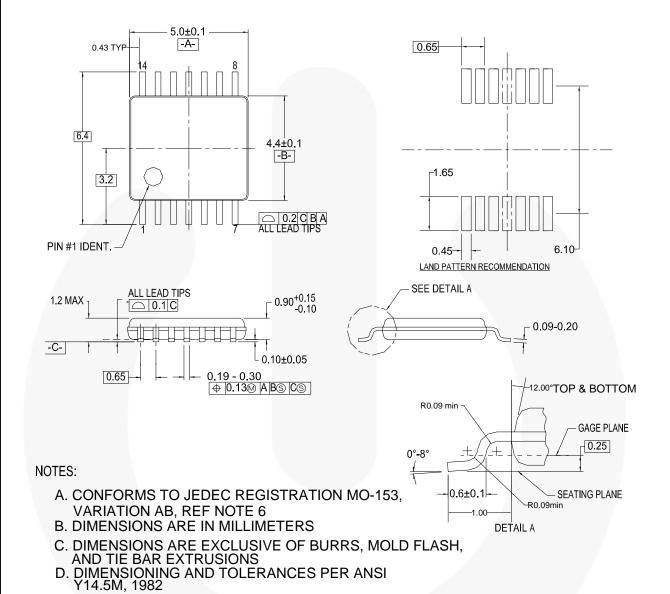


Figure 8. 14-Lead, Thin Shrink Small Outline Package (TSSOP) MO-153, 4mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/

E. LANDPATTERN STANDARD: SOP65P640X110-14M

F. DRAWING FILE NAME: MTC14REV6





#### **TRADEMARKS**

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks

ACEx® Build it Now™ CorePLUS™ CorePOWER™ CROSSVOLT™ CTL TM

Current Transfer Logic™ EcoSPARK® EfficentMax™ EZSWITCH™ \*

Fairchild<sup>®</sup> Fairchild Semiconductor® FACT Quiet Series™ FACT®

FAST® FastvCore™ FlashWriter®\*

FPS™ F-PFS™ FRFET®

Global Power Resources Green FPS™

Green FPS™e-Series™ GTO™

IntelliMAX™ ISOPLANAR™ MegaBuck™ MICROCOUPLER™ MicroFET™

MicroPak™ MillerDrive™ MotionMax™ Motion-SPM™ OPTOLOGIC® OPTOPLANAR® PDP SPM™ Power-SPM™ PowerTrench®

Programmable Active Droop™

QFET® QS<sup>TM</sup>

Quiet Series™ RapidConfigure™ Saving our world, 1mW at a time™

SmartMax ™

SMART START™

SPM® STEALTH™ SuperFET™ SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS™ SyncFET\*\* SYSTEM ® GENERAL

The Power Franchise®



TinyBoost™ TinyBuck™ TinyLogic<sup>®</sup> TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ μSerDes™

UHC' UniFET™

Ultra FRFET™ VCX<sup>TM</sup> VisualMax™

\* EZSWITCH™ and FlashWriter® are trademarks of System General Corporation, used under license by Fairchild Semiconductor.

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN, FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

#### As used herein:

- 1. Life support devices or systems are devices or systems 2. A critical component in any component of a life support, which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
  - device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	This datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev 134

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdt/Patent-Marking.pdf">www.onsemi.com/site/pdt/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

**ON Semiconductor:** 

FST3125MX FST3125QSCX FST3125MTCX FST3125M