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MMSTA63/MMSTA64

PNP SURFACE MOUNT DARLINGTON TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (MMSTA13/MMSTA14)
- Ultra-Small Surface Mount Package
- Ideal for Medium Power Amplification and Switching
- High Current Gain
- Lead Free/RoHS Compliant (Note 2)
- "Green" Device (Note 3 and 4)

Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- MMSTA63 Marking K2E, K3E, See Page 3
- MMSTA64 Marking K3E, See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.006 grams (approximate)

| SOT-323 | | | | | | | | | | |
|----------------------|------|------|--|--|--|--|--|--|--|--|
| Dim | Min | Max | | | | | | | | |
| Α | 0.25 | 0.40 | | | | | | | | |
| В | 1.15 | 1.35 | | | | | | | | |
| С | 2.00 | 2.20 | | | | | | | | |
| D 0.65 Nominal | | | | | | | | | | |
| Е | 0.30 | 0.40 | | | | | | | | |
| G | 1.20 | 1.40 | | | | | | | | |
| Н | 1.80 | 2.20 | | | | | | | | |
| J | 0.0 | 0.10 | | | | | | | | |
| κ | 0.90 | 1.00 | | | | | | | | |
| L | 0.25 | 0.40 | | | | | | | | |
| М | 0.10 | 0.18 | | | | | | | | |
| α | 0° | 8° | | | | | | | | |
| All Dimensions in mm | | | | | | | | | | |

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

| Characteristic | Symbol | Value | Unit | |
|--|-----------------------------------|-------------|------|--|
| Collector-Base Voltage | V _{CBO} | -30 | V | |
| Collector-Emitter Voltage | V _{CEO} | -30 | V | |
| Emitter-Base Voltage | V _{EBO} | -10 | V | |
| Collector Current - Continuous | Ic | -500 | mA | |
| Power Dissipation (Note 1) | Pd | 200 | mW | |
| Thermal Resistance, Junction to Ambient (Note 1) | R ₀ JA | 625 | °C/W | |
| Operating and Storage Temperature Range | T _j , T _{STG} | -55 to +150 | °C | |

Notes:

1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

2. No purposefully added lead.

3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php

 Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

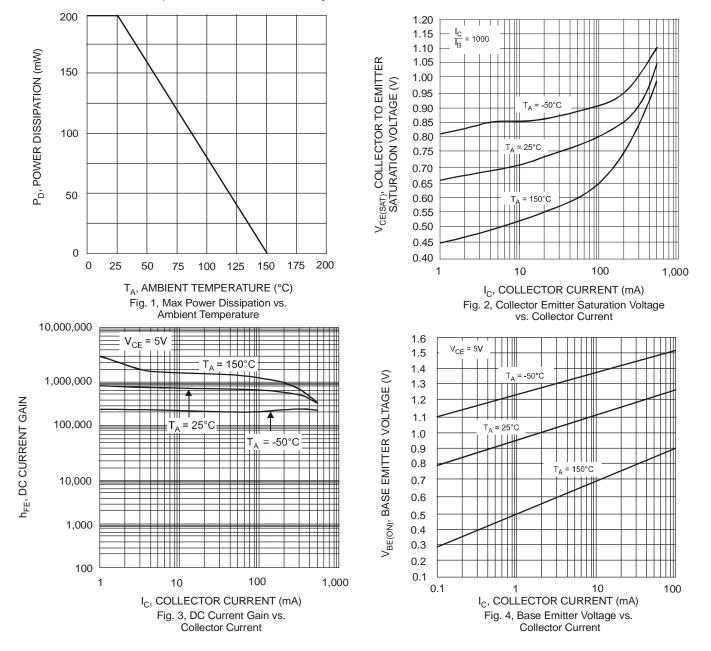


Electrical Characteristics @T_A = 25°C unless otherwise specified

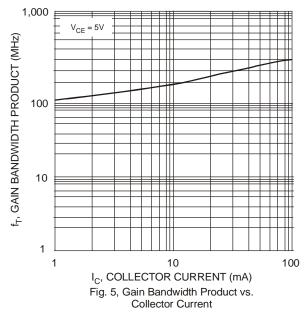
| Characteristic | Symbol | Min | Max | Unit | Test Condition | | |
|--------------------------------------|--|----------------------|-------------------------------------|------|----------------|--|--|
| OFF CHARACTERISTICS (Note 5) | | | | | | • | |
| Collector-Emitter Breakdown Voltage | | V _{(BR)CEO} | -30 | _ | V | $I_{C} = -100 \mu A V_{BE} = 0V$ | |
| Collector Cutoff Current | | I _{CBO} | _ | -100 | nA | $V_{CB} = -30V, I_E = 0$ | |
| Emitter Cutoff Current | | I _{EBO} | | -100 | nA | $V_{EB} = -10V, I_C = 0$ | |
| ON CHARACTERISTICS (Note 5) | | | | | | | |
| DC Current Gain | MMSTA63 MMSTA64 MMSTA63 MMSTA64 | h _{FE} | 5,000 10,000 10,000 20,000 | _ | _ | $ \begin{array}{ll} I_{C} = & -10 \text{mA}, \ V_{CE} = -5.0 \text{V} \\ I_{C} = & -10 \text{mA}, \ V_{CE} = -5.0 \text{V} \\ I_{C} = -100 \text{mA}, \ V_{CE} = -5.0 \text{V} \\ I_{C} = -100 \text{mA}, \ V_{CE} = -5.0 \text{V} \end{array} $ | |
| Collector-Emitter Saturation Voltage | | V _{CE(SAT)} | _ | -1.5 | V | $I_{C} = -100 \text{mA}, I_{B} = -100 \mu \text{A}$ | |
| Base- Emitter Saturation Voltage | | V _{BE(SAT)} | _ | -2.0 | V | I _C = -100mA, V _{CE} = -5.0V | |
| SMALL SIGNAL CHARACTERISTICS | | | | | | | |
| Current Gain-Bandwidth Product | | f⊤ | 125 | _ | MHz | $V_{CE} = -5.0V, I_C = -10mA, f = 100MHz$ | |



5. Short duration pulse test used to minimize self-heating effect.





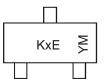


Ordering Information (Note 4 & 6)

| Device | Packaging | Shipping |
|-------------|-----------|------------------|
| MMSTA63-7-F | SOT-323 | 3000/Tape & Reel |
| MMSTA64-7-F | SOT-323 | 3000/Tape & Reel |

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



 $\begin{array}{l} \mathsf{KxE} = \mathsf{Product Type Marking Code, e.g. K2E} = \mathsf{MMSTA63}\\ \mathsf{YM} = \mathsf{Date Code Marking}\\ \mathsf{Y} = \mathsf{Year ex: N} = 2002\\ \mathsf{M} = \mathsf{Month ex: 9} = \mathsf{September} \end{array}$

| Date Code Key | | | | | | | | | | | | | | | |
|---------------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| Year | 1998 | 1999 | 2000 | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 | 2011 | 2012 |
| Code | J | к | L | М | Ν | Р | R | s | Т | U | V | W | Х | Y | Z |
| | | | | | | | | | | | | | | | |
| Month | Jan | Fe | b I | Mar | Apr | Мау | Ju | n | Jul | Aug | Sep | Oc | t I | Nov | Dec |
| Code | 1 | 2 | | 3 | 4 | 5 | 6 | | 7 | 8 | 9 | 0 | | Ν | D |

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