Hyper Fast Rectifier, 8 A FRED Pt[®]



DPAK (TO-252AA
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PRIMARY CHARACTERISTICS								
Package	DPAK (TO-252AA)							
I _{F(AV)}	8 A							
V _R	600 V							
V _F at I _F	1.3 V							
t _{rr} (typ.)	18 ns							
T _J max.	175 °C							
Circuit configuration	Single die							

FEATURES

- Hyper fast recovery time, reduced Q_{rr} and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM/CCM operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyper fast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Peak repetitive reverse voltage	V _{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 143 °C	8							
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	90	А						
Peak repetitive forward current	I _{FM}	$T_{C} = 143 \text{ °C}, f = 20 \text{ kHz}, d = 50 \text{ \%}$	16							
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C						

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-				
Forward voltage	V _F	I _F = 8 A	-	2.0	2.4	V			
Forward voltage		I _F = 8 A, T _J = 150 °C	-	1.3	1.8				
Povoroo lookago ourront		V _R = V _R rated	-	-	50	μA			
Reverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μΑ			
Junction capacitance	CT	V _R = 600 V	-	8	-	pF			
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH			

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RoHS

COMPLIANT

HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 50$	0 Α/μs, V _R = 30 V		21				
	÷	$I_F = 1 \text{ A}, dI_F/dt = 10$	-	18	22	ns			
	t _{rr}	T _J = 25 °C		-	25	-	A		
		T _J = 125 °C	I _F = 8 A dI _F /dt = 200 A/µs	-	34	-			
Poak rocovory ourrent	I _{RRM}	T _J = 25 °C		-	3.3	-			
Peak recovery current		$T_{\rm J} = 125 ^{\circ}{\rm C}$ $V_{\rm R} = 390 ^{\circ}{\rm V}$		-	4.8	-	A		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	39	-			
		T _J = 125 °C		-	90	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C			
Thermal resistance, junction to case per leg	R _{thJC}		-	1.8	2.2	°C/W			
				0.3		g			
Approximate weight				0.01		oz.			
Marking device		Case style TO-252AA (D-PAK)	8EWH06FN						

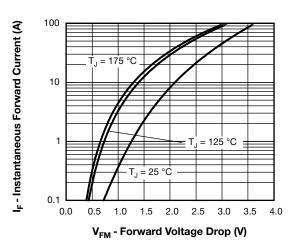


Fig. 1 - Typical Forward Voltage Drop Characteristics

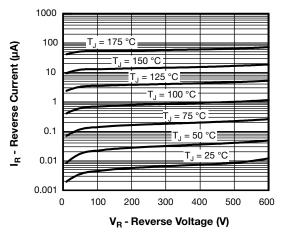


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



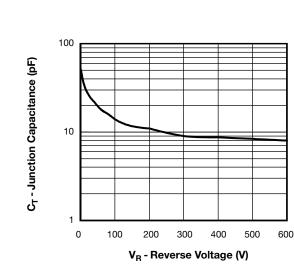


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

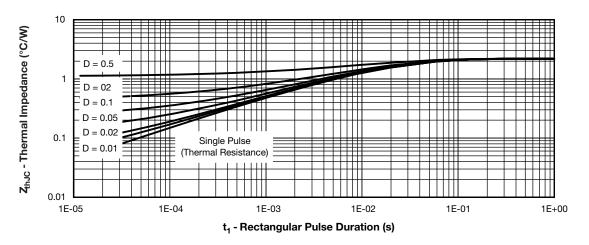
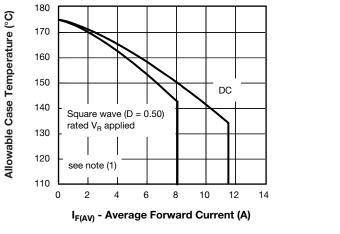
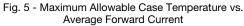


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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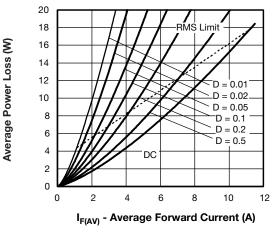


Fig. 6 - Forward Power Loss Characteristics

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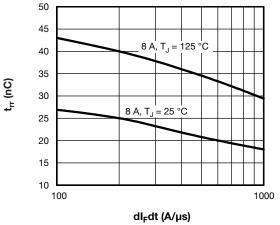
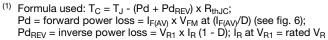


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





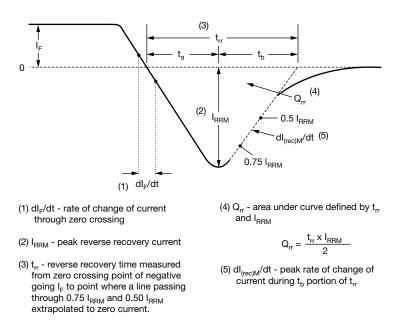
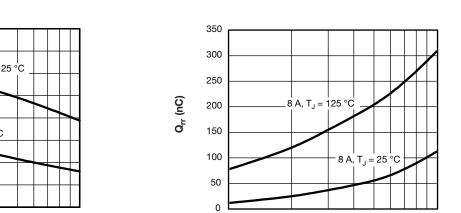


Fig. 9 - Reverse Recovery Waveform and Definitions



100

dl_Fdt (A/µs) Fig. 8 - Typical Stored Charge vs. dl_F/dt

VS-8EWH06FN-M3

Vishay Semiconductors

1000

ORDERING INFORMATION TABLE

Device code	VS-	8	E	w	н	06	FN	TRL	-M3
		(2)	(3)	(4)	(5)	(6)	(7)		(9)
	\bigcirc	(2)	\bigcirc	4	\bigcirc	\bigcirc	()	0	9
	1	- Visl	hay Sen	nicondu	ctors pro	oduct			
	2	- Cur	rent rati	ng (8 =	8 A)				
	3	- Circ	cuit conf	iguratio	า:				
		E =	single o	diode					
	4	- Pac	kage id	entifier:					
		W =	= D-PAK	K					
	5	- H=	hyperfa	ast recov	/ery				
	6	- Vol	tage rati	ing (06 =	= 600 V)				
	7	- FN	= TO-25	52AA					
	8	• N	one = tu	ibe					
		• TI	R = tape	e and re	el				
		• TI	RL = tap	be and r	eel (left	orienteo	d)		
		• TI	RR = tap	pe and r	eel (righ	nt orient	ed)		
	9	- Env	vironmer	ntal digit	:				
		-M3	3 = Halo	gen-free	e, RoHS	-compli	ant, and	d termin	ations l

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-8EWH06FN-M3	75	3000	Antistatic plastic tube							
VS-8EWH06FNTR-M3	2000	2000	13" diameter reel							
VS-8EWH06FNTRL-M3	3000	3000	13" diameter reel							
VS-8EWH06FNTRR-M3	3000	3000	13" diameter reel							

LINKS TO RELATED DOCUMENTS									
Dimensions	www.vishay.com/doc?95627								
Part marking information	www.vishay.com/doc?95176								
Packaging information	www.vishay.com/doc?95033								
SPICE model	www.vishay.com/doc?96114								







D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTED	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA



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