

BTA216-600BT Triacs high commutation Rev. 3 – 20 September 2018

**Product data sheet** 

## 1. Product profile

### 1.1 General description

Passivated high commutation triac in a plastic envelope. Featuring high maximum junction temperature and high commutation capability. Intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. This device will commutate the full rated RMS current at the maximum rated junction temperature, without the aid of a snubber.

### **1.2 Features and benefits**

High maximum junction temperature
 High commutation capability

### 1.3 Quick reference data

- $\bullet V_{DRM} \le 600 \ V$
- $\blacksquare \quad I_{GT} \leq 50 \ mA$
- T<sub>j</sub> ≤ 150 °C

■ I<sub>T(RMS)</sub> ≤ 16 A

- I<sub>TSM</sub> ≤ 140 A
- dl<sub>com</sub>/dt = 18 A/ms

### 2. Pinning information

Table 1:	Pinning		
Pin	Description	Simplified outline	Symbol
1	main terminal 1 (T1)		N I
2	main terminal 2 (T2)	mb	T2-T1
3	gate (G)	P ⊂ ۲	Sym051
mb	mounting base		
		SOT78 (TO-220AB)	

[1] Connected to main terminal 2 (T2)

### 3. Ordering information

Table 2: Ordering information						
Type number	Package	ge				
	Name	Description	Version			
BTA216-600BT	TO-220AB	plastic single-ended package; heatsink mounted; 3 leads; 1 mounting hole	SOT78			

# 4. Limiting values

#### Table 3:Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

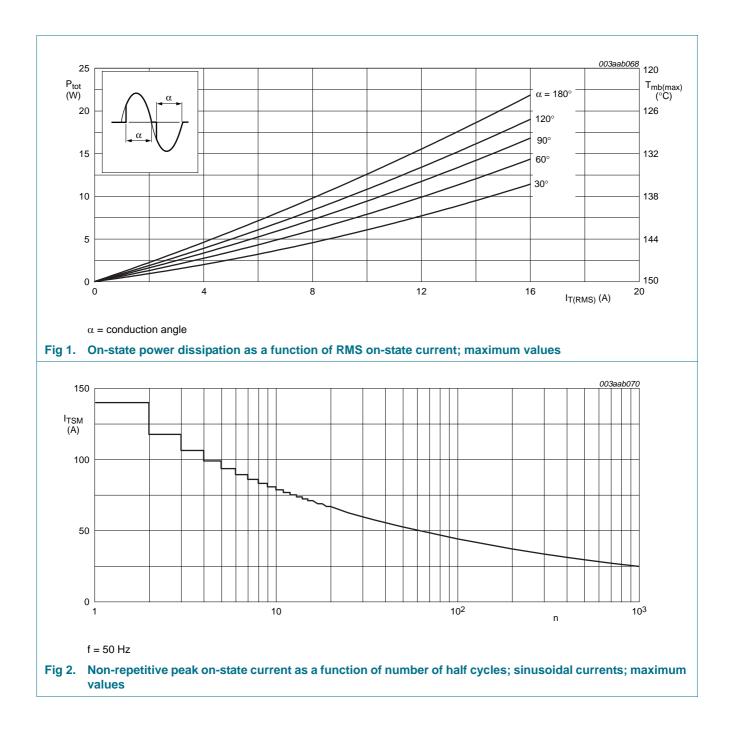
Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		<u>[1]</u> _	600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 124 °C; see <u>Figure 4</u> and <u>5</u>	-	16	А
I <sub>TSM</sub>	non-repetitive peak on-state current	full sine wave; T <sub>j</sub> = 25 °C prior to surge; see <u>Figure 2</u> and <u>3</u>			
		t = 20 ms	-	140	А
		t = 16.7 ms	-	150	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 10 ms	-	98	A <sup>2</sup> s
dl <sub>T</sub> /dt	rate of rise of on-state current	$I_{TM}$ = 20 A; $I_G$ = 0.2 A; $dI_G/dt$ = 0.2 A/ $\mu s$	-	100	A/μs
I <sub>GM</sub>	peak gate current		-	2	А
$V_{GM}$	peak gate voltage		-	5	V
P <sub>GM</sub>	peak gate power		-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.5	W
T <sub>stg</sub>	storage temperature		-40	+150	°C
Tj	junction temperature		-	150	°C

[1] Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/µs.

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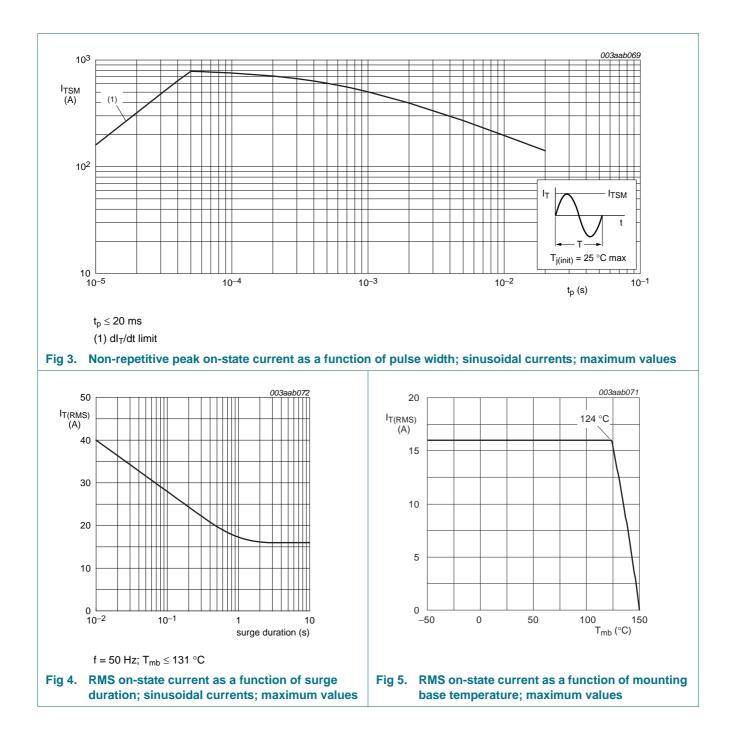
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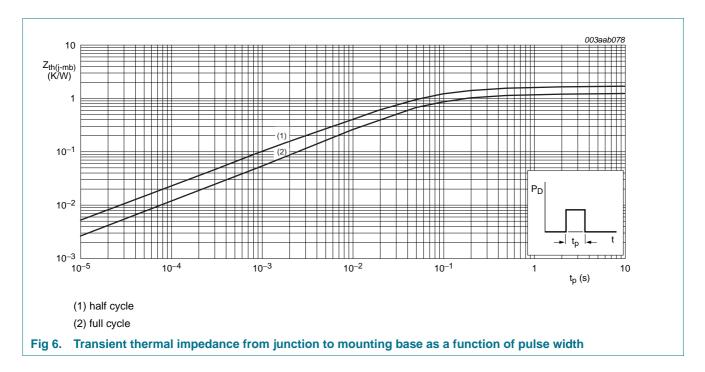
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## 5. Thermal characteristics

#### Table 4: Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	full cycle; see Figure 6	-	-	1.2	K/W
		half cycle; see Figure 6	-	-	1.7	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	60	-	K/W





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## 6. Static characteristics

### Table 5: Static characteristics

 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ see } \frac{\text{Figure 8}}{100000000000000000000000000000000000$	[1]				
		T2+ G+		2	18	50	mA
		T2+ G-		2	21	50	mA
		T2– G–		2	34	50	mA
IL	latching current	$V_D = 12 \text{ V}; \text{ I}_{GT} = 0.1 \text{ A}; \text{ see } \frac{\text{Figure } 10}{100000000000000000000000000000000$					
		T2+ G+		-	31	60	mA
		T2+ G–		-	34	90	mA
		T2– G–		-	30	60	mA
I <sub>H</sub>	holding current	$V_D = 12 \text{ V}; \text{ I}_{GT} = 0.1 \text{ A}; \text{ see } \frac{\text{Figure } 11}{100000000000000000000000000000000$		-	31	60	mA
VT	on-state voltage	I <sub>T</sub> = 20 A; see <u>Figure 9</u>		-	1.2	1.5	V
V <sub>GT</sub>	gate trigger voltage	$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ see } \frac{\text{Figure 7}}{100000000000000000000000000000000000$		-	0.7	1.5	V
		$V_D = 400 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T}_j = 150 \ ^\circ\text{C}$		0.25	0.4	-	V
I <sub>D</sub>	off-state current	$V_D = V_{DRM(max)}; T_j = 150 \ ^{\circ}C$		-	0.5	3	mA

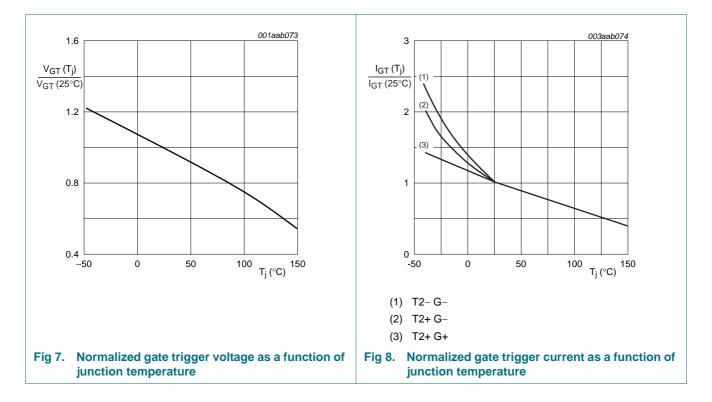
[1] Device does not trigger in the T2– G+ quadrant.

## 7. Dynamic characteristics

#### Table 6: Dynamic characteristics

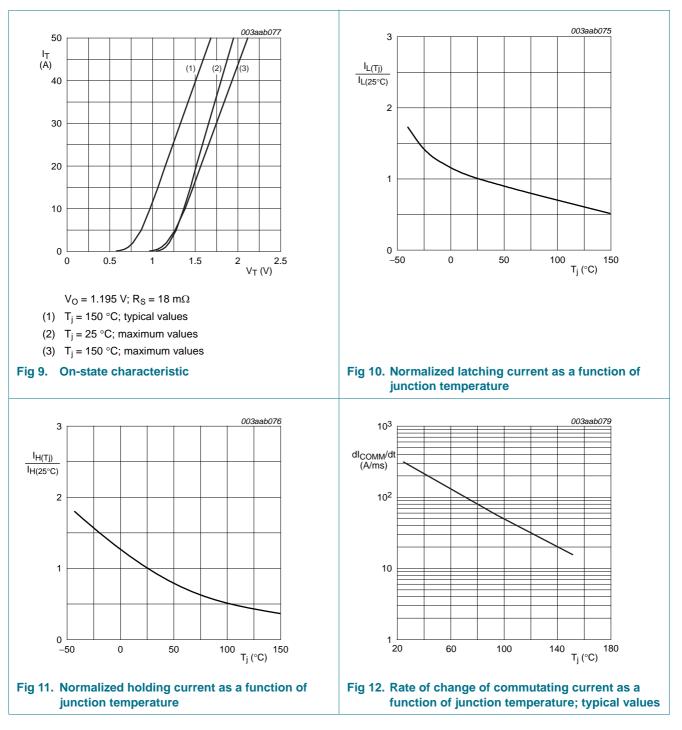
 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

,	,					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM} = 0.67 V_{DRM(max)}$ ; $T_j = 150 \text{ °C}$ ; exponential waveform; gate open circuit	500	1500	-	V/µs
dl <sub>com</sub> /dt	rate of change of commutating current	$V_{DM}$ = 400 V; T <sub>j</sub> = 150 °C; I <sub>T(RMS)</sub> = 16 A; without snubber; gate open circuit; see <u>Figure 12</u>	9	18	-	A/ms
t <sub>gt</sub>	gate-controlled turn-on time	$\begin{split} I_{TM} &= 20 \text{ A};  V_D = V_{DRM(max)};  I_G = 0.1 \text{ A}; \\ dI_G/dt &= 5  A/\mu s \end{split}$	-	2	-	μS



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## 8. Package information

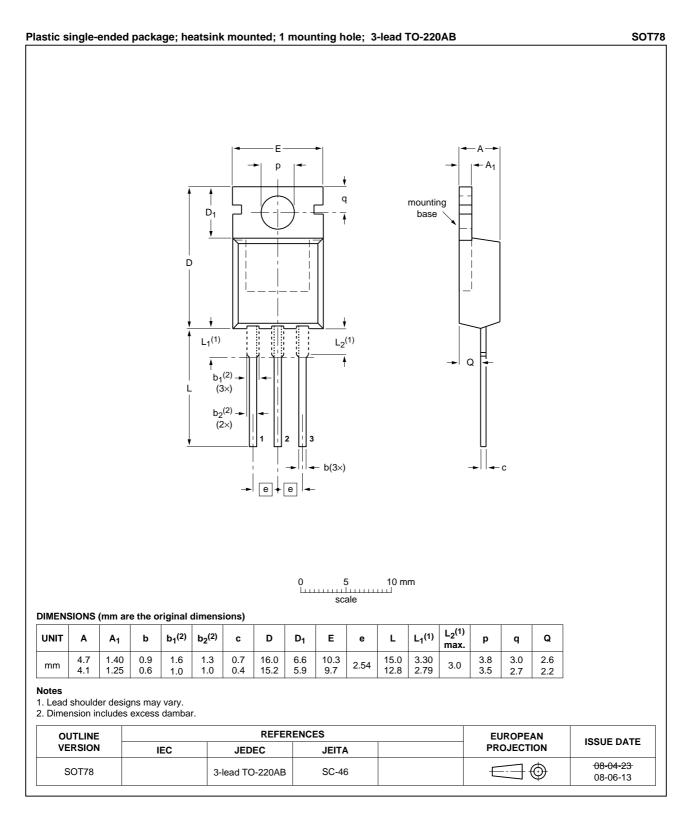
Plastic meets UL94 V-0 at <sup>1</sup>/<sub>8</sub> inch.

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### 9. Package outline



#### Fig 13. Package outline SOT78 (TO-220AB)

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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