SQD35N05-26L



Vishay Siliconix

Automotive N-Channel 55 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	55				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.020				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.026				
I _D (A)	30				
Configuration	Single				



FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- AEC-Q101 Qualified^d
- Compliant to RoHS Directive 2002/95/EC



Top View	N-Channel MOSFET		
ORDERING INFORMATIO	DN		
Package		TO-252	
Lead (Pb)-free and Halogen-free		SQD35N05-26L-GE3	

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \ ^{\circ}C$, unless	otherwise noted	l)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	55		
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current	T _C = 25 °C ^a		30		
	T _C = 125 °C	I _D	19		
Continuous Source Current (Diode Conduction) ^a		I _S	30	А	
Pulsed Drain Current ^b		I _{DM}	120		
Single Pulse Avalanche Energy	L = 0.1 mH	I _{AS}	20		
Single Pulse Avalanche Current		E _{AS}	20	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	D	50	w	
	T _C = 125 °C	P _D	16		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	60	°C/W	
inction-to-Case (Drain)		R _{thJC}	3	0/10	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu\text{s},~\text{duty}~\text{cycle} \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static	-							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		55	-	-	v	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2	2.5		
Gate-Source Leakage	I _{GSS}	V _{DS} =	0 V, V_{GS} = ± 20 V	-	-	± 100	nA	
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	$V_{DS} = 55 V$	-	-	1		
	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = 55 \text{ V}, \text{ T}_{\text{J}} = 125 ^{\circ}\text{C}$	-	-	50	μA	
		$V_{GS} = 0 V$	V_{DS} = 55 V, T_J = 175 °C	-	-	250		
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = 5 V$	$V_{DS} \geq 5 \ V$	30	-	-	А	
Drain-Source On-State Resistance ^a		$V_{GS} = 10 V$	I _D = 20 A	-	0.016	0.020	- Ω	
	Brach	$V_{GS} = 10 V$	$I_D = 20 \text{ A}, T_J = 125 ^\circ\text{C}$	-	-	0.035		
	R _{DS(on)}	$V_{GS} = 10 V$	$I_D = 20 \text{ A}, \text{T}_\text{J} = 175 \ ^\circ\text{C}$	-	-	0.043		
		$V_{GS} = 4.5 V$	I _D = 15 A	-	0.021	0.026		
Forward Transconductance ^b	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		-	34	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = 25 V, f = 1 MHz	-	938	1175	pF nC	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	203	255		
Reverse Transfer Capacitance	C _{rss}			-	86	110		
Total Gate Charge ^c	Qg		V _{DS} = 25 V, I _D = 35 A	-	12	18		
Gate-Source Charge ^c	Q _{gs}	$V_{GS} = 5 V$		-	4.1	-		
Gate-Drain Charge ^c	Q _{gd}			-	4.8	-		
Gate Resistance	Rg	f = 1 MHz		1.3	2.6	4	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	7	11		
Rise Time ^c	t _r	$\label{eq:VDD} \begin{array}{l} V_{DD} = 25 \; V, \; R_{L} = 0.71 \; \Omega \\ I_{D} \cong 35 \; A, \; V_{GEN} = 10 \; V, \; R_{g} = 1 \; \Omega \end{array}$		-	10	15	- ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	18	27		
Fall Time ^c	t _f			-	5	8		
Source-Drain Diode Ratings and Chara	acteristics ^b	•		•	•			
Pulsed Current ^a	I _{SM}			-	-	120	А	
	V _{SD}	$I_{\rm F} = 80$ A, $V_{\rm GS} = 0$ V			1.2		V	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)







Source Drain Diode Forward Voltage

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0.001

0.0

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TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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