



### Description

- Suitable for real time clock applications  
32.768kHz output crystal oscillators  
Ceramic package with a seam sealed metal lid, hermetically sealed
- Model CFPS-107
- Model Issue number 4

### Frequency Parameters

- Frequency 32.7680kHz
- Frequency Stability  $\pm 50.00\text{ppm}$
- Operating Temperature Range  $-40.00$  to  $85.00^\circ\text{C}$
- Ageing  $\pm 3\text{ppm}$  max per year

### Electrical Parameters

- Supply Voltage  $1.8\text{V} \pm 5\%$
- Current Draw  $1.500\text{mA}$

### Output Details

- Output Compatibility CMOS
- Drive Capability  $15\text{pF}$  max
- Rise and Fall Time  $50.0\text{ns}$  max
- Duty Cycle  $40/60\%$
- Logic '1' to pad 1 enables oscillator output  
Logic '0' to pad 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state  
No connection to pad 1 enables oscillator output

### Environmental Parameters

- Storage Temperature Range:  $-55$  to  $125^\circ\text{C}$
- Shock: MIL-STD-883F, Method 2002.4:  $1500\text{G}$ ,  $0.5\text{ms}$ , 3 times in each of 3 mutually perpendicular planes.
- Vibration: MIL-STD-883F, Method 2007.3:  $20\text{G}$  ( $20\text{Hz}$ - $2000\text{Hz}$ ),  $1.52\text{mm}$  amplitude

### Manufacturing Details

- RoHS Terminations NiAu
- RoHS Reflow Temp  $260^\circ\text{C}$  10s

### Compliance

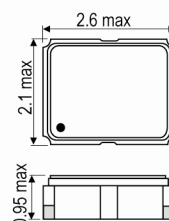
- RoHS Status (2011/65/EU) Compliant
- REACH Status Compliant
- MSL Rating (JDEC-STD-033): Not Applicable

### Packaging Details

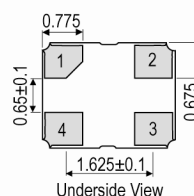
- Pack Style: Reel Tape & reel in accordance with EIA-481-D  
Pack Size: 1,000
- Alternative packing option available



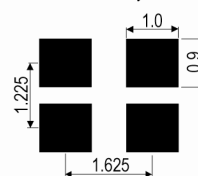
### Outline (mm)



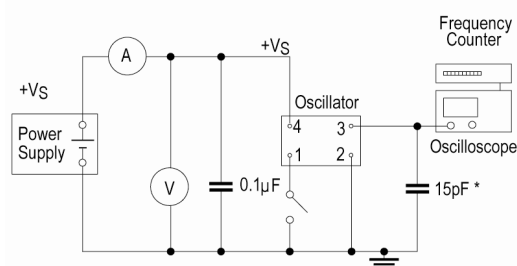
Pad Connections  
1. Enable/Disable  
2. GND  
3. Output  
4. +Vs



### Solder Pad Layout



### Test Circuit



\* Inclusive of jigging and equipment capacitance

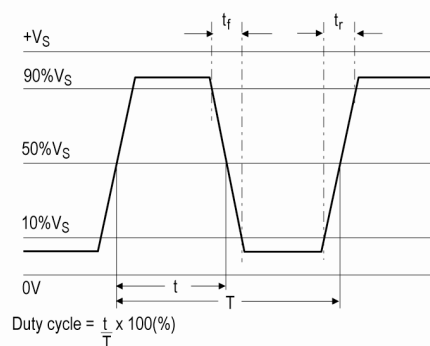
### Sales Office Contact Details:

UK: +44 (0)1460 270200  
Germany: 0800 1808 443

France: 0800 901 383  
USA: +1.760.318.2824

Email: [info@iqdfrequencyproducts.com](mailto:info@iqdfrequencyproducts.com)  
Web: [www.iqdfrequencyproducts.com](http://www.iqdfrequencyproducts.com)

## Wave Form



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France: 0800 901 383  
USA: +1.760.318.2824

Email: [info@iqdfrequencyproducts.com](mailto:info@iqdfrequencyproducts.com)  
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