

Features

- RoHS compliant*
- 2/4 isolated resistors in an 0404/0804 size package
- E24 series from 10 ohms to 1 megohm
- Convex termination style
- Resistance tolerance ±5 %
- Suitable for all types of soldering processes

Model CAY10 - Chip Resistor Array

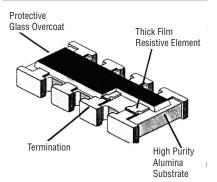
Specifications

Requirement	Characteristics	Test Method
Short Time Overload	±2 %	Rated Voltage X 2.5, 5 seconds
Soldering Heat	±1 %	260 °C ±5 °C, 10 seconds ±1 second
Temperature Cycling (5)	±1 %	125 °C (30 minutes) - normal (15 minutes) -30 °C (30 minutes) - normal (15 minutes)
Moisture Load Life	±3 %	1000 hours
Load Life	±3 %	1000 hours

Characteristics

Characteristics	CAY10-xxxJ2	CAY10-xxxJ4	
Number of Elements (Isolated)	2	4	
Power Rating @ 70 °C per Resistor	0.0625 W	0.0625 W	
Package Power Rating @ 70 °C	0.125 W	0.250 W	
Resistance Tolerance	±5 %		
Resistance Range (E24) plus Zero-ohm Jumper	3 ohms - 1 megohm		
T.C.R.	3 ohms ~ 9.1 ohms: ±500 ppm/°C 10 ohms ~ 1 megohm: ±250 ppm/°C		
Max. Overload Voltage	50 V		
Max. Working Voltage	25 V		
Operating Temp. Range	-55 °C to +125 °C		
Rating Temperature	+70 °C		
Packaging	10,000 pieces per reel		

Characteristics



Derating Curve 70 YC 125 YC 100 80 🛛 Rated Load (%) 60 40 20 0 40 60 80 100 120 140 160 -55 Ambient Temp. (°C)

Typical Part Marking

None on part. Label on reel will include part number.

How To Order

CA Y 10 - 103 J 4 LF
Chip Arrays
Туре
• Y = Convex
Model
• 10 = 04 Package Width
Resistance Code • <10 ohms: "R" represents decimal point (example: 4R7 = 4.7 ohms).
 ≥10 ohms: First two digits are signifi- cant, third digit represents number of zeros to follow (example: 474 = 470k ohms
• 000 = Zero-ohm Jumper
Resistance Tolerance $$ • J = $\pm 5 \%$
Resistors
Terminations
• LE - Tip plated (PoUS compliant)

Paper tape on reel for automatic placement

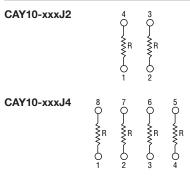
LF = Tin-plated (RoHS compliant)

For Standard Values Used in Capacitors, Inductors, and Resistors, <u>click here</u>.

Environmental Characteristics

Moisture Sensitivity Level	.1
ESD Classification (HBM)N	/A

Isolated Circuit



WARNING Cancer and Reproductive Harm - <u>www.P65Warnings.ca.gov</u>

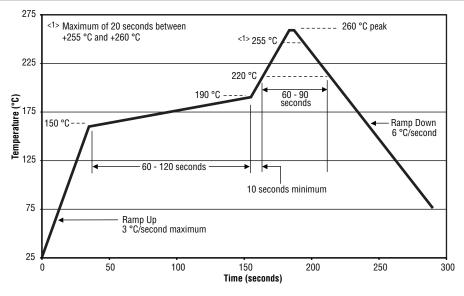
*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

Specifications are subject to change without notice. Users should verify actual device performance in their specific applications.

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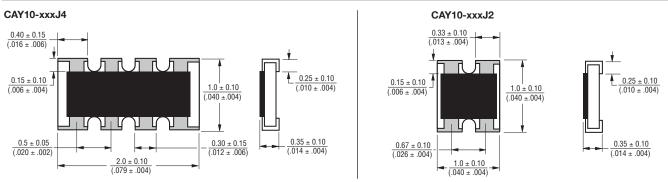
Model CAY10 - Chip Resistor Array

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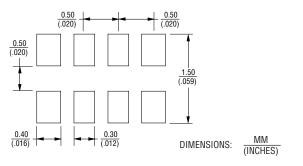
Soldering Profile for RoHS Compliant Chip Resistors and Arrays

Product Dimensions

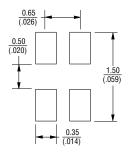


Land Pattern

CAY10-xxxJ4



CAY10-xxxJ2



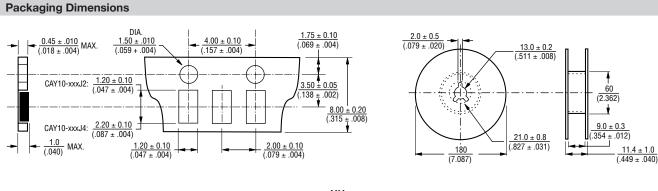
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Model CAY10 - Chip Resistor Array

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DIMENSIONS: $\frac{MM}{(INCHES)}$

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