Vishay General Semiconductor

Dual Low-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.28$ V at $I_F = 5.0$ A



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DESIGN SUPPORT TOOLS



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _{RRM}	45 V			
I _{FSM}	240 A			
V_F at $I_F = 20$ A	0.41 V			
T _J max.	150 °C			
Package D ² PAK (TO-263AB				
Circuit configuration Common cathode				

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN • Meets MSL level 1, per J-STD-020, LF maximum FREE peak of 245 °C
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VBT4045C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	per device		40	٨	
	per diode	IF(AV)	20	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	240	А	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	

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COMPLIANT



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F (1)	0.41	-	V
	I _F = 10 A			0.44	-	
	I _F = 20 A			0.50	0.58	
	I _F = 5 A	T _A = 125 °C		0.28	-	
	I _F = 10 A			0.33	-	
	I _F = 20 A			0.41	0.50	
Reverse current per diode	V _B = 45 V	$T_A = 25 \text{ °C}$	I _R ⁽²⁾	-	3000	μA
	$v_{\rm R} = 45 v$	T _A = 125 °C		18	50	mA

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VBT4045C	UNIT	
Typical thermal resistance	per diode	$R_{ ext{ heta}JC}$	1.5	°C/W	
	per device		0.8		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT4045C-M3/4W	1.38	4W	50/tube	Tube	
TO-263AB	VBT4045C-M3/8W	1.38	8W	800/reel	Tape and reel	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

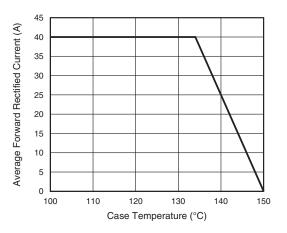


Fig. 1 - Maximum Forward Current Derating Curve

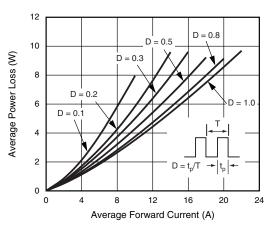
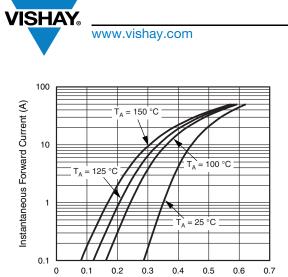


Fig. 2 - Forward Power Loss Characteristics Per Diode

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Instantaneous Forward Voltage (V) Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

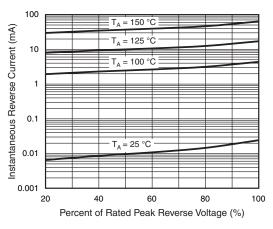


Fig. 4 - Typical Reverse Characteristics Per Diode

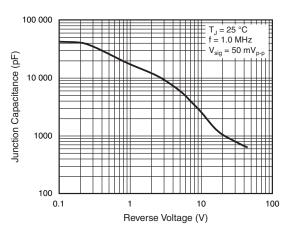


Fig. 5 - Typical Junction Capacitance Per Diode

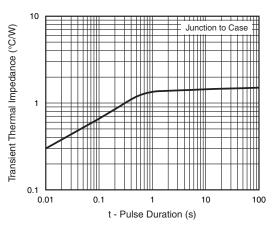
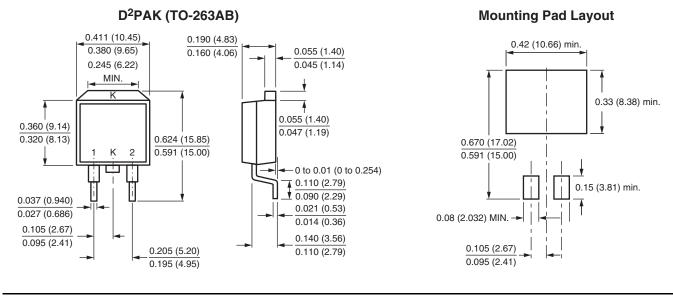


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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