

Surface Mount Ceramic Capacitor Products





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Surface Mount Ceramic Capacitor Products



X5R



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How to Order

Part Number Explanation



Commercial Surface Mount Chips

EXAMPLE: 08055A101JAT2A

0805	5	Α	101	J*	Α	T	2	A **
	T	T	T	T	T	T	T	T
Size	Voltage	Dielectric	Capacitance	Tolerance	Failure	Terminations	Packaging	Special
(L" x W")	4 = 4V	A = NPO(COG)) 2 Sig. Fig +	$B = \pm .10 pF$	Rate	T = Plated Ni	<u>Available</u>	Code
0101*	6 = 6.3V	C = X7R	No. of Zeros	$C = \pm .25 pF$	A = N/A	and Sn	2 = 7" Reel	A = Std
0201	Z = 10V	D = X5R	Examples:	$D = \pm .50 pF$	4 = Automotive	7 = Gold Plated	4 = 13" Reel	K = 30K (0603 2mm pitch)
0402	Y = 16V	F = X8R	100 = 10 pF	F = ±1%		U = Conductive	U = 4mm TR	22K (0805/1206
0603	3 = 25V	G = Y5V	101 = 100 pF	(≥ 10 pF)		Expoxy for	(01005)	<0.030"/ 0.76mm)
0805	D = 35V	U = U Series	102 = 1000 pF	$G = \pm 2\%$		Hybrid		H = 18K (0603/0805/1206
1206	5 = 50V	W = X6S	223 = 22000 pF	(≥ 10 pF)		Applications	Contact	<0.037" / 0.94mm)
1210	1 = 100V	Z = X7S	224 = 220000 pF	$J = \pm 5\%$		Z = FLEXITERM®	Contact	J = 15K (0805/1206
1812	2 = 200V		105 = 1μF	$K = \pm 10\%$		*X = FLEXITERM®	Factory For	<0.050" / 1.27mm)
1825	7 = 500V		106 = 10μF	$M = \pm 20\%$		with 5% min	Multiples	1 = 12K (0805/1206
2220			107 = 100μF	Z = +80%,		lead (X7R &	-	<0.055 / 1.4mm)
2225		Factory for	For values below	-20%		X8R only)		**Non std options upon
	Special	Voltages	10 pF, use "R"	P = +100%,				approval from the factory
*EIA 01005	F = 63V	9 = 300V	in place of	-0%		Contact		
	* = 75V	X = 350V	Decimal point, e.g.,			Factory For		
	E = 150V	8 = 400V	9.1 pF = 9R1.			= Pd/Ag Term		
	V = 250V				•	. u,rig icilii		

^{*} B, C & D tolerance for ≤10 pF values.

Standard Tape and Reel material (Paper/Embossed) depends upon chip size and thickness. See individual part tables for tape material type for each capacitance value.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. For Tin/Lead Terminations, please refer to LD Series

High Voltage MLC Chips

EXAMPLE: 1808AA271KA11A

1808	A	A	271	K	A	<u>T</u>	2	A
AVX	Voltage	Temperature	Capacitance	Capacitance	Failure	Termination	Packaging/	Special
Style 0805 1206 1210 1808 1812 1825 2220 2225 3640	C = 600V/630V A = 1000V S = 1500V G = 2000V W = 2500V H = 3000V J = 4000V K = 5000V	2:	Code (2 significant digits + no. of zeros) Examples: 10 pF = 100 100 pF = 101 1,000 pF = 102 2,000 pF = 223 0,000 pF = 224 1 µF = 105	Tolerance COG: J = ±5% K = ±10% M = ±20% X7R: K = ±10% M = ±20% Z = +80%, -20%	Rate A=Not Applicable	1 = Pd/Ag T = Plated Ni and Sn B = 5% Min Pb Z = FLEXITERM® *X = FLEXITERM® with 5% min lead (X7R only)	Marking 2 = 7" Reel 4 = 13" Reel	Code A = Standard

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. For Tin/Lead Terminations, please refer to LD Series

Not RoHS Compliant



For RoHS compliant products, please select correct termination style.

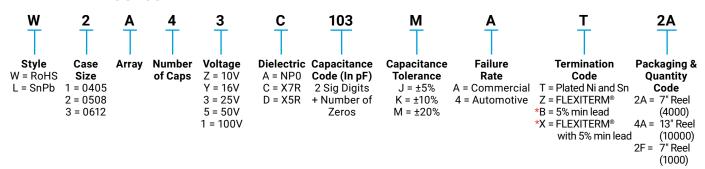
How to Order

Part Number Explanation



Capacitor Array

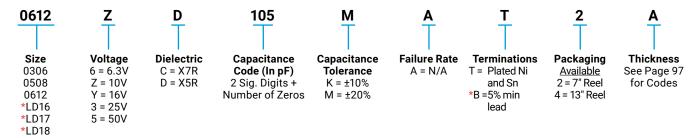
EXAMPLE: W2A43C103MAT2A



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

Low Inductance Capacitors (LICC)

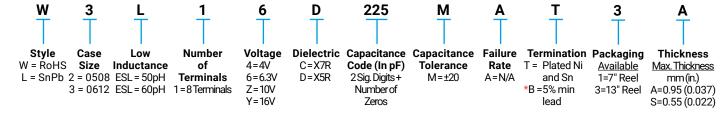
EXAMPLE: 0612ZD105MAT2A



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

Interdigitated Capacitors (IDC)

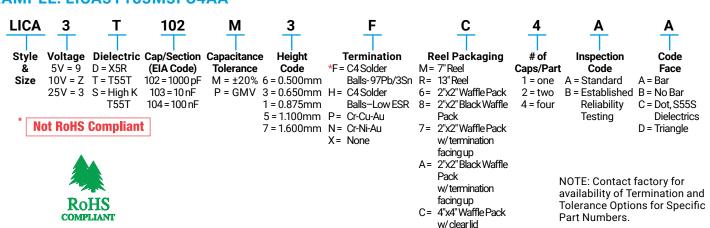
EXAMPLE: W3L16D225MAT3A



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

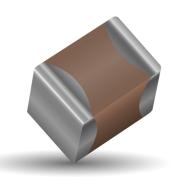
Low Inductance Decoupling Capacitor Arrays (LICA)

EXAMPLE: LICA3T183M3FC4AA



General Specifications



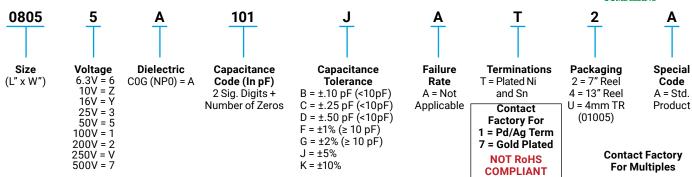


COG (NPO) is the most popular formulation of the "temperature-compensating," EIA Class I ceramic materials. Modern COG (NPO) formulations contain neodymium, samarium and other rare earth oxides.

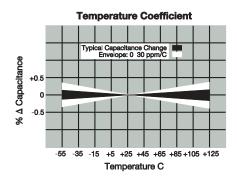
COG (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is 0 ± 30 ppm/°C which is less than $\pm0.3\%$ C from -55°C to +125°C. Capacitance drift or hysteresis for COG (NP0) ceramics is negligible at less than $\pm0.05\%$ versus up to $\pm2\%$ for films. Typical capacitance change with life is less than $\pm0.1\%$ for COG (NP0), one-fifth that shown by most other dielectrics. COG (NP0) formulations show no aging characteristics.

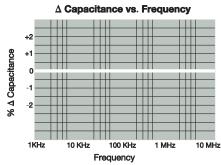
PART NUMBER (see page 4 for complete part number explanation)

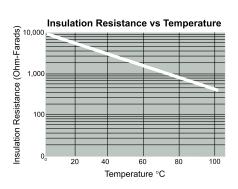


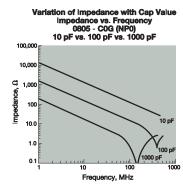


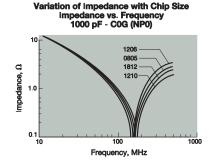
NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.

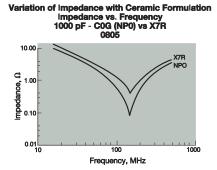
















Parame	ter/Test	NP0 Specification Limits	Measuring (Conditions			
Operating Tem	perature Range	-55°C to +125°C	Temperature C				
•	citance Q	Within specified tolerance <30 pF: Q≥ 400+20 x Cap Value ≥30 pF: Q≥ 1000	Freq.: 1.0 MHz ± 10% 1.0 kHz ± 10% fo Voltage: 1.0	r cap > 1000 pF			
Insulation	Resistance	100,000MΩ or 1000MΩ - μ F, whichever is less	Charge device with rated @ room tem				
Dielectric	: Strength	No breakdown or visual defects	Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.				
	Appearance	No defects		_			
Resistance to	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Deflection: 2mm Test Time: 30 seconds 1mm/sec				
Flexure	Q	Meets Initial Values (As Above)					
Stresses	Insulation Resistance	≥ Initial Value x 0.3	90 mm				
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic sol ± 0.5 se				
	Appearance	No defects, <25% leaching of either end terminal					
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Dip device in eutectic	solder at 260°C for			
Resistance to	Q	Meets Initial Values (As Above)	60sec- onds. Store at room temperature for 24 ± 2hours before measuring elec				
Solder Heat	Insulation Resistance	Meets Initial Values (As Above)	properties.				
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes			
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp	≤ 3 minutes			
Thermal Shock	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2° 30 ± 3 minute				
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes			
	Dielectric	Meets Initial Values (As Above)	Repeat for 5 cycles				
	Strength Appearance	No visual defects	24 hours at roor	n temperature			
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twic				
Load Life	Q (C=Nominal Cap)	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	chamber set at for 1000 hou Remove from test cha	rs (+48, -0).			
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	room temperatu before me	re for 24 hours			
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects					
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater	Store in a test chamber s	et at 85°C ± 2°C/ 85% ±			
Load Humidity	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	5% relative humidi (+48, -0) with rated	ty for 1000 hours I voltage applied.			
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature for 24 ± 2 ho				
	Dielectric Strength	Meets Initial Values (As Above)					

Capacitance Range



PREFERRED SIZES ARE SHADED

SI	ZE	010	01*	02	01		0402				0603						0805						1206			
Sold	ering	Reflov	v Only	Reflov	v Only	Ref	low/Wa	ave		Re	eflow/W	ave				Refl	ow/Wave	 e				R	Reflow/W	ave		
Pack	aging	All P		All P		Δ	II Pape	r			All Pape	er				Paper	/Emboss	ed				Par	per/Emb	ossed		
(L) Length	mm	0.40 ±		0.60 ±			00 ± 0.1				1.60 ± 0.						1 ± 0.20			3.20 ± 0.20						
(=) congui	(in.) mm	(0.016 ± 0.20 ±		0.30		_	40 ± 0.0 50 ± 0.1				063 ± 0. 0.81 ± 0.	_					9 ± 0.00 25 ± 0.20	B)			(0.126 ± 0.008) 1.60 ± 0.20					
W) Width	(in.)	(0.008 ±		(0.011 ±			20 ± 0.0	i			032 ± 0.						19 ± 0.00	8)					.063 ± 0.			
(t) Terminal	mm	0.10 ±		0.15			25 ± 0.1				0.35 ± 0.						50 ± 0.25						0.50 ± 0.			
(c) reminal	(in.)	(0.004 ±		(0.006 ±	50	(0.0°	10 ± 0.0	006) 50	16	(0.0	014 ± 0. 50	006) 100	200	16	25	(0.02	20 ± 0.01	0) 200	250	16	25	50	.020 ± 0.	010) 200	250	500
Сар	0.5			A	A	С	С	С	G	G	G	G	200	J	J	J	J	J	200	J	J	J	J	J	200	J
(pF)	1.0 1.2	E	3	A	A	CC	C	C	G G	G G	G	G		J	J	J	J	J J		J .	J	J	J	J		J J
	1.5	E		A	A	C	C	C	G G	G	G	G G		J	J	J	J	J		J	J	J	J	J		J
	2.2 2.7	E		A A	A	C C	C C	C C	G G	G G	G G	G G		J	J	J	J	J		J	J	J	J	J		J J
	3.3	E		A	A	С	С	С	G	G	G	G		J	J	J	J	J		J	J	J	J	J		J
	3.9	E		A	A	С	С	С	G	G	G	G		J	J	J	J	J		J	J	J	J	J		J
	4.7 5.6	E		A	A	C	C	C	G G	G	G	G		J	J	J	J	J		J	J	J	J	J		J
	6.8	E		Α	A	С	С	С	G	G	G	G		J	J	J	J	J		J	J	J	J	J		J
	8.2	E		A	A	C	C	C	G	G	G	G	G	J	J	J	J	J	N	J	J	J	J	J	J	J
	12	Е	3	Α	Α	С	С	С	G	G	G	G	G	J	J	J	J	J	N	J	J	J	J	J	J	J
	15 18	E		A	A	C	C	C	G G	G	G	G G	G	J	J	J	J	J	N N	J	J	J	J	J	J	J
	22	E	3	Α	Α	С	С	С	G	G	G	G	G	J	J	J	J	J	N	J	J	J	J	J	J	J
	27 33	E		A	A	C	C	C C	G G	G G	G	G G	G G	J	J	J	J	J	N N	J	J	J	J	J	J	J
	39	E	3	Α	A	С	С	С	G	G	G	G	G	J	J	J	J	J	N	J	J	J	J	J	J	J
	47 56	E		A	A	C	C	C	G	G	G	G G	G G	J	J	J	J	J	N N	J	J	J	J	J	J	J
	68 82	E	3	A	A	C	C	C	G	G	G	G	9 9	J	J	J	J	J	N N	J	J	J	J	J		J J
	100	Е		Α	Α	С	С	С	G	G	G	G	G	J	J	J	J	J	N	J	J	J	J	J		J
	120 150					C	C	C C	G G	G G	G G	G G	G G	J	J	J	J	J	N N	J	J	J	J	J		J J
	180					С	С	С	G	G	G	G	G	J	J	J	J	J	N	J	J	J	J	J		J
	220 270					C	C	C	G G	G G	G	G G	G	J	J	J	J	J	N N	J	J	J	J	J		M M
	330					С	С	С	G	G	G	G		J	J	J	J	J	N	J	J	J	J	J		M
	390 470					C	С	C C	G	G G	G	G		J	J	J	J	J		J	J	J	J	J		М
	560					С	C	С	G	G	G	G		J	J	J	J	J		J	J	J	J	J		M M
	680					С	С	С	G	G	G	G		J	J	J	J	J		J	J	J	J	J		Р
	820 1000					C	C	C	G	G	G	G G		J	J	J	J	J		J	J	J	J	M Q		
	1200								G	G	G			J	J	J	J	J		J	J	J	J	Q		
	1500 1800								G	G G	G			J	J	J	J N			J	J	J M	M	Q		
	2200								G	G	G			N	N	N	N			J	J	М	Р	Q		
	2700 3300								G	G	G			N P	N N	N N	N N			J	J	M	P P	Q		
	3900								G	G	G			Р	Р	Р	N			J	J	М	Р			
	4700 5600	-				\vdash			G	G	G			P P	P	P P	N			J	J	M	P P			
	6800			l	_									Р	Р	Р				М	М	М	Р			
Сар	8200 0.010	-		<u>_L</u>			<u> </u>	V->	/-		-			P P	P P	P P				M P	M P	M P	P P			
(μF)	0.012				_				T					P	P	P										
	0.015 0.018		_	()))) _		ノユ						P P	P	P P										
	0.022				$\stackrel{\smile}{\smile}$									P	P	P										
	0.027		_		₹				_	_	_					_					<u> </u>					
	0.039				'	<u> </u>		١																		
	0.047 0.068														_											
	0.068 0.082 0.1																									
W	/DC	1	6	25	50	16	25	50	16	25	50	100	200	16	25	50	100	200	250	16	25	50	100	200	250	500
S	ZE	010	01*	02	01		0402				0603						0805						1206			
Letter	А	В	С		E	G		J		K		М	N	1	I	P	Q		Х		Υ		Z			
Max.	0.33	0.22	0.56	0.	71	0.90		0.94		1.02		1.27	1.4	40	1.	52	1.78	3	2.29		2.54	2	.79			
Thickness		(0.009)	(0.022)) (0.0	028)	(0.035		0.037)	((0.040)		0.050)	(0.0			060)	(0.07	0)	(0.090)).100)		110)			
			F	PAPER												EMB	OSSEI)								

Capacitance Range



PREFERRED SIZES ARE SHADED

EFERR																				
													Ш			Ш			Ш	
SIZ				1210					1812				1825			2220			2225	
Solde				Reflow Only					Reflow Only			_	Reflow Only			Reflow Onl			eflow Only	
Packa	ging mm			per/Embos 3.20 ± 0.20					4.50 ± 0.30				4.50 ± 0.30			II Embosse 5.70 ± 0.40			.72 ± 0.25	
(L) Length	(in.)		(0	.126 ± 0.00	(8)			(1	0.177 ± 0.01	2)		(0	0.177 ± 0.01	2)	(0	.225 ± 0.01	6)	(0.:	225 ± 0.010	0)
W) Width	mm (in.)			2.50 ± 0.20 0.098 ± 0.00				((3.20 ± 0.20 0.126 ± 0.00				6.40 ± 0.40 0.252 ± 0.01			5.00 ± 0.40 1.197 ± 0.01			.35 ± 0.25 250 ± 0.010	
(t) Terminal	mm			0.50 ± 0.25	5				0.61 ± 0.36				0.61 ± 0.36			0.64 ± 0.39		C	.64 ± 0.39	
	(in.) WVDC	25	50	0.020 ± 0.01	200	500	25	50	0.024 ± 0.01	4) 200	500	50	0.024 ± 0.01	4) 200	50	.025 ± 0.01	200	50	025 ± 0.015 100	200
Cap	0.5																			
(pF)	1.0 1.2																			
	1.5 1.8																			
	2.2																'		W.	
	2.7 3.3															~		<	$\overline{}$) \
	3.9																		سلر	1
	4.7 5.6																	+	-	-
	6.8																	t		
	8.2 10					J										-				
	12					J														
	15 18					J		-				-				-				-
	22					J														
	27 33					J														
	39					J														
	47 56					J														
	68					J														
	82 100					J														
	120					J														
	150 180					J														
	220					J														
	270 330					J														
	390 470					М														
	560	J	J	J	J	M M														
	680	J	J	J	K	Р														
	820 1000	J	J	J P	K P	P P	K	K	N	N	М	М	М	М				М	M	P
	1200 1500	P P	P P	P P	P P	P P	K	K K	N	N	М	М	М	М				М	М	P P
	1800	P	P	P	P	P	K K	K	N N	N N	M	M	M	M M				M M	M M	P
	2200	Р	Р	Р	Р	N	K	K	N	N	Р	X	x	М				М	М	Р
	2700 3300	P P	P P	P P	P P		K K	K	N N	P P	Q Q	X	X	X		 	Х	M	M M	P P
	3900 4700	P P	P P	P P			K K	K K	N N	P P	Q Y	X X	x x	X X	X	X	X X	M M	M M	P P
	5600	Р	Р	Р			K	К	Р	Р	Y	Х	Х	Х	Х	Х	Х	М	М	Р
	6800 8200	P P	P P	Р			K K	K M	Q Q	Q Q		X X	X X	X X	X X	X X	X X	M M	M M	P P
Сар	0.010	N	N				K	М	Q	Q		Х	Х	Х	Х	Х	Х	М	М	Р
(pF)	0.012 0.015	N	N				K P	M P	Q Q			X X	X X	X X	X X	X	X X	M M	M M	P Y
	0.018						Р	Р	Q			Х	Х	Х	Х	Х	X	М	М	Υ
	0.022 0.027						P Q	P Q	Q X			X X	X X	X Y	X X	X X		M P	Y Y	Y
	0.033						Q	Q	Х			Х	X		Х	X		Х	Y	Υ
	0.039 0.047						X X	X	X X			X X			Y Y			X X	Y Z	Y
	0.068						Z	Z	Y						Z			Х	Z	
	0.082 0.1						Z Z	Z Z	Y Z						Z Z			X Z	Z Z	
	WVDC	25	50	100	200	500	25	50	100	200	500	50	100	200	50	100	200	50	100	200
	SIZE			1210					1812				1825			2220			2225	
				_	-	1 .		J		1				1 .				1		
Letter	Α	В		С	E	G		J	K	М		N	Р	Q		Х	Υ	Z		
Letter Max. Thickness	0.33 (0.013)	0.22 (0.009		0.56 0.022)	0.71 (0.028)	0.90 (0.03		0.94	1.02 (0.040)	1.27 (0.05		N 1.40 0.055)	1.52 (0.060)	1.7 (0.07	8	X 2.29 0.090)	2.54 (0.100)	2.7 (0.1	9	

RF/Microwave C0G (NP0) Capacitors (RoHS)

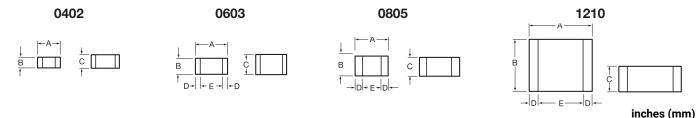
Ultra Low ESR, "U" Series, COG (NP0) Chip Capacitors



GENERAL INFORMATION

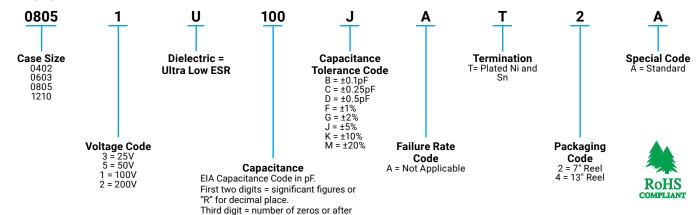
"U" Series capacitors are COG (NP0) chip capacitors specially designed for "Ultra" low ESR for applications in the communications market. Max ESR and effective capacitance are met on each value producing lot to lot uniformity. Sizes available are EIA chip sizes 0402, 0603, 0805, and 1210.

DIMENSIONS: INCHES (MILLIMETERS)



Size	Α	В	С	D	E
0402	0.039±0.004 (1.00±0.1)	0.020±0.004 (0.50±0.1)	0.022 (0.55mm) max	N/A	N/A
0603	0.060±0.010 (1.52±0.25)	0.030±0.010 (0.76±0.25)	0.036 (0.91mm) max	0.010±0.005 (0.25±0.13)	0.030 (0.76) min
0805	0.079±0.008 (2.01±0.2)	0.049±0.008 (1.25±0.2)	0.040±0.005 (1.02±0.127)	0.020±0.010 (0.51±0.255)	0.020 (0.51) min
1210	0.126±0.008 (3.2±0.2)	0.098±0.008 (2.49±0.2)	0.050±0.005 (1.27±0.127)	0.025±0.015 (0.635±0.381)	0.040 (1.02) min

HOW TO ORDER



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

ELECTRICAL CHARACTERISTICS

Capacitance Values and Tolerances:

Size 0402 - 0.2 pF to 30 pF @ 1 MHz Size 0603 - 1.0 pF to 100 pF @ 1 MHz Size 0805 - 1.6 pF to 160 pF @ 1 MHz Size 1210 - 2.4 pF to 1000 pF @ 1 MHz

Temperature Coefficient of Capacitance (TC):

0±30 ppm/°C (-55° to +125°C)

Insulation Resistance (IR):

 $10^{12}\,\Omega$ min. @ 25°C and rated WVDC $10^{11}\,\Omega$ min. @ 125°C and rated WVDC

Working Voltage (WVDC):

Size Working Voltage 0402 - 100, 50, 25 WVDC 0603 - 200, 100, 50 WVDC

0805 - 200, 100 WVDC 1210 - 200, 100 WVDC

"R" significant figures.

Dielectric Working Voltage (DWV):

250% of rated WVDC

Equivalent Series Resistance Typical (ESR):

0402 - See Performance Curve, page 13
0603 - See Performance Curve, page 13
0805 - See Performance Curve, page 13
1210 - See Performance Curve, page 13

Marking:

Laser marking EIA J marking standard (except 0603) (capacitance code and tolerance upon request).

Military Specifications

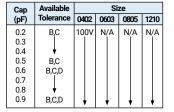
Meets or exceeds the requirements of MIL-C-55681

RF/Microwave C0G (NP0) Capacitors (RoHS)





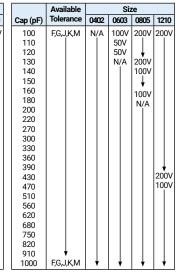
CAPACITANCE RANGE



	Availa		Size									
Cap (pF)	Tolera	nce	04	02	06	03	08	05	12	10		
1.0	B,C,	D _	10	0V	20	0V	20	0V	20	0V		
1.1												
1.2												
1.3												
1.4												
1.5												
1.6												
1.7												
1.8												
1.9												
2.0												
2.1												
2.2												
2.4												
2.7												
3.0												
3.3												
3.6												
3.9												
4.3												
4.7												
5.1												
5.6	▼											
6.2	B,C,											
6.8	B,C,J,	K,M	<u>'</u>	7	1	7	,	7	١.	,		

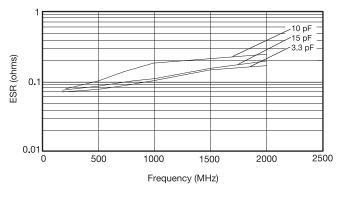
	Available		SIZ						
Cap (pF)	Tolerance	0402	0603	0805	1210				
7.5	B,C,J,K,M	100V	200V	200V	200V				
8.2	↓								
9.1	B,C,J,K,M	↓							
10	F,G,J,K,M	100V							
11		50V							
12		l I							
13									
15			♦						
18			200V						
20			100V						
22									
24									
27		.							
30		50V							
33		N/A							
36									
39									
43									
47									
51									
56 68									
75									
75 82									
91	↓	↓	↓	l ↓	↓				
71		_ •							

Avoilable

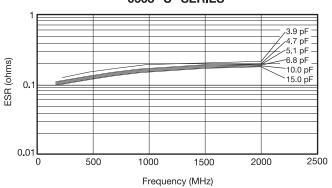


ULTRA LOW ESR, "U" SERIES

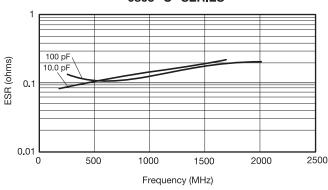
TYPICAL ESR vs. FREQUENCY 0402 "U" SERIES



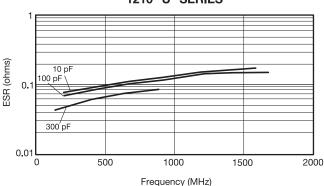
TYPICAL ESR vs. FREQUENCY 0603 "U" SERIES



TYPICAL ESR vs. FREQUENCY 0805 "U" SERIES



TYPICAL ESR vs. FREQUENCY 1210 "U" SERIES



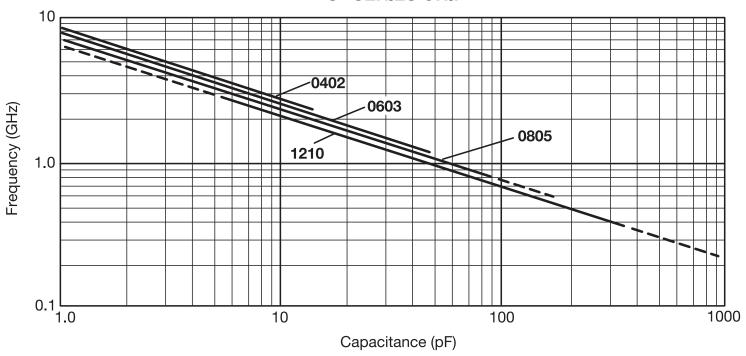
ESR Measured on the Boonton 34A

RF/Microwave C0G (NP0) Capacitors





TYPICAL SERIES RESONANT FREQUENCY "U" SERIES CHIP



RF/Microwave COG (NP0) Capacitors (Sn/Pb)

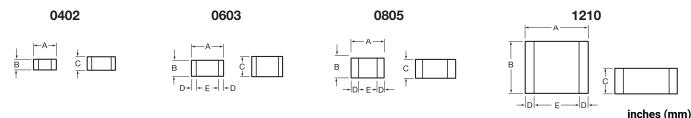
Ultra Low ESR, "U" Series, COG (NP0) Chip Capacitors



GENERAL INFORMATION

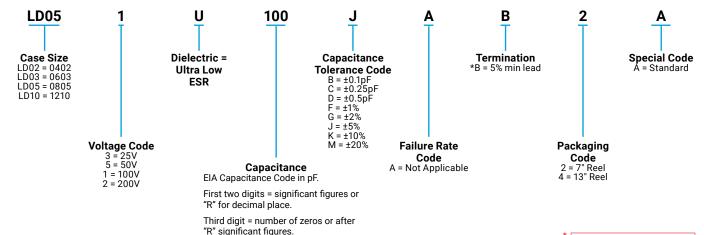
"U" Series capacitors are COG (NPO) chip capacitors specially designed for "Ultra" low ESR for applications in the communications market. Max ESR and effective capacitance are met on each value producing lot to lot uniformity. Sizes available are EIA chip sizes 0402, 0603, 0805, and 1210.

DIMENSIONS: INCHES (MILLIMETERS)



Size	Α	В	С	D	E Ì
0402	0.039±0.004 (1.00±0.1)	0.020±0.004 (0.50±0.1)	0.022 (0.55mm) max	N/A	N/A
0603	0.060±0.010 (1.52±0.25)	0.030±0.010 (0.76±0.25)	0.036 (0.91mm) max	0.010±0.005 (0.25±0.13)	0.030 (0.76) min
0805	0.079±0.008 (2.01±0.2)	0.049±0.008 (1.25±0.2)	0.040±0.005 (1.02±0.127)	0.020±0.010 (0.51±0.254)	0.020 (0.51) min
1210	0.126±0.008 (3.2±0.2)	0.098±0.008 (2.49±0.2)	0.050±0.005 (1.27±0.127)	0.025±0.015 (0.635±0.381)	0.040 (1.02) min

HOW TO ORDER



Not RoHS Compliant

ELECTRICAL CHARACTERISTICS

Capacitance Values and Tolerances:

Size 0402 - 0.2 pF to 22 pF @ 1 MHz

Size 0603 - 1.0 pF to 100 pF @ 1 MHz

Size 0805 - 1.6 pF to 160 pF @ 1 MHz

Size 1210 - 2.4 pF to 1000 pF @ 1 MHz

Temperature Coefficient of Capacitance (TC):

0±30 ppm/°C (-55° to +125°C)

Insulation Resistance (IR):

 $10^{12} \Omega$ min. @ 25° C and rated WVDC $10^{11} \Omega$ min. @ 125° C and rated WVDC

Working Voltage (WVDC):

Working Voltage Size 0402 - 50, 25 WVDC 0603 - 200, 100, 50 WVDC

Dielectric Working Voltage (DWV):

250% of rated WVDC

Equivalent Series Resistance Typical (ESR):

0402 - See Performance Curve, page 16

0603 - See Performance Curve, page 16

0805 - See Performance Curve, page 16

1210 - See Performance Curve, page 16

Marking:

Laser marking EIA J marking standard (except 0603) (capacitance code and tolerance upon request).

Military Specifications

Meets or exceeds the requirements of MIL-C-55681



0805 - 200, 100 WVDC

1210 - 200, 100 WVDC

RF/Microwave C0G (NP0) Capacitors (Sn/Pb)



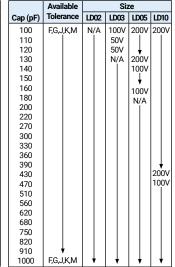


CAPACITANCE RANGE

	Available	Size						
Cap (pF)	Tolerance	LD02	LD03	LD05	LD10			
0.2	B,C	50V	N/A	N/A	N/A			
0.3								
0.4	+							
0.5	B,C							
0.6	B,C,D							
0.7								
8.0	+							
0.9	B,C,D	↓	↓	↓	↓			

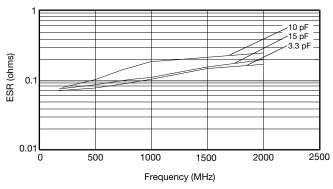
	Available	Size										
Cap (pF)	Tolerance	LD02	LD03	LD05	LD10							
1.0	B,C,D	50V	200V	200V	200V							
1.1												
1.2												
1.3												
1.4												
1.5												
1.6												
1.7												
1.8												
1.9												
2.0												
2.1												
2.2												
2.4												
2.7												
3.0												
3.3												
3.6												
3.9												
4.3												
4.7												
5.1												
5.6	*											
6.2	B,C,D											
6.8	B,C,J,K,M	_ *	_ *	*	*							

	Available	Size			
Cap (pF)	Tolerance	LD02	LD03	LD05	LD10
7.5	B,C,J,K,M	50V	200V	200V	200V
8.2	↓				
9.1	B,C,J,K,M				
10	F,G,J,K,M				
11					
12					
13					
15			+		
18			200V		
20			100V		
22					
24		↓			
27		50V			
30		N/A			
33		''' ^			
36					
39 43					
43 47					
51					
56					
68					
75					
82					
91	↓	↓	↓	↓	↓

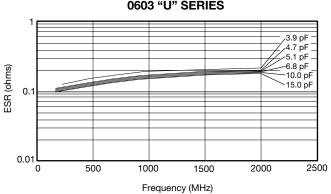


ULTRA LOW ESR, "U" SERIES

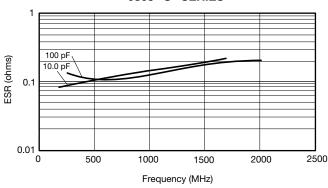
TYPICAL ESR vs. FREQUENCY 0402 "U" SERIES



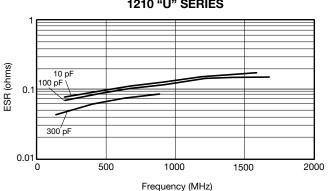
TYPICAL ESR vs. FREQUENCY 0603 "U" SERIES



TYPICAL ESR vs. FREQUENCY 0805 "U" SERIES



TYPICAL ESR vs. FREQUENCY 1210 "U" SERIES



ESR Measured on the Boonton 34A

RF/Microwave Automotive C0G (NP0) Capacitors (RoHS)

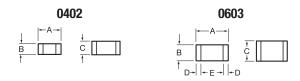


AEC Q200 Qualified Ultra Low ESR, "U" Series, C0G (NP0) Chip Capacitors

GENERAL INFORMATION

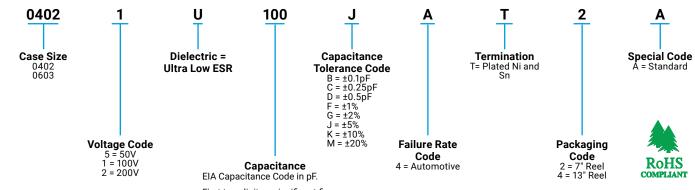
Automotive "U" Series capacitors are COG (NP0) chip capacitors specially designed for "Ultra" low ESR for applications in the automotive market. Max ESR and effective capacitance are met on each value producing lot to lot uniformity. Sizes available are EIA chip sizes 0402 and 0603.

DIMENSIONS: INCHES (MILLIMETERS)



Size	Α	В	С	D	E
0402	1.00±0.1 (0.039±0.004)	0.50±0.1 (0.020±0.004)	0.60 max (0.024)	N/A	N/A
0603	1.52±0.25 (0.060±0.010)	0.76±0.25 (0.030±0.010)	0.91 max (0.036)	0.25±0.13 (0.010±0.005)	0.76 min (0.030)

HOW TO ORDER



First two digits = significant figures or "R" for decimal place.

Third digit = number of zeros or after "R" significant figures.

ELECTRICAL CHARACTERISTICS

Capacitance Values and Tolerances:

Size 0402 - 0.2 pF to 22 pF @ 1 MHz Size 0603 - 1.0 pF to 100 pF @ 1 MHz

Temperature Coefficient of Capacitance (TC):

0±30 ppm/°C (-55° to +125°C)

Insulation Resistance (IR):

 $10^{12}\,\Omega$ min. @ 25°C and rated WVDC $10^{11}\,\Omega$ min. @ 125°C and rated WVDC

Working Voltage (WVDC):

Size Working Voltage 0402 - 50, 25 WVDC 0603 - 200, 100, 50 WVDC

Dielectric Working Voltage (DWV):

250% of rated WVDC

Equivalent Series Resistance Typical (ESR):

0402 - See Performance Curve 0603 - See Performance Curve

Automotive Specifications

Meets or exceeds the requirements of AEC Q200

RF/Microwave Automotive C0G (NP0) Capacitors (RoHS)



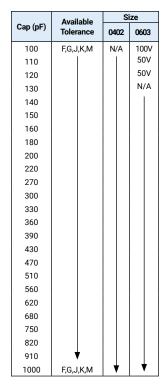
AEC Q200 Qualified, Ultra Low ESR, "U" Series, COG (NP0) Chip Capacitors

CAPACITANCE RANGE

	Δvailable	Available Siz	
Cap (pF)	Tolerance	0402	0603
0.2	B,C	100V	N/A
0.3			
0.4	♦		
0.5	B,C		
0.6	B,C,D		
0.7			
0.8	♦		
0.9	B,C,D		_ ▼

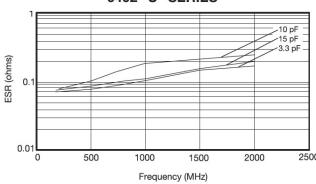
	Available	Si	ze
Cap (pF)	Tolerance	0402	0603
1.0	B,C,D	100V	200V
1.1			
1.2			
1.3			
1.4			
1.5			
1.6			
1.7			
1.8			
1.9			
2.0			
2.1			
2.2			
2.4			
2.7			
3.0			
3.3			
3.6			
3.9			
4.3			
4.7			
5.1			
5.6	\		
6.2	B,C,D		
6.8	B,C,J,K,M	♦	\ \

	Available	Si	ze
Cap (pF)	Tolerance	0402	0603
7.5	B,C,J,K,M	100V	200V
8.2	*		
9.1	B,C,J,K,M		
10	F,G,J,K,M		
11			
12			
13			
15			▼
18			200V
20			100V
22			
24			
27		♦	
30		50V	
33		N/A	
36			
39			
43			
47			
51			
56			
68			
75			
82			
91	▼	▼	\ \

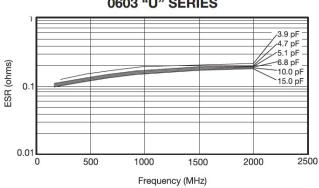


ULTRA LOW ESR, "U" SERIES

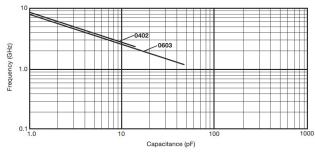
TYPICAL ESR vs. FREQUENCY 0402 "U" SERIES



TYPICAL ESR vs. FREQUENCY 0603 "U" SERIES



TYPICAL SERIES RESONANT FREQUENCY **"U" SERIES CHIP**





"U" SERIES KITS

0402

	Kit 5000 UZ						
Cap. Value pF	Tolerance	Cap. Value pF	Tolerance				
0.5		4.7					
1.0	B (±0.1pF)	5.6	B (±0.1pF)				
1.5		6.8	Б (±0.1рг)				
1.8		8.2					
2.2		10.0					
2.4		12.0	(±5%)				
3.0		15.0	(±3%)				
3.6							

^{***25} each of 15 values

0603

Kit 4000 UZ						
Cap. Value pF	Tolerance	Cap. Value pF	Tolerance			
1.0		6.8				
1.2		7.5	B (±0.1pF)			
1.5		8.2				
1.8		10.0				
2.0		12.0				
2.4	D (101-F)	15.0				
2.7	B (±0.1pF)	18.0				
3.0		22.0	J (±5%)			
3.3		27.0				
3.9		33.0				
4.7		39.0				
5.6	f 0 4l	47.0				

^{***25} each of 24 values

0805

	Kit 3000 UZ						
Cap. Value pF	Tolerance	Cap. Value pF	Tolerance				
1.0		15.0					
1.5		18.0					
2.2		22.0					
2.4		24.0					
2.7		27.0					
3.0		33.0					
3.3	B (±0.1pF)	36.0					
3.9		39.0	J (±5%)				
4.7		47.0					
5.6		56.0					
7.5		68.0					
8.2		82.0					
9.1		100.0					
10.0	J (±5%)	130.0					
12.0	3 (±3%)	160.0					

^{***25} each of 30 values

1210

Kit 3500 UZ						
Cap. Value pF	Tolerance	Cap. Value pF	Tolerance			
2.2		36.0				
2.7		39.0				
4.7		47.0				
5.1	B (±0.1pF)	51.0				
6.8		56.0				
8.2		68.0				
9.1		82.0				
10.0		100.0	J (±5%)			
13.0		120.0				
15.0		130.0				
18.0	J (±5%)	240.0				
20.0	3 (±3%)	300.0				
24.0		390.0				
27.0		470.0				
30.0		680.0				

^{***25} each of 30 values

X8R/X8L Dielectric

General Specifications

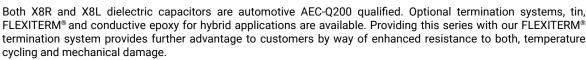




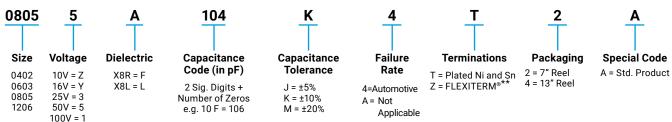
AVX has developed a range of multilayer ceramic capacitors designed for use in applications up to 150° C. These capacitors are manufactured with an X8R and an X8L dielectric material. X8R material has capacitance variation of $\pm 15\%$ between -55°C and +150°C. The X8L material has capacitance variation of $\pm 15\%$ between -55°C to $\pm 150\%$ C and +150°C.

The need for X8R and X8L performance has been driven by customer requirements for parts that operate at elevated temperatures. They provide a highly reliable capacitor with low loss and stable capacitance over temperature.

They are ideal for automotive under the hood sensors, and various industrial applications. Typical industrial application would be drilling monitoring system. They can also be used as bulk capacitors for high temperature camera modules.







NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

	Size		0603		0805 1206		0603 0805 1206		06
	Solderin	ıg	Reflow	/Wave	Reflow/Wave		Reflow	/Wave	
		WVDC	25V	50V	25V	50V	25V	50V	
271	Cap	270	G	G					
331	(pF)	330	G	G	J	J			
471		470	G	G	J	J			
681		680	G	G	J	7			
102		1000	G	G	J	٦	J	J	
152		1500	G	G	J	J	J	J	
222		2200	G	G	J	J	J	J	
332		3300	G	G	J	J	J	J	
472		4700	G	G	J	7	J	J	
682		6800	G	G	J	J	J	J	
103	Сар	0.01	G	G	J	J	J	J	
153	(µF)	0.015	G	G	J	J	J	J	
223		0.022	G	G	J	J	J	J	
333		0.033	G	G	J	J	J	J	
473		0.047	G	G	J	J	J	J	
683		0.068	G		N	N	М	М	
104		0.1			N	N	М	М	
154		0.15			N	N	М	М	
224		0.22			N		М	М	
334		0.33					М	М	
474		0.47					М		
684		0.68							
105		1	-						
155	-	1.5							
225		2.2							
		WVDC	25V	50V	25V	50V	25V	50V	
	SIZE		06	03	08	05	12	06	

Size		0603	0805	1206	1210
Solderin	g	Reflow/Wave	Reflow/Wave	Reflow/Wave	Reflow/Wave
Packagir	ng	All Paper	Paper//Embossed	Paper/Embossed	Paper/Embossed
(L) Longth	mm	1.60 ± 0.15	2.01 ± 0.20	3.20 ± 0.20	3.30 ± 0.4
(L) Length (in)	(in)	(0.063 ± 0.006)	(0.079 ± 0.008)	(0.126 ± 0.008)	(0.130 ± 0.016)
040 145 -141-	mm	0.81 ± 0.15	1.25 ± 0.20	1.60 ± 0.20	2.50 ± 0.20
(W) Width (in)	(0.032 ± 0.006)	(0.049 ± 0.008)	(0.063 ± 0.008)	(0.098 ± 0.008)	
(t) Terminal	mm	0.35 ± 0.15	0.50 ± 0.25	0.50 ± 0.25	0.50 ± 0.25
	(in)	(0.014 ± 0.006)	(0.020 ± 0.010)	(0.020 ± 0.010)	(0.020 ± 0.010)

_							<u>ol</u>							
	Size		0603			0805			12	06			1210	
5	Soldering		eflow/W			flow/W			Reflow	/Wave		Re	flow/W	ave
	WVD		50V	100V	25V	50V	100V	16V	25V	50V	100V	10V	50V	100V
271	Cap 270		G											
331	(pF) 330		G	G	J	J	J							
471	470		G	G	J	J	J							
681	680		G	G	J	J	J							
102	1000		G	G	J	J	J		J	J				
152	1500		G	G	J	J	J		J	J	J			
182	180		G	G	J	J	J		J	J	J			
222	220		G	G	J	J	J		J	J	J			
272	270		G	G	J	J	J		J	J	J			
332	330		G	G	J	J	J		J	J	J			
392	390		G	G	J	J	J		J	J	J			
472	470		G	G	J	J	J		J	J	J			
562	560		G	G	J	J	J		J	J	J			
682	680		G	G	J	J	J		J	J	J			
822	820		G	G	J	J	J		J	J	J			
103	Cap 0.0		G	G	J	J	J		J	J	J			ļ
123	(μF) 0.01:		G		J	J	J		J	J	J			
153	0.01		G		J	J	J		J	J	J			
183	0.01		G		J	J	J		J	J	J			
223	0.02		G		J	J	J		J	J	J			ļ
273	0.02		G		J	J	J		J	J	J			
333	0.03		G		J	J	N		J	J	J			
393	0.03		G		J	J	N		J	J	J			
473	0.04		G		J	J	N		J	J	J			
563	0.05		G		J	J	N		J	J	J			
683	0.06		G		J	J	N		J	J	J			
823	0.08		G		J	J	N		J	J	J			
104	0.		G		J	J	N		J	J	M			
124	0.1		1		J	N			J	J	M			-
154	0.1		-		J	N		J	J	J	Q			
184	0.1		+	-	N	N	_	J	J	J	Q		_	-
224 274	0.2		+-	-	N	N	_	J	J	J	Q			-
334	0.2		+	-	N N	-	-	J	M	M	Q			-
394	0.3		+	-	N	-	<u> </u>	M	M	M P	Q		<u> </u>	-
474			+	-	N	-		M	M	P	Q			-
684	0.4		+	-	N	-		M	M	P	Q	-		+
824	0.8		+	-	N		-	M	M	P	Q		_	1
105		<u></u>	+	-	N		-	M	M	P	Q			1
155	1.		+	\vdash	IN	_		M	M	F	Q		_	1
225	2.:		+	_			 	M	M	_			Z	Z
475		+	+	-		 	-	IVI	IVI				Z	
106		+	+	 		 	-		—	-		Z		
100	WVD	25V	50V	100V	25V	50V	100V	16V	25V	50V	100V	10V	50V	100V
	SIZE	200	0603	1001	250	0805	1001	137		06	1001	130	1210	1001
										-				

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z	
Max.	0.33	0.56	0.71	0.9	0.94	1.02	1.27	1.4	1.52	1.78	2.29	2.54	2.79	
Thickness	(-0.013)	(-0.022)	(-0.028)	(-0.035)	(-0.037)	(-0.04)	(-0.05)	(-0.055)	(-0.06)	(-0.07)	(-0.09)	(-0.1)	(-0.11)	
			PAPER						EMBO	SSED				



X8R/X8L Dielectric

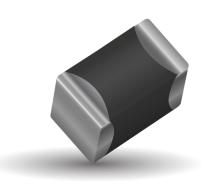
General Specifications



APPLICATIONS FOR X8R AND X8L CAPACITORS

- All market sectors with a 150°C requirement
- Automotive on engine applications
- Oil exploration applications
- Hybrid automotive applications
 - Battery control
 - Inverter / converter circuits
 - Motor control applications
- Water pump
- Hybrid commercial applications
- Emergency circuits
- Sensors
- Temperature regulation





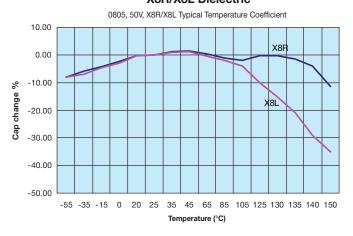
ADVANTAGES OF X8R AND X8L MLC CAPACITORS

- · Both ranges are qualified to the highest automotive AEC-Q200 standards
- Excellent reliability compared to other capacitor technologies
- RoHS compliant
- Low ESR / ESL compared to other technologies
- Tin solder finish
- FLEXITERM® available
- Epoxy termination for hybrid available
- 100V range available

ENGINEERING TOOLS FOR HIGH VOLTAGE MLC CAPACITORS

- · Samples
- **Technical Articles**
- · Application Engineering
- · Application Support

X8R/X8L Dielectric



X8R/X8L Dielectric





Parame	ter/Test	X8R/X8L Specification Limits	Measuring (Conditions
Operating Tem	perature Range	-55°C to +150°C	Temperature C	ycle Chamber
•	itance on Factor	Within specified tolerance ≤ 2.5% for ≥ 50V DC rating ≤ 3.5% for 25V DC and 16V DC rating	Freq.: 1.0 k Voltage: 1.0	
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with rated @ room tem	
Dielectric	: Strength	No breakdown or visual defects	Charge device with 250 1-5 seconds, w/charge limited to 50 Note: Charge device with for 500V	and discharge current mA (max) n 150% of rated voltage
	Appearance	No defects	Deflectio	n: 2mm
Resistance to Flexure	Capacitance Variation	≤ ±12%	Test Time: 3	0 seconds 7 1mm/sec
Stresses	Dissipation Factor	Meets Initial Values (As Above)		
	Insulation Resistance	≥ Initial Value x 0.3	90 r	mm —
Solder		≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic sol ± 0.5 se	
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±7.5%	Dip device in eutection	c solder at 260°C for
Resistance to Solder Heat	Dissipation Factor	Meets Initial Values (As Above)	60 seconds. Store at 24 ± 2 hours before r	room temperature for
Solder Heat	Insulation Resistance	Meets Initial Values (As Above)	properties.	neasuring electrical
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ± 2 hours at ro	
	Appearance	No visual defects	_	
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 r	
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	for 1000 hou	/\
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamb temperature for 24 ± 2 h	
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s 5% relative humidi	
Load Humidity	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated	l voltage applied.
Humaity	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature and humidity	for 24 ± 2 hours before
	Dielectric Strength	Meets Initial Values (As Above)	measu	iring

General Specifications





X7R formulations are called "temperature stable" ceramics and fall into EIA Class II materials. X7R is the most popular of these intermediate dielectric constant materials. Its temperature variation of capacitance is within ±15% from -55°C to +125°C. This capacitance change is non-linear.

Capacitance for X7R varies under the influence of electrical operating con-ditions such as voltage and frequency.

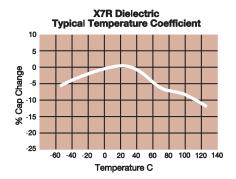
X7R dielectric chip usage covers the broad spectrum of industrial applications where known changes in capacitance

due to applied voltages are acceptable.

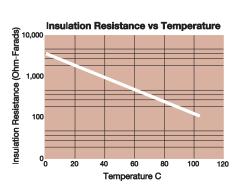
PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

0805	<u>5</u>	<u>C</u>	103	<u>M</u>	<u>A</u>	<u> </u>	<u>2</u>	<u>A</u>
Size (L" x W")	Voltage 4V = 4 6.3V = 6 10V = Z 16V = Y 25V = 3 50V = 5 100V = 1 200V = 2 500V = 7	Dielectric X7R = C		Capacitance Tolerance J = ± 5%* K = ±10% M = ± 20% *≤1µF only, contact factory for additional values		Terminations T = Plated Ni and Sn Z= FLEXITERM®** *Optional termination **See FLEXITERM® X7R section	Packaging 2 = 7" Reel 4 = 13" Reel Contact Factory For Multiples	Special Code A = Std. Product

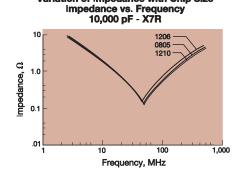
Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.



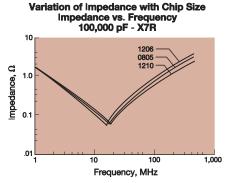
△ Capacitance vs. Frequency ∆ Capacitance -30 1KHz 10 KHz 100 KHz 1 MHz 10 MHz Frequency



Variation of Impedance with Cap Value Impedance vs. Frequency 1,000 pF vs. 10,000 pF - X7R 10.00 1.000 pF 0.10 mbedance, 0 0.01 10 Frequency, MHz



Variation of Impedance with Chip Size



Specifications and Test Methods



	ter/Test	X7R Specification Limits		Conditions
	perature Range	-55°C to +125°C	Temperature	Cycle Chamber
	on Factor	Within specified tolerance ≤ 10% for ≥ 50V DC rating≤ 12.5% for 25V DC rating ≤ 12.5% for 25V and 16V DC rating ≤ 12.5% for ≤ 10V DC rating Contact Factory for DF by PN	Voltage: 1.	kHz ± 10% 0Vrms ± .2V 0.5Vrm @ 120Hz
Insulation	Resistance	100,000ΜΩ or 1000ΜΩ - μF, whichever is less		th rated voltage for om temp/humidity
Dielectric	Strength	No breakdown or visual defects	seconds, w/charge and to 50 m Note: Charge device wi	% of rated voltage for 1-5 discharge current limited nA (max) th 150% of rated voltage / devices.
	Appearance	No defects		
Resistance to	Capacitance Variation	≤ ±12%	Deflecti	on: 2mm
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)	Test Time:	30 seconds
	Insulation Resistance	≥ Initial Value x 0.3		
Solde	rability	≥ 95% of each terminal should be covered with fresh solder		c solder at 230 ± 5°C .5 seconds
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±7.5%		
Resistance to	Dissipation Factor	Meets Initial Values (As Above)		solder at 260°C for 60 m temperature for 24 ±
Solder Heat	Insulation Resistance	Meets Initial Values (As Above)		ng electrical properties.
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)		nd measure after 24 ± 2 n temperature
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	test chamber set at 125	rated voltage (≤ 10V) in 5°C ± 2°C for 1000 hours
	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	· ·	8, -0) est voltage will be 2xRV
Load Life	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	but there are exceptions	s (please contact AVX for on exceptions)
	Dielectric Strength	Meets Initial Values (As Above)	Remove from test cham	ber and stabilize at room hours before measuring.
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%		set at 85°C ± 2°C/ 85% ± 1000 hours (+48, -0) with
Load Humidity	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	rated volta	ige applied.
·······································	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	temperature and humidi	er and stabilize at room ty for 24 ± 2 hours before
	Dielectric Strength	Meets Initial Values (As Above)	meas	suring.

Capacitance Range



PREFERRED SIZES ARE SHADED

SIZE	•	0101*		()20 ⁻	1				040	2					0	603	3						08	805								120)6			
Solderi	ing	Reflow Only		Ref	low C	nly			Ref	low/V	Vave					Reflo	ow/W	ave						Reflo	w/Wa	ve						Re	flow/	Wave	;		
Packag	ing	Paper/Embossed		Α	II Pap	er			А	II Pap	er					All	Pape	er					Pa	aper/E	mbo	ssed						Pap	er/Em	boss	ed		
(1) 1	mm	0.40 ± 0.02		0.6	0 ± 0	.09			1.0	00 ± 0	.10					1.6	0 ± 0.	15						2.01	± 0.2	0						3	.20 ±	0.20			
(L) Length	(in.)	(0.016 ± 0.0008)		(0.02	4 ± 0	.004)			(0.0	40 ± 0	.004)					(0.06	3 ± 0.	006)					(0.079	± 0.0	08)						(0.	126 ±	0.008	3)		
14/) 14/: 444	mm	0.20 ± 0.02		0.3	0 ± 0	.09			0.	50 ± 0	.10					0.8	1 ± 0.	15						1.25	± 0.2	0						1	.60 ±	0.20			
W) Width	(in.)	(0.008 ± 0.0008)		(0.01	1 ± 0	.004)			(0.0	20 ± 0	.004)					(0.03	2 ± 0.	006)					(0.049	± 0.0	(80						(0.0	063 ±	0.008	3)		
(t) Terminal	mm	0.10± 0.04		0.1	5 ± 0	.05			0.2	25 ± 0	.15					0.3	5 ± 0.	15						0.50	± 0.2	5						C	.50 ±	0.25			
	(in.)	(0.004 ± 0.0016)		(0.00	6 ± 0	.002)			(0.0	10 ± 0	.006)					(0.01	4 ± 0.	006)					(0.020	± 0.0	10)						(0.0	020 ±	0.010	J)		
WVDC		16	63	10	16	25	50	63	10	16	25	50	63	10	16	25	50	100	200	250	63	10	16	25	50	100	200	250	6.3	10	16	25	50	100	200	250	500
Cap 100	0 101	В	Α	Α	Α	Α	Α			С	С	С					G	G	G																		
(pF) 150	0 151	В	Α	Α	Α	Α	Α			С	С	С					G	G	G																		
220	0 221	В	Α	Α	Α	Α	Α			С	С	С					G	G	G		Е	Е	Ε	Ε	Ε	Е	Е										
330	0 331	В	Α	Α	Α	Α	Α			С	С	С					G	G	G			J	J	J	J	J	J										K
470	0 471	В	Α	Α	Α	Α	Α			С	С	С					G	G	G			J	J	J	J	J	J										K
680	0 681	В	Α	Α	Α	Α				С	С	С					G	G	G			J	J	J	J	J	J										K
1000	0 102	В	Α	Α	Α	Α			С	С	С	С					G	G	G	G		J	J	J	J	J	J	J								۲	K
1500	0 152	В	Α	Α	Α	Α			С	С	С	С					G	G	J	G		J	J	J	J	J	J	J		J	J	J	J	J	J	J	М
2200	0 222	В	Α	Α	Α	Α			С	С	С	С					G	G	J	G		J	J	J	J	J	J	J		J	J	J	J	J	J	J	М
3300	0 332		Α	Α	Α	Α			С	С	С	С					G	G	J	G		J	J	J	J	J	J	J		J	J	J	J	J	J	J	М
4700	0 472		Α	Α	Α	Α			С	С	С	С					G	G	J	G		J	J	J	J	J	J	J		J	J	J	J	J	J	J	М
6800	0 682		Α	Α	Α	Α			С	С	С	С					G	G	J	G		J	J	J	J	J	J	J		J	J	J	J	J	J	J	Р
Cap 0.0	1 103		Α	Α	Α	Α			С	С	С	С				G	G	G	J	G		J	J	J	J	J	J	J		J	J	J	J	J	J	J	Р
(μF) 0.015	5 153								С	С	С	С				G	G	G	J			J	J	J	J	J	J	N		J	J	J	J	J	М	J	Q
0.022	2 223								С	С	С	С				G	G	G				J	J	J	J	J	N	N		J	J	J	J	J	М	J	Q
0.033	3 333								С	С	С	С				G	G	J				J	J	J	J	N	N	N		J	J	J	J	J	М	J	Q
0.047	7 473								С	С	С	С			G	G	G	J				J	J	J	J	N	N	N		J	J	J	J	J	М	М	
0.068	8 683								С	С	С	С			G	G	G	J				J	J	J	J	N	N			J	J	J	J	J	Р	М	
0.1	1 104								С	С	С	С		G	G	G	G	J				J	J	J	J	N	N			J	J	J	J	Р	Р	Р	
0.15	5 154												G	G	G	G	J					J	J	J	N	N				J	J	J	J	Q	Q	Q	
0.22	2 224								С	С	С		G	G	J	J	J					J	J	N	N	N				J	J	J	J	Q	Q	Q	
0.33	3 334												J	J	J	J	J					N	N	N	N	N				J	J	М	Р	Q			
0.47	7 474	_						С	С				J	J	J	J	J					N	N	N	N	N				М	М	М	Р	Q			
0.68	8 684												J	J	J							N	N	N						М	М						
1.0	0 105							С					J	J	J	J	J					N	N	N	N					М	М						
2.2	2 225												J	J	J							Р	Р	Р	P**					Q	Q	Q	Q	Q**			
4.7	7 475												J									Р	Р	Р						Q	Q	Q	Z				П
10	0 106																				Р	Р	Р							Q	Q	Х					П
22	2 226																												Х	Q	Q						
4	7 476																																				
100	0 107														Ì																					П	П
WVDC	;	16	6.3	10	16	25	50	6.3	10	16	25	50	6.3	10	16	25	50	100	200	250	6.3	10	16	25	50	100	200	250	6.3	10	16	25	50	100	200	250	500
SIZE		0101*		(20	1				040	2					0	603	3						0	B05								120)6			

Letter	А	В	С	E	G	J	K	М	N	Р	Q	Х	Y	Z
Max. Thickness	0.33 (0.013)	0.22 (0.009)	0.56 (0.022)	0.71 (0.028)	0.90 (0.035)	0.94 (0.037)	1.02 (0.040)	1.27 (0.050)	1.40 (0.055)	1.52 (0.060)	1.78 (0.070)	2.29 (0.090)	2.54 (0.100)	2.79 (0.110)
			PAF	PER						EMBO	SSED			

NOTE: Contact factory for non-specified capacitance values

^{*}EIA 01005

^{**}Contact Factory for Specifications

Capacitance Range



PREFERRED SIZES ARE SHADED

	SIZE					1210)					18	312				1825				2220				2225	
5	Soldering				Re	flow (Only					Reflo	w Only	/		Re	flow O	nly		Re	flow C	nly		Re	flow 0	nly
Р	ackaging				Pape	r/Emb	ossec	<u></u>				All Em	bosse	ed .		All E	Embos	sed		All I	Embos	ssed		All E	mbos	sed
		mm				3.30 ± 0						4.50	± 0.30			4.	50 ± 0.	30		5.	.70 ± 0.	50		5.	72 ± 0.2	25
(L) Leng	jtn	(in.)				130± 0.						`	± 0.012	2)		(0.1	77 ± 0.	012)		(0.2	24 ± 0.	020)		(0.2	25 ± 0.0)10)
W) Widt	h	mm				.50 ± 0							± 0.20			ł	40 ± 0.				.00 ± 0.				35 ± 0.2	
11) Triac		(in.)				098 ± 0						<u>` </u>	± 0.008	3)		<u> </u>	52 ± 0.				97 ± 0.				50 ± 0.0	
(t) Term	inal	mm				.50 ± 0							± 0.36			ł	61 ± 0.				.64 ± 0.				64 ± 0.3	
•		(in.)	10	1.0		020 ± 0		000	500	1.0		` 	± 0.014	′ 	F00	,	24 ± 0.		0.5	_	25 ± 0.		F00	,	25 ± 0.0	
Con	100	WVDC 101	10	16	25	50	100	200	500	16	25	50	100	200	500	50	100	200	25	50	100	200	500	50	100	200
Cap (pF)	150	151																		_			-	~	-W_	· -
(pi)	220	221															<u> </u>				*	كا-	_		₩.	<
	330	331																			(1)	Ţ⊤ -
	470	471																			(_		_		
	680	681																								-
	1000	102																					t			-
	1500	152	J	J	J	J	J	J	М												ſ	1	i 1	l		Ι -
	2200	222	J	J	J	J	J	J	М																	
	3300	332	J	J	J	J	J	J	М																	
	4700	472	J	J	J	J	J	J	М																	
	6800	682	J	J	J	J	J	J	М																	
Сар	0.01	103	J	J	J	J	J	J	М		K	K	K	K	K	М	М	М		Х	Х	Х	Х	М	Р	Р
(μF)	0.015	153	J	J	J	J	J	J	Р		K	K	K	K	М	М	М	М		Х	Х	Х	Х	М	Р	Р
	0.022	223	J	J	J	J	J	J	Q		K	K	K	K	Р	М	М	М		Х	Х	Х	Х	М	P	P
	0.033	333	J	J	J	J	J	J	Q		K	K	K	K	Х	М	М	М		X	X	X	Х	М	P	P
	0.047	473	J	J	J	J	J	J	Q		K	K	K	K	Х	M	M	М		X	X	X	Х	М	Р	Р
	0.058	683 104	J	J	J	J	J	M	Q X		K K	K	K	K	X	M M	M	M M		X	X	X	X	M M	P P	P P
	0.15	154	J	J	J	J	M	Z	۸		K	K	K	P	Z	M	M	M		X	X	X	X	M	P	X
	0.13	224	J	J	J	J	P	Z			K	K	K	P	Z	M	M	M		X	X	X	X	M	P	X
	0.22	334	J	J	J	J	Q				K	K	M	X	Z	M	M	IVI		X	X	X	X	M	P	X
	0.47	474	М	М	М	М	Q				K	K	P	X	Z	M	М			X	X	X	Х	М	P	X
	0.68	684	M	М	P	X	X				M	М	Q			M	P			X	X			M	P	X
	1.0	105	N	N	Р	Х	Z				М	М	X	Z		М	Р			Х	Х			М	Р	Х
	1.5	155	N	N	Z	Z	Z				Z	Z	Z			Q				Х	Х			М	Х	Z
	2.2	225	Х	Х	Z	Z	Z				Z	Z	Z							Х	Х			М	Х	Z
	3.3	335	Х	Х	Z	Z	Z				Z	Z	Z							Х	Z					
	4.7	475	Z	Z	Z	Z	Z				Z	Z								Z	Z					
	10	106	Z	Z	Z	Z				Z										Z	Z					
	22	226	Z	Z	Z												<u> </u>		Z							
	47	476	Z																							
	100	107											4.5.5		=6.5		4				4					
	WVDC		10	16	25	50	100	200	500	16	25	50	100	200	500	50	100	200	25	50	100	200	500	50	100	200
	SIZE					1210						18	312				1825				2220				2225	
																1			1							
Le	etter	Α		В	l c		E I	G		.1	K		иΙ	N	l p		0	l x		Υ	l z					

Letter	Α	В	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z
Max. Thickness	0.33 (0.013)	0.22 (0.009)	0.56 (0.022)	0.71 (0.028)	0.90 (0.035)	0.94 (0.037)	1.02 (0.040)	1.27 (0.050)	1.40 (0.055)	1.52 (0.060)	1.78 (0.070)	2.29 (0.090)	2.54 (0.100)	2.79 (0.110)
			PA	PER						EMBO	SSED			

NOTE: Contact factory for non-specified capacitance values

General Specifications





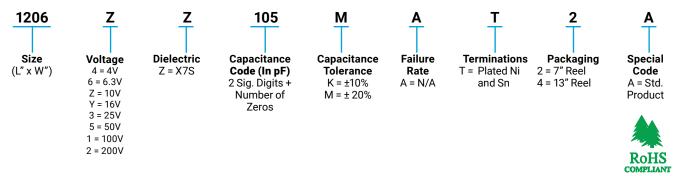
GENERAL DESCRIPTION

X7S formulations are called "temperature stable" ceramics and fall into EIA Class II materials. Its temperature variation of capacitance s within $\pm 22\%$ from -55° C to $\pm 125^{\circ}$ C. This capacitance change is non-linear.

Capacitance for X7S varies under the influence of electrical operating conditions such as voltage and frequency.

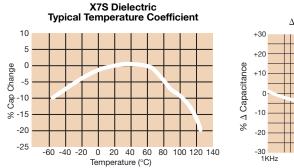
X7S dielectric chip usage covers the broad spectrum of industrial applications where known changes in capacitance due to applied voltages are acceptable.

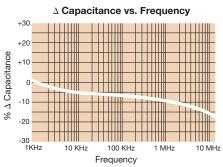
PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

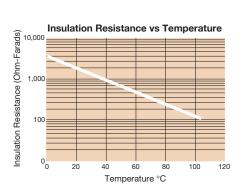


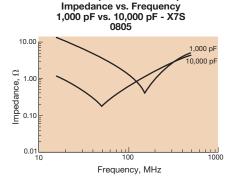
NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.

TYPICAL ELECTRICAL CHARACTERISTICS

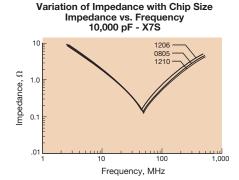


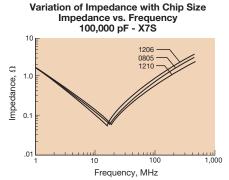






Variation of Impedance with Cap Value









Parame	ter/Test	X7S Specification Limits	Measuring	Conditions
	perature Range	-55°C to +125°C	Temperature C	ycle Chamber
Capac	itance	Within specified tolerance		
Dissipati	on Factor	≤ 5.0% for ≥ 100V DC rating ≤ 5.0% for ≥ 25V DC rating ≤ 10.0% for ≥ 10V DC rating ≤ 10.0% for ≤ 10V DC rating	Freq.: 1.0 k Voltage: 1.0 For Cap > 10 μF, 0	Vrms ± .2V
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with 120 ± 5 secs @ roo	
Dielectric	Strength	No breakdown or visual defects	Charge device with 250 1-5 seconds, w/charge limited to 50	and discharge current
	Appearance	No defects	Deflection	
Resistance to	Capacitance Variation	≤ ±12%	Test Time: 3	30 seconds 7 1mm/sec
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)		
	Insulation Resistance	≥ Initial Value x 0.3	90 1	mm —
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutection for 5.0 ± 0.	
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±7.5%	5	
Resistance to Solder Heat	Dissipation Factor	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room	temperature for 24 ± 2
	Insulation Resistance	Meets Initial Values (As Above)	hours before measuring	g electrical properties.
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ± 2 hours at ro	
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 i	
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	for 1000 hou	
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamb temperature for 24 ± 2 h	
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s 5% relative humidi	
Load Humidity	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated	
numany	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature an	nd humidity for
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours bef	ore measuring.

Capacitance Range



PREFERRED SIZES ARE SHADED

SIZE		0402		0603		0805		1206		121	0													
Solder	ing	Reflow/Wav	е	Reflow/Wav	e Re	eflow/Wave	Re	flow/Wa	ave	Reflow	Only													
Packag	jing	All Paper		All Paper		er/Embossed		er/Embo		Paper/Em														
(L) Length	mm (in)	1.00 ±		1.60 ± 0.15		2.01 ± 0.20		.20 ± 0.2		3.20 ±														
	(in.) mm	(0.040 ± 0. 0.50 ±		0.063 ± 0.00 0.81 ± 0.15		079 ± 0.008) .25 ± 0.20		26 ± 0.0		(0.126 ± 2.50 ±														
W) Width	(in.)	(0.020 ± 0.		$(0.032 \pm 0.00$		049 ± 0.008)		0.0 ± 0.0		(0.098 ±														
(t)	mm	0.25 ±		0.35 ± 0.15		0.50 ± 0.25		.50 ± 0.2		0.50 ±														
Terminal	(in.)	(0.010 ± 0.	.006)	(0.014 ± 0.00	6) (0.	020 ± 0.010)		20 ± 0.0		(0.020 ±														
Сар	WVDC 100	6.3		6.3		4	10	50	100	6.3														
(pF)	150							l	_	· 💉														
()	220							_L		\sim	>													
	330						~		_	,)	ÎT													
	470 680									1	•													
	1000						+		$\overline{}$		_													
	1500								a t	*														
	2200								, ,															
	3300																							
	4700 6800											ı												
Сар	0.010																							
(μF)	0.015																							
	0.022																							
	0.033	С																						
	0.047	С																						
	0.068	C C																						
	0.15	U																						
	0.22																							
	0.33			G																				
	0.47 0.68			G G																				
	1.0			G			+																	
	1.5			J		N																		
	2.2					N																		
	3.3					N			04															
	4.7 10					N	Q		Q*															
	22						+			Z														
	47									_														
	100																							
	WVDC	6.3		6.3		4	10	50	100	6.3														
	SIZE	0402		0603		0805		1206		121	0													
Letter	Α	С	Е	G	J	К	М		N	Р	Q		Х	X Y	Х У	Х У	XY	X Y Z	X Y Z	X Y Z	X Y Z	X Y Z	X Y Z	X Y Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27		.40	1.52	1.90		2.29				 				 			
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)		055)	(0.060)	(0.075)		(0.090)											
			PAPER							EMBC	SSED											-		

^{*}Contact Factory for Specifications

General Specifications





GENERAL DESCRIPTION

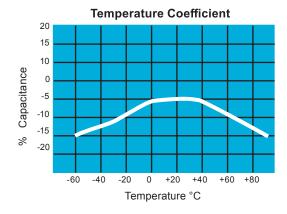
- · General Purpose Dielectric for Ceramic Capacitors
- EIA Class II Dielectric
- Temperature variation of capacitance is within ±15% from -55°C to +85°C
- Well suited for decoupling and filtering applications
- Available in High Capacitance values (up to 100μF)

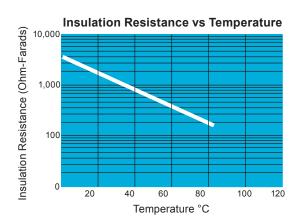
PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)



NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.

TYPICAL ELECTRICAL CHARACTERISTICS





Specifications and Test Methods



Parame	ter/Test	X5R Specification Limits	Measuring C	conditions
Operating Tem	perature Range	-55°C to +85°C	Temperature Cy	cle Chamber
Capac	itance	Within specified tolerance		
Dissipatio	on Factor	≤ 2.5% for ≥ 50V DC rating ≤ 12.5% for 25V, 35V DC rating ≤ 12.5% Max. for 16V DC rating and lower Contact Factory for DF by PN	Freq.: 1.0 kł Voltage: 1.0\ For Cap > 10 μF, 0.9	/rms ± .2V
Insulation I	Resistance	10,000MΩ or 500MΩ - μF, whichever is less	Charge device with rate secs @ room te	
Dielectric	Strength	No breakdown or visual defects	Charge device with 250° 1-5 seconds, w/charge a limited to 50	and discharge current
	Appearance	No defects	Deflection	n: 2mm
Resistance to	Capacitance Variation	≤ ±12%	Test Time: 30	
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)	V	
	Insulation Resistance	≥ Initial Value x 0.3	90 m	ım ————
Solder	ability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solo ± 0.5 sec	
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±7.5%		
Resistance to Solder Heat	Dissipation Factor	Meets Initial Values (As Above)	Dip device in eutectic 60seconds. Store at roon	n temperature for 24 ±
Coluct Float	Insulation Resistance	Meets Initial Values (As Above)	2hours before measuring	g electrical properties.
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +85°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and hours at room	
	Appearance	No visual defects	Charge device with 1.5X	rated voltage in test
	Capacitance Variation	≤ ±12.5%	chamber set at 85°C ± (+48,	2°C for 1000 hours
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	Note: Contact factory for	
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	part numbers that are to voltage	
	Dielectric Strength	Meets Initial Values (As Above)	Remove from test char room temperature	
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s ± 5% relative humidity fo	
Load	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	with rated volta	
Humidity	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature and	d humidity for
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours befo	ne measumg.

Capacitance Range



PREFERRED SIZES ARE SHADED

Case Size		01	01*			0201					04	02						0603							0805			
Soldering		Reflo	v Onlv		Re	flow C)nlv				Reflow	//Wav	——— е				Refl	ow/W	feve					Ref	low/W	feve		
Packaging		Paper/Er				II Pap						aper	-					II Pap						Pape	r/Emb	ossed		
/1 \ 1	mm	0.40	± 0.02		0.0	60 ± 0.	.09				1.00 :	± 0.15					1.6	50 ± 0.	.15					2.	01 ± 0	.20		
(L) Length	(in.)	(0.016 ±	(8000.0		(0.0	24 ± 0.	.004)			((0.040 :	± 0.00	6)				(0.06	53 ± 0.	.006)					(0.0)	79 ± 0	.008)		
\A/\ \A/: - + -	mm	0.20 :	± 0.02		0.3	30 ± 0.	.09				0.50 :	± 0.15					0.0	31 ± 0.	.15					1.:	25 ± 0	.20		
W) Width	(in.)	(0.008 ±	(8000.0			11 ± 0					0.020 :						(0.03	32 ± 0.	.006)						49 ± 0			
(t) Terminal	mm		± 0.04		0.	15 ± 0	.05	0.25 ± 0.15 0.35 ± 0.15											0.	50 ± 0	.25							
(t) Terminal	(in.)	(0.004 ±	0.0016)		(0.0)	06 ± 0	.002)			((0.010 :	± 0.00	6)				(0.0)	14 ± 0.	.006)					(0.0)	20 ± 0	.010)		
Voltage:		63	16	4	63	10	16	25	4	63	10	16	25	50	4	63	10	16	25	35	50	4	63	10	16	25	35	50
Cap (pF) 100	101		В					Α																				
150	151		В					Α																				
220	221		В					Α						С														
330	331		В					Α						С														
470	471		В					Α						С														
680	681		В					Α						С														
1000	102		В				Α	Α						С														
1500	152	В	В		-		Α	Α					ļ	С														
2200	222	В	В		-	Α	Α	Α					-	С														
3300	332	В	В		-	Α	Α	Α						С														
4700	472	В	В		-	Α	A	Α					С								G							 -
6800	682	B B	B B		-	Α	A	A		-			C						_	_	G							-
Cap (μF) 0.01 0.015	103 150	В	В		-	Α	Α	Α					C						G G	G G	G G							\vdash
0.013	223	В		_	Α	Α	Α	Α		-		С	C			_			G	G	G							N
0.022	333	В			А	A	A	А		-		C	U						G	G	G							N
0.033	473	В			Α	Α	Α	Α				C	С						G	G	G							N
0.068	689	В										C							G	-	G							N
0.000	104	В			Α	Α	Α	Α			С	C	С	С					G	G	G					N	N	N
0.15	154				-/-	- / (- / (- / (G							N	N	
0.22	224	В		Α	Α	Α				С	С	С	С	С				G	G							N	N	N
0.33	334																	G	G							N		
0.47	474	В		Α	Α				С	С	С	С	С	Е				G	J							N	Р	Р
0.68	684																	G								N		
1.0	105			Α	Α	С	С		С	С	С	С	С	Е	G	G	G	G	J	G	G				N	N	Р	Р
1.5	155																											
2.2	225			С	С	С			С	С	С	С	С		G	G	J	J	J	K	K			N	N	Р	Р	Р
3.3	335														J	J	J						N	N				
4.7	475								Е	Е	Е	Е			J	J	J	G	G			N	Р	J	N	N	Р	Р
10	106								Е	Е	Е				K	J	J	J				Р	Р	Р	Р	Р		
22	226								Е	Е					K	K	K					Р	Р	Р	Р	Р		<u> </u>
47	476														K	K						Р	Р	Р				Ļ
100	107																									<u> </u>		
Voltage:		63	16	4	63	10	16	25	4	6.3	10	16	25	50	4	63	10	16	25	35	50					50		
Case Size		01	01*			0201					04	02	0603					0805										

Letter	Α	В	С	Е	G		K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.22	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.009)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAF	PER						EMBO	SSED			

PAPER and EMBOSSED available for 01005 NOTE: Contact factory for non-specified capacitance values *EIA 01005





PREFERRED SIZES ARE SHADED

Cas	e Size				1	206							1210							1812			
Sol	dering				Reflo	w/W	ave					Re	flow C	nly					Re	flow C	nly		
Pac	kaging				Paper/	Emb	ossec	i				Pape	r/Emb	ossec	l				All I	Embos	ssed		
(L) Len	ath	mm) ± 0.							20 ± 0.							50 ± 0			
(=) ==:		(in.)			(0.126								26 ± 0.							77 ± 0			
W) Wid	ith	mm (in.)			(0.06	0 ± 0 .							50 ± 0. 98 ± 0							20 ± 0 26 ± 0.			
		mm) ± 0.							50 ± 0.							61 ± 0.			
(t) Term	inal	(in.)			(0.020								20 ± 0.							24 ± 0			
Vo	ltage:	` '	4	6.3	10	16	25	35	50	4	6.3	10	16	25	35	50	4	6.3	10	16	25	35	50
Cap (pF)	100	101																					
, ,,	150	151																					
	220	221																					
	330	331																					
	470	471																					
	680	681																					
	1000	102								1													
	1500	152																					
	2200	222																					
	3300	332																					
	4700	472																					
Can (uE)	6800 0.01	682 103																					
Cap (µF)	0.015	150															_						
	0.013	223															-						
	0.022	333																					
	0.047	473																					
	0.068	689																					
	0.1	104																					
	0.15	154																					
	0.22	224																					
	0.33	334																					
	0.47	474					Q	Q							Х	Χ							
	0.68	684																					
	1.0	105					Q	Q	Q					Χ	Х	Χ							
	1.5	155																					
	2.2	225			Q	Q	Q	Q	Q					Χ	Z	Z							
	3.3	335	V	Q	Q	V						-	-	-	_	-							
	4.7	475	X	X	X	X	X	X	X		V	Z	Z	Z	Z	Z					7		
	10 22	106		X	X	X	X	Х	Х	7	X Z	X Z	Z	Z		Z	7	7	7	7	Z		
	<u> </u>	226 476	X	X	X	X	Χ			Z Z	Z	Z	Z	Z			Z	Z	Z	Z			
	100	107	X	X	^	^				Z	Z												
Vo	Itage:	107	4	6.3	10	16	25	35	50	4	6.3	10	16	25	35	50	4	6.3	10	16	25	35	50
	e Size		•	J.J		206				•	0.0		1210		,	,	•			1812			
Lette	ar .	Α		В	С		Е		3	J	l	(М		N	Р		Q		х	Υ		Z
Max		0.33		.22				1	-		1.0	-				1.5	-	1.78	-	29	2.54		2.79
IVIAX	•	0.33	0	.ZZ	0.56		0.71	U.	90	0.94	1.0	UZ	1.27		1.40	1.5	2	1./8	2.	.29	2.54	2	/9

PAPER PAPER and EMBOSSED available for 01005

(0.028)

(0.035)

(0.037)

(0.040)

(0.050)

(0.055)

(0.060)

EMBOSSED

(0.070)

(0.090)

(0.100)

(0.110)

(0.022)

NOTE: Contact factory for non-specified capacitance values *EIA 01005

(0.013)

Thickness

(0.009)



Y5V Dielectric

General Specifications





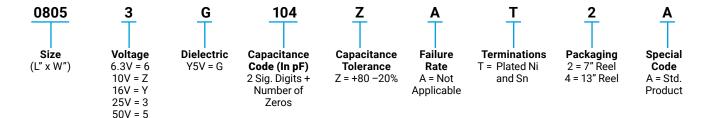
GENERAL DESCRIPTION

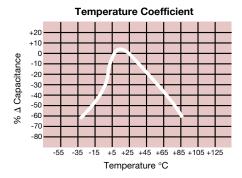
Y5V formulations are for general-purpose use in a limited temperature range. They have a wide temperature characteristic of +22% -82% capacitance change over the operating temperature range of -30°C to +85°C.

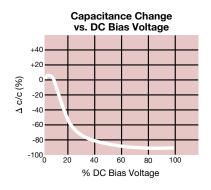
These characteristics make Y5V ideal for decoupling applications within limited temperature range.

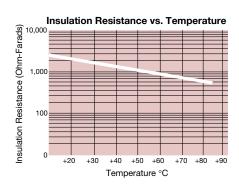


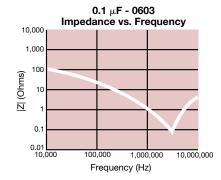
PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

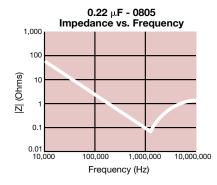


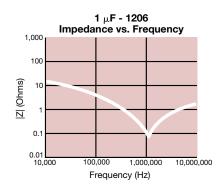












Y5V Dielectric

Specifications and Test Methods



Parame	ter/Test	Y5V Specification Limits	Measuring	Conditions
Operating Tem	perature Range	-30°C to +85°C	Temperature C	ycle Chamber
Capac	itance	Within specified tolerance		
Dissipati	on Factor	≤ 5.0% for ≥ 50V DC rating ≤ 7.0% for 25V DC rating ≤ 9.0% for 16V DC rating ≤ 12.5% for ≤ 10V DC rating	Freq.: 1.0 k Voltage: 1.0 For Cap > 10 μF, 0	Vrms ± .2V .5Vrms @ 120Hz
Insulation	Resistance	10,000MΩ or 500MΩ - μF, whichever is less	Charge device with rated @ room tem	
Dielectric	Strength	No breakdown or visual defects	Charge device with 250 1-5 seconds, w/charge limited to 50	and discharge current
	Appearance	No defects	Deflection	
Resistance to	Capacitance Variation	≤ ±30%	Test Time: 3	30 seconds 7 1mm/sec
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)		
	Insulation Resistance	≥ Initial Value x 0.1	90 1	mm —
Solder	ability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutection for 5.0 ± 0.	
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±20%		
Resistance to Solder Heat	Dissipation Factor	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room hours before measurin	temperature for 24 ± 2
	Insulation Resistance	Meets Initial Values (As Above)	Hours before measurin	g electrical properties.
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects	Step 1: -30°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±20%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +85°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ±2 hours at ro	
	Appearance	No visual defects	_	
	Capacitance Variation	≤ ±30%	Charge device with twice chamber set a	
Load Life	Dissipation Factor	≤ Initial Value x 1.5 (See Above)	for 1000 hou	/\
	Insulation Resistance	≥ Initial Value x 0.1 (See Above)	Remove from test chamb temperature for 24 ± 2 h	
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects		
	Capacitance Variation	≤ ±30%	Store in a test chamber s 5% relative humidi	
Load Humidity	Dissipation Factor	≤ Initial Value x 1.5 (See above)	(+48, -0) with rate	d voltage applied.
riumuity	Insulation Resistance	≥ Initial Value x 0.1 (See Above)	Remove from chamber temperature ar	nd humidity for
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours bef	ore measuring.

Y5V Dielectric

Capacitance Range



PREFERRED SIZES ARE SHADED

SIZE		02	01			0402				06	03			08	05			12	06			12	10	
Solderii	ng	Reflov	v Only		Ref	low/W	'ave		F	Reflow	/Wav	е		Reflow	//Wav	е	F	Reflow	Mfeve	е	F	Reflow	//Wave	e
Packagi	ng	All P	aper		Α	II Pape	er			All P	aper		Pa	per/E	mboss	sed	Pa	per/Er	nboss	sed	Pa	per/E	mboss	sed
(L) Length	mm	0.60 ±	£ 0.09		1.0	00 ± 0.	10			1.60 :	± 0.15			2.01 :	± 0.20			3.20 ±	± 0.20			3.20 :	± 0.20	
(L) Length	(in.)	(0.024 ±				10 ± 0.			(0	0.063 :	± 0.00	6)	(0		± 0.00		(0).126 ±	± 0.00	8)			± 0.00	
W) Width	mm	0.30 ±				50 ± 0.					0.15				± 0.20			1.60					± 0.20	
vv) vvidili	(in.)	(£ 0.004)			20 ± 0.			(0	0.032 :		6)	(0		± 0.00	-,	(-		± 0.00	-,	(0		± 0.00	8)
(t) Terminal	mm	0.15 ±				25 ± 0.					± 0.15				± 0.25			0.50 ±			4-		0.25	
(t) Terrimian	(in.)	(0.006 ±	,			0 ± 0.	,		,	0.014 :					± 0.01	-,	(-		± 0.01	-,	(-		± 0.01	- /
Сар	WVDC 820	63	10	6	10	16	25	50	10	16	25	50	10	16	25	50	10	16	25	50	10	16	25	50
(pF)	1000																				~	' 	₹w.	
(pr)	2200		A																١,	اسم			7	*
	4700		A																<u> </u>	(5		\Box	ŢT
Сар	0.010	Α	A																	_	$\overline{}$			
(μF)	0.022	A	_ ^																		4	-		
(μι)	0.022	A				С													_	1	. '		1 1	, 1
	0.10	,,			С	C					G	G				К				İ				i l
	0.22									G										İ				i
	0.33									G														
	0.47					С				G	G		İ		İ		İ			İ				i i
	1.0			С	С				G	G	J			N	N	N		М	М	М				N
	2.2				С				J					N	N				K	Q				
	4.7												N	N	N			Р	Q			N	N	
	10.0												N	Р			Q	Q	Х		Х	Q	Q	Х
	22.0																Q				Х	Z		
	47.0	63	10		10	16	25	50	10	16	25	50	10	10	25	- 50	10	16	25	50	10	10	25	50
SIZE	WVDC	63 02		6	10	0402	_ 25_	50	10	06		_50_	10	16	05	50	10			1 20	10	16	25]	_ 50
SIZE		02	UI			0402				- 00	UJ			Uð	UJ		1206			12	10			

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBO	SSED			

MLCC Gold Termination — AU Series







AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of Gold. This termination is indicated by the use of a "7" or "G" in the 12th position of the AVX Catalog Part Number. This fulfills AVX's commitment to providing a full range of products to our customers. Please contact the factory if you require additional information on our MLCC Gold Termination.

PART NUMBER

AU03	<u>Y</u>	G	104	<u>K</u>	<u>A</u>	7	2	<u>A</u>
Size AU02 - 0402 AU03 - 0603 AU05 - 0805 AU06 - 1206 AU10 - 1210 AU12 - 1812 AU13 - 1825 AU14 - 2225 AU16 - 0306	Voltage 6.3V = 6 10V = Z 16V = Y 25V = 3 35V = D 50V = 5 100V = 1 200V = 2 500V = 7	Dielectric COG (NPO) = A X7R = C X5R = D	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros	Capacitance Tolerance B = $\pm .10 \text{ pF} (<10 \text{pF})$ C = $\pm .25 \text{ pF} (<10 \text{pF})$ D = $\pm .50 \text{ pF} (<10 \text{pF})$ F = $\pm 1\% (\ge 10 \text{ pF})$ G = $\pm 2\% (\ge 10 \text{ pF})$ J = $\pm 5\%$ K = $\pm 10\%$ M = $\pm 20\%$	Failure Rate A = Not Applicable	Terminations G*=1.9 μ" to 7.87 μ" 7 = 100 μ" minimum	Packaging 2 = 7" Reel 4 = 13" Reel U = 4mm TR (01005) Contact Factory For Multiples*	Special Code A = Std. Product
AU06 - 1206 AU10 - 1210 AU12 - 1812 AU13 - 1825	25V = 3 35V = D 50V = 5 100V = 1 200V = 2	X5R = D		D = $\pm .50 \text{ pF} (<10 \text{pF})$ F = $\pm 1\% (\ge 10 \text{ pF})$ G = $\pm 2\% (\ge 10 \text{ pF})$ J = $\pm 5\%$	Applicable	P	(01005) Contact Factory For	Prod

^{*} Contact factory for availability.

MLCC Gold Termination - AU Series

Capacitance Range (NP0 Dielectric)



PREFERRED SIZES ARE SHADED

SIZE	312		AU02			AU	103				AU05					AL	106		
Solderii			flow/Epo			Reflow	/Epoxy/				flow/Epo					Reflow	/Epoxy/		
			Vire Bono			Wire f					Vire Bond						Bond*		
Packagi	i ng mm		All Paper .00 ± 0.1			All P					er/Embo .01 ± 0.2					Paper/E	t 0.20	<u> </u>	
(L) Length	(in.)		040 ± 0.0			(0.063 ±					0.2 ± 0.2						± 0.20 ± 0.008)		
W) Width	mm	C	0.50 ± 0.1	0		0.81 ±				1	.25 ± 0.2	20					± 0.20		
vv) vvidtn	(in.)	•	020 ± 0.0			(0.032 ±				•	0.0 ± 0.0					,	± 0.008)		
(t) Terminal	mm		.25 ± 0.1			0.35 ±					.50 ± 0.2						± 0.25		
(4)	(in.)	,	010 ± 0.0	,	16	(0.014 ±		100	16		020 ± 0.0			16	05	•	± 0.010)	000	I 1700
Cap	0.5	16 C	25 C	50 C	16 G	25 G	50 G	100 G	16 J	25 J	50 J	100 J	200 J	16 J	25 J	50 J	100 J	200 J	500 J
(pF)	1.0	С	С	С	G	G	G	G	J	J	J	J	J	J	J	Ĵ	J	J	J
	1.2 1.5	C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	1.8	C	C	C	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	2.2	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	2.7 3.3	C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	3.9	C	С	C	G	G	G	G	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	J
	4.7	C	С	C	G G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J
	5.6 6.8	C	C	C	G	G	G	G G	J	J	J	J	J	J	J	J	J	J	J
	8.2	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	10 12	C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	15	C	C	C	G	G	G	G	J	J	J	J	J	J	J	J	J	5 7	Ĵ
	18 22	C C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	27	C	C	C	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	33	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	39 47	C C	C	C	G G	G G	G G	G G	J	J	J	J	J J	J	J	J J	J	J	J
	56	C	C	C	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	68	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	82 100	C C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	120	С	С	С	G	G	G	G	Ĵ	Ĵ	Ĵ	J	Ĵ	Ĵ	J	Ĵ	Ĵ	J	Ĵ
	150 180	C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	220	C	C	C	G	G	G	G	J	J	J	J	J	J	J	J	J	J	M
	270	С	С	С	G	G	G	G	J	J	J	J	М	J	J	J	J	J	М
	330 390	C	C	C	G G	G G	G G	G	J	J	J	J	M M	J	J	J	J	J	M
	470	Č	Č	Č	G	G	G		J	J	J	J	М	J	J	Ĵ	J	J	М
	560				G	G	G G		J	J	J	J	M	J	J	J	J	J	M P
	680 820				G G	G G	G		J	J	J	J	M M	J	J	J	J	J M	P
	1000				G	G	G		J	J	J	J	М	J	J	J	J	Q	
	1200 1500								J	J	J			J	J	J	J M	Q Q	
	1800								J	J	J			J	J	М	М	~	
	2200 2700								J	J	N N			J	J	M M	P P		
	3300								J	J	IN			J	J	M	P		
	3900								J	J				J	J	М	Р		
	4700 5600								J	J				J	J	M M	Р		-
	6800													М	М				
	8200 0.010			2002										M M	M M				
	0.012						-W,												
	0.015						\mathcal{I}_{\leq}	<u> </u>											
	0.018 0.022			_			リノ	ŢΤ											
	0.027		(_)]_	/													
	0.033	_																	
	0.039				₹ T														
	0.068		ı	ı		I	I	_											
	0.082																		
	WVDC	16	25	50	16	25	50	100	16	25	50	100	200	16	25	50	100	200	500
	SIZE		AU02			AU	103				AU05					AL	106		

* Contact Factory

Letter	Α	C	E	G	J	K	M	N	P	Q	X	Y	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMB	OSSED			

Capacitance Range (NPO Dielectric)



PREFERRED SIZES ARE SHADED

SIZE				AU10					AU12				AU13			AU14	
Solderii	ng			low/Epo					low/Epo				Reflow/Epoxy	/		Reflow/Epoxy	/
Packagi				/ire Bond er/Embos					/ire Bond Emboss				Wire Bond*			Wire Bond* All Embossed	
	mm			.20 ± 0.2					.50 ± 0.3				4.50 ± 0.30			5.72 ± 0.25	
(L) Length	(in.)		(0.1	126 ± 0.0	08)			(0.1	77 ± 0.0	112)			(0.177 ± 0.012	2)		(0.225 ± 0.010))
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	mm			.50 ± 0.2				3	.20 ± 0.2	20			6.40 ± 0.40			6.35 ± 0.25	
W) Width	(in.)			0.0 ± 0.0					26 ± 0.0				(0.252 ± 0.016)	((0.250 ± 0.010))
(t) Terminal	mm			.50 ± 0.2					.61 ± 0.3				0.61 ± 0.36			0.64 ± 0.39	
(t) Terminal	(in.)		· `	020 ± 0.0					24 ± 0.0				(0.024 ± 0.014	<u></u>		0.025 ± 0.015	<u></u>
	WVDC	25	50	100	200	500	25	50	100	200	500	50	100	200	50	100	200
Cap (pF)	0.5 1.0																
(pi)	1.2																
	1.5																
	1.8																-W-
	2.2 2.7														<		
	3.3																
	3.9																
	4.7 5.6																
	6.8															Tt I	
	8.2																
	10					J											
	12 15					J J											
	18					J											
	22					J											
	27 33					J											
	39					J											
	47					J											
	56					J											
	68 82					J J											
	100					J											
	120					J											
	150 180					J											
	220					J											
	270					J											
	330 390					J M											
	470					M											
	560	J	J	J	J	М											
	680	J	J	J	J	M											
	820 1000	J	J	J	J	M M	K	K	K	K	М	М	M	М	М	М	Р
	1200	J	J	J	М	М	K	K	K	K	М	М	M	M	М	M	P
	1500	J	J	J	M	М	K	K	K	K	M	M	M	M	M	M	P
	1800 2200	J	J	J	M Q		K K	K K	K K	K K	M P	M M	M M	M M	M M	M M	P P
	2700	J	J	J	Q		K	K	K	Р	Q	М	М	М	М	М	Р
	3300	J	J	J			K	K	K	Р	Q	М	М	М	М	M	P
	3900 4700	J	J	M M			K K	K K	K K	P P	Q	M M	M M	M M	M M	M M	P P
	5600	J	J	IVI			K	K	M	P	X	M	M	M	M	M	P
	6800	J	J				K	K	М	Х		М	М	М	М	М	Р
	8200 0.010	J J	J				K K	M	M M			M M	M M		M M	M M	P P
	0.010	J	J				K	M	141			M	M		M	M	P
	0.015						М	М				М	М		М	М	Υ
	0.018						M	M				Р	M		M	M	Y
	0.022 0.027						M M	M M				P P			M P	Y	Y
	0.033						M	M				Р			Р		
	0.039						М	М				Р			Р		
-	0.047 0.068						M M	M M				Р			P P		
	0.088						M	M							Q		
			I	I	1				1	l			l		Q	I	l
	0.1																
	0.1 WVDC	25	50	100 AU10	200	500	25	50	100 AU12	200	500	50	100 AU13	200	50	100 AU14	200

* Contact Factory

Letter	Α	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
	PAPER								EMB	DSSED			





PREFERRED SIZES ARE SHADED

SIZE			AU	102					AU03	3						AU0	5						Αl	J06			
Solderin	ng	F		/Epoxy Bond*	1/				ow/E		'					ow/E	poxy	'/					eflow Wire				
Packagi	ng		All P	aper				Α	All Par	er				F	ape	r/Em	boss	ed				Pa	per/E	mbos	sed		
(I) Ith-	mm		1.00 :	± 0.10				1.	60 ± 0).15					2.	01 ±	0.20						3.20	± 0.20)		
(L) Length	(in.)	((0.040 :	± 0.004	4)			(0.0	63 ± 0	0.006)				(0.0)	79 ±	0.008	3)				(0	.126	± 0.00	08)		
W) Width	mm			± 0.10					81 ± 0							25 ±							1.60				
vv) vvidur	(in.)	((± 0.004	4)				32 ± 0)				<u> </u>		0.008	3)				(0	.063				
(t) Terminal	mm	,		± 0.15	٠.				35 ± 0							50 ±						,_	0.50				
	(in.)			± 0.006		- 60	10		14 ± 0			1 000			, ` 		0.010	·—	1 000	- 60	10		.020		, 	1000	
WVDC		10	16	25	50	63	10	16	25	50	100	200	63	10	16	25	50	100	200	63	10	16	25	50	100	200	500
Cap	100																										
(pF)	150																										
	220 330				С				G																		1.6
	I				С					G	G	G		J	J	J	J	J	J								K
	470				С					G	G	G		J	J	J	J	J	J								K
	680				С					G	G	G		J	J	J	J	J	J			_	-	<u> </u>	1	-	K
	1000				С					G	G	G		J	J	J	J	J	J								K
	1500				С					G	G			J	J	J	J	J	J		J	J	J	J	J	J	М
	2200				С					G	G			J	J	J	J	J	J		J	J	J	J	J	J	М
	3300			С	С					G	G			J	J	J	J	J	J		J	J	J	J	J	J	М
	4700			С	С					G	G			J	J	J	J	J	J		J	J	J	J	J	J	M
	6800		С	С						G	G			J	J	J	J	J	J		J	J	J	J	J	J	Р
Сар	0.010		С					G		G	G			J	J	J	J	J	J		J	J	J	J	J	J	Р
(μF)	0.015		С						G	G				J	J	J	J	J	J		J	J	J	J	J	M	
(F-)	0.022	С	С						G	G				J	J	J	J	J	N		J	J	J	J	J	М	
	0.033	С							G	G				J	J	J	J	N			J	J	J	J	J	M	
	0.047							G	G	G				J	J	J	J	N			J	J	J	J	J	M	
	0.068							G	G	G				J	J	J	J	N			J	J	J	J	J	Р	
	0.10						G	G	G	G				J	J	J	J				J	J	J	J	М	Р	
	0.15					G	G							J	J	J	N	N			J	J	J	J	Q		
	0.22					G	G							J	J	N	N	N			J	J	J	J	Q		
	0.33													N	N	N	N	N			J	J	М	Р	Q		
	0.47													N	N	N	N	N			М	М	М	Р	Q		
	0.68													N	N	N					М	М	Q	Q	Q		
	1.0													N	N	N					М	М		Q	Q		
	1.5																				Р	Q	Q				
	2.2															P*					Q	Q	Q				
	3.3																										
	4.7													P*							Q	Q					
	10																				Q*						
	22																			Q*							
	47																										
	100																										
	WVDC	10	16	25	50	6.3	10	16	25	50	100	200	63	10	16	25		100	200	63	10	16	25	50	100	200	500
	SIZE			AU02	2				AU03	3						AU0	5						AL	J06			

^{*} Contact Factory

Letter	Α	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBO	DSSED			





PREFERRED SIZES ARE SHADED

mm 0.50 ± 0.25 0.61 ± 0.36 0.61 ± 0.36 0.64 ± 0.39	SIZE					AU10					AU	J12		Δ.	\U13		AU14
Packaging Packaging Packaging Packaging Packaging Packaging Packaging Packaging Packaging Packaging Packaging Packaging All Embossed All Embos	Caldarina																
(L) Length (n) (0.126 ± 0.008) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012) (0.225 ± 0.016) (0.177 ± 0.012)	Soldering	J				Wire Bor	nd*				Wire I	Bond*		Wire	e Bond*	Wi	re Bond*
(L) Length (n)	Packaging	g												l			
Width mm	(L) Length															1	
(t) Terminal (n)	(L) Length				•		,				•	,		١ ،	,	,	•
(t) Terminal (in)	W) Width																
(1) Terminal (In)		. ,															
Cap (pF) 150 (pF) 220	(t) Terminal	(in.)			(0	.020 ± 0	.010)										
Cap (pF) 220	WVDC		10	16	25	50	100	200	500	50	100	200	500	50	100	50	100
(pF)		100												_		<i>-</i>	-
1000														_			
470 680	(βι)												_<	<) 1 —
1000																ر ا)
1000													1				
1500 J J J J J J J M M M M M P Cap 0.010 J J J J J J M M M M M P Cap 0.033 J J J J J J J J J J M M M M M P Cap 0.068 J J J J J J J J M M K K K K K Z M M M M P Cap 0.010 J J J J J J M K K K K K K Z M M M M P Cap 0.033 J J J J J J M K K K K K K Z M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J M K K K K K P M M M M P Cap 0.068 J J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M M P Cap 0.068 J J J J J J M K K K K K Z M M M M M P Cap 0.068 J J J J J J M K K K K Z M M M M M P Cap 0.068 J J J J J J M K K K K Z M M M M M P Cap 0.068 M M M P Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z													_				
2200			.l	.l	.l	.1	.l	.I	M						4		
3300 J J J J J J J J M M M M M P M M M M P M M M M							-							ı	' '	I	ı
Cap		3300															
Cap (µF) 0.015		4700	J	J	J	J	J	J	М								
Cap (μF) 0.015 (μF) J J J J J J J J J J M M M M P 0.022 J J J J J J J J J J M M M M M M P 0.033 J J J J J J J J J J J M M M M M M P 0.047 O.068 J J J J J J J J J J J M K K K K K K K K K K K K K K K K F M M M P M M M P M M M P M M M M P M M M P			J	J	J	J	J	J	М								
(μF) 0.022 J J J J J J J Q K K K P M M M M P 0.033 J J J J J J J J G K K K K P M M M M P 0.047 J J J J J J J K K K K Z M M M M P 0.068 J J J J J J M K K K K Z M M M M P 0.10 J J J J J M K K K K Z M M M P 0.11 J J J J M Z K K K F P M M M M P 0.12 J J J J M Z K K K P M M M M P 0.15 J J J J J M Z K K K P M M M M P 0.22 J J J J J P Z K K K P M M M M P 0.33 J J J J J J P Z K K K P M M M M P 0.47 M M M M M Q K P M Q K P M M M M P 1.0 N N Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z	Can			-	-	-	-	-									
0.022																	
0.047	,			_					-								
0.068			-	-	-	-	-	-	Q								
0.10 J J J J J J M Z K K P M M M P 0.15 J J J J J P Z K K P M M M P 0.22 J J J J J Q K K P M M M P 0.33 J J J J J Q K M X M M M P 0.47 M M M M M Q K P 0.68 M M P X X Z M M P M P 1.0 N N Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z																	
0.15				_		-											
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0.47 M M M M Q K P M M P M P 0.68 M M P X X X X M P M P M P 1.0 N N X Z Z Z Z M P M P M P 1.5 N N Z Z Z Z Z M M Y M P M P M P M P M P M P M P M N X Z Z Z Z Z Z Z M X X X X X X X X Z Z Z Z Z Z X X X X X X X X X X X X <td< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>Р</td><td></td><td></td><td></td><td></td><td></td></td<>												Р					
0.68 M M P X X M Q M P M P 1.0 N N X Z X Z M X M P M N X Z <td></td> <td>0.33</td> <td>J</td> <td>J</td> <td>J</td> <td>J</td> <td>Q</td> <td></td> <td></td> <td>K</td> <td>М</td> <td>Х</td> <td></td> <td>М</td> <td>М</td> <td>М</td> <td>Р</td>		0.33	J	J	J	J	Q			K	М	Х		М	М	М	Р
1.0 N N N Z Z Z Z Z M M X M P M P M P M X X Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z				М			-							М		М	
1.5 N N Z Z Z Z Z M M M X 2.2 X X Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z					Р										-		•
2.2 X X Z Z Z W 3.3 X X Z Z Z Z 4.7 X X Z Z Z Z 10 Z Z Z Z Z 47 T															Р		
3.3 X X Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z														M			X
4.7 X X Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z							Z				Z					IVI	
10 Z Z Z																	
22 47 100 WVDC 10 16 25 50 100 200 500 50 100 200 500 50 100 50						_											
47 100 WVDC 10 16 25 50 100 200 500 50 100 200 500 50 100 50 100																	
WVDC 10 16 25 50 100 200 500 50 100 200 500 50 100 50 100																	
		100															
SIZE AU10 AU12 AU13 AU14		WVDC	10	16	25	50	100	200	500	50	100	200	500	50	100	50	100
		SIZE				AU10					AU	112		Δ	.U13		AU14

^{*} Contact Factory

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBC	SSED			





PREFERRED SIZES ARE SHADED

SIZ	Έ			Αl	J02					-	U0	3					Αl	J05					ΑU	106					-	\U1	0				AU	12	
Solde	ring				/Ep Bon				F	Reflo		pox ond*					flow Vire						flow /ire l								pox ond*					/Epo: Bond	
Packa	ging		/	All F	аре	er				ΑII	Pa	per			F	Раре	er/E	mbo	sse	d	F	аре	er/Er	mbo	sse	d		Pa	per/	Em	bos	sed		All	Emb	ooss	ed
(L)	mm		1	.00	± 0.1	0				1.6	0 ± 0).15				- 2	2.01	± 0.2	:0			3	.20 :	± 0.2	0				3.2	0 ± 0	0.20			4	.50 ±	± 0.30)
Length	(in.)		(0.	040	± 0.0	004)			(0.06	3 ± 0	0.006	5)			(0.	079	± 0.0	(80			(0.	126 :	± 0.0	(80			(0.12	6 ± 0	0.008	3)		(0.	177 ±	± 0.01	12)
W)	mm		C	.50	± 0.1	0				0.8	1 ± ().15					1.25	± 0.2	0			1	.60 :	± 0.2	0				2.5	0 ± 0	0.20			3	.20 ±	± 0.20)
Width	(in.)		(0.	020	± 0.0	004)			(0.03	2 ± (0.006	5)			(0.	049	± 0.0	(80			(0.	063 :	± 0.0	08)			(0.09	8 ± (0.008	3)		(0.	126 ±	± 0.00	08)
	mm												_																					<u> </u>			
(t)	(in.)				± 0.1						5 ± (0.50						.50 :							0 ± 0						± 0.36	
Terminal	(111.)		(0.	010	± 0.0	006)			(0.01	4 ± (0.006	5)			(0.	020	± 0.0	10)			(0.	020 :	± 0.0	10)			(0.02	0 ± 0	0.010))		(0.)24 ±	± 0.01	14)
WVD	oc	4	63	10	16	25	50	4	63	10	16	25	35	50	63	10	16	25	35	50	6.3	10	16	25	35	50	4	6.3	10	16	25	35	50	6.3	10	25	50
Сар	100																																				П
(pF)	150																																				
	220				L					L		L												L					L				L			L	
	330						С																														П
	470						С																														
	680						С																														
	1000						С																														
	1500						С																														
	2200						С																														
	3300						С																														
	4700					С								G																							
	6800					С								G																			<u> </u>				Ш
Сар	0.010					С						_	-	G																							
(µF)	0.015 0.022					С						G	G	G																							
	0.022	\vdash			С	С						G	G	G						N				-									H			┢	H
	0.033				C		l					G	G	G						N																	
	0.047				C	С	1					G	G	G						N N																	
	0.008	\vdash	С		С	С		\vdash		_	\vdash	G		G		H		N		N		_									\vdash		┢			⊢	Н
	0.15		C		C							G		G				N	N	IN																	
	0.13		C*								G	G						N	N																		
	0.33	\vdash									G	G						N	-							Q							\vdash			\vdash	H
	0.47	C*									G							N						Q	Q	~											
	0.68	-									G							N															Х				1
	1.0	П				t			G	G	G	J*			N		N	N		P*				Q	Q			П			Х	Х	Х				П
	1.5														N				1																		
	2.2	C*						G*	G*	J*	J*				N	N	N	N					Q	Q							Z	Х	Ĺ				
	3.3							J*	J*	J*	J*				N	N					Q	Q															П
	4.7							J*	J*	J*						N	N*	N*			Q	Q	Q	Q						Q	Z						
	10							K*							P*	P*	P*				Q	Q	Q	Q*					Х	Z	Z		L			Z	Ш
	22														P*						Q*	Q*	Q*					Z	Z	Z	Z						
	47																				Q*						_	Z*									
	100																		-					-			Z*	Z*								L	
	WVDC	4	63	_		25	50	4	63				35	50	63	10			35	50	6.3	10			35	50	4	6.3			_	35	50	6.3		25	50
	SIZE			Αl	J02						\U0	ა					Αl	J05					AU	106					_ /	\U1	U				AU	12	

^{*} Contact Factory

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBC	SSED			

= *Optional Specifications - Contact Factory

NOTE: Contact factory for non-specified capacitance values

AU16/AU17/AU18



	ZE		(AU1 030	6)			(0	U17 508	3)			(AU1 0612	2)	
Pack	aging			nbos					boss					nboss		
Length	mm			31 ± 0					7 ± 0.					0 ± 0		
20119411	(in.)			32 ± 0)	(0 ± 0. 0 ± 0.			<u> </u>		3 ± 0 0 ± 0	.010)	
Width	mm (in.)			50 ± 0		١	۱ ،		0 ± 0. 0 ± 0.						i.25 i.010)	
Cap Code	WVDC	4	6.3	10	16	25	6.3	10	16	25	50	6.3	10	16	25	50
102	Cap 0.001		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧
222	(μF) .0022		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧
332	0.0033		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧
472	0.0047		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧
682	0.0068		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧
103	0.01		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧
153	0.015		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	W
223	0.022		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	W
333	0.033		Α	Α	Α		S	S	S	٧	٧	S	S	S	S	W
473	0.047		Α	Α	Α		S	S	S	٧	Α	S	S	S	S	W
683	0.068		Α	Α	Α		S	S	S	Α	Α	S	S	S	٧	W
104	0.1		Α	Α	///		S	S	٧	Α	Α	S	S	S	٧	W
154	0.15		Α	Α			S	S	٧			S	S	S	W	W
224	0.22		Α	Α			S	S	Α			S	S	٧	W	
334	0.33						٧	٧	Α			S	S	٧		
474	0.47						٧	٧	1			S	S	٧		
684	0.68						Α	Α				٧	>	V		
105	1	A					Α	Α				٧	٧	Α		
155	1.5						//					W	W			
225	2.2											Α	Α			
335	3.3											/				
475	4.7															
685	6.8															
106	10															

Solid = X7R



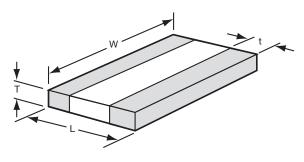


mm (in.) AU16 (0306)Code Thickness A 0.56 (0.022)

	mm (in.)
	AU16
-	(0508)
Code	Thickness
S	0.56 (0.022)
V	0.76 (0.030)
Α	1.02 (0.040)

	mm (in.)
	AU16
	(0612)
Code	Thickness
S	0.56 (0.022)
V	0.76 (0.030)
W	1.02 (0.040)
Α	1.27 (0.050)

PHYSICAL DIMENSIONS AND PAD LAYOUT



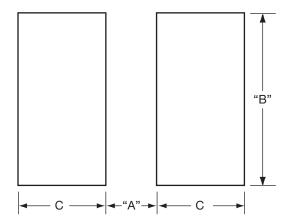
MM (IN.) **PHYSICAL DIMENSIONS**

	L	W	t
AU16	0.81 ± 0.15	1.60 ± 0.15	0.13 min.
(0306)	(0.032 ± 0.006)	(0.063 ± 0.006)	(0.005 min.)
AU17	1.27 ± 0.25	2.00 ± 0.25	0.13 min.
(0508)	(0.050 ± 0.010)	(0.080 ± 0.010)	(0.005 min.)
AU18	1.60 ± 0.25	3.20 ± 0.25	0.13 min.
(0612)	(0.063 ± 0.010)	(0.126 ± 0.010)	(0.005 min.)

T - See Range Chart for Thickness and Codes

PAD LAYOUT DIMENSIONS MM (IN.)

	Α	В	С
AU16 (0306)	0.31 (0.012)	1.52 (0.060)	0.51 (0.020)
AU17 (0508)	0.51 (0.020)	2.03 (0.080)	0.51 (0.020)
AU18 (0612)	0.76 (0.030)	3.05 (0.120)	0.635 (0.025)



MLCC Tin/Lead Termination "B" (LD Series)

COG (NPO) - General Specifications





AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a "B" in the 12th position of the AVX Catalog Part Number. This fulfills AVX's commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special "B" termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination "B" products.

Not RoHS Compliant

PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

LD05	5	<u>A</u>	101	<u> </u>	<u>A</u>	<u>B</u>	2	<u>A</u>
Size LD02 - 0402 LD03 - 0603 LD04 - 0504* LD05 - 0805 LD06 - 1206 LD10 - 1210 LD12 - 1812 LD13 - 1825 LD14 - 2225 LD14 - 2225	Voltage 6.3V = 6 10V = Z 16V = Y 25V = 3 35V = D 50V = 5 100V = 1 200V = 2 500V = 7	Dielectric COG (NPO) = A X7R = C X5R = D X8R = F	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros	Capacitance Tolerance B = ±.10 pF (<10pF) C = ±.25 pF (<10pF) D = ±.50 pF (<10pF) F = ±1% (≥ 10 pF) G = ±2% (≥ 10 pF) J = ±5% K = ±10% M = ±20%	Failure Rate A = Not Applicable	Terminations B = 5% min lead X = FLEXITERM® with 5% min lead** **X7R only	Packaging 2 = 7" Reel 4 = 13" Reel Contact Factory For Multiples*	Special Code A = Std. Product

△ Capacitance vs. Frequency

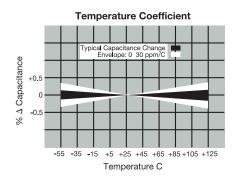
 $M = \pm 20\%$

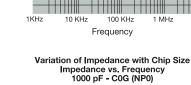
*LD04 has the same CV ranges as LD03.

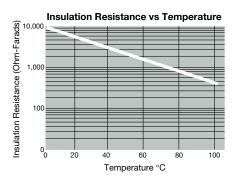
See FLEXITERM® section for CV options

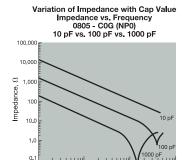
NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.

A Capacitance

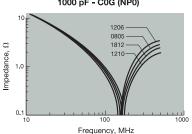


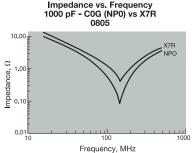






Frequency, MHz





Variation of Impedance with Ceramic Formulation

The Important Information/Disclaimer is incorporated in these specifications by reference and should be reviewed in full before placing any order.





Parame	ter/Test	NP0 Specification Limits	Measuring Conditions				
Operating Tem	perature Range	-55°C to +125°C	Temperature Cycle Chamber				
Capac	itance	Within specified tolerance	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF				
(2	<30 pF: Q≥ 400+20 x Cap Value ≥30 pF: Q≥ 1000	1.0 kHz ± 10% for cap > 1000 pF Voltage: 1.0Vrms ± .2V				
Insulation	Resistance	100,000ΜΩ or 1000ΜΩ - μF, whichever is less	Charge device with rated voltage for 60 ± 5 secs @ room temp/humidity				
Dielectric	: Strength	No breakdown or visual defects	Charge device with 250% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.				
	Appearance	No defects	Deflection: 2mm				
Resistance to Flexure	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 30 seconds 7 1mm/sec				
Stresses	Q	Meets Initial Values (As Above)					
	Insulation Resistance	≥ Initial Value x 0.3	90 mm —				
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds				
	Appearance	No defects, <25% leaching of either end terminal					
	Capacitance Variation Q Insulation Resistance Dielectric	≤ ±2.5% or ±.25 pF, whichever is greater	Dia during in contrasting allows a 00000 for 00				
Resistance to Solder Heat	Q	Meets Initial Values (As Above)	Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2				
Solder Fleat		Meets Initial Values (As Above)	hours before measuring electrical properties.				
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects	Step 1: -55°C ± 2° 30 ± 3 minutes				
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp ≤ 3 minutes				
Thermal Shock	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2° 30 ± 3 minutes				
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp ≤ 3 minutes				
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature				
	Appearance	No visual defects					
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twice rated voltage in test chamber set at 125°C ± 2°C				
Load Life	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	for 1000 hours (+48, -0). Remove from test chamber and stabilize at room				
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	temperature for 24 hours before measuring.				
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects					
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater	Store in a test chamber set at 85°C ± 2°C/ 85% ±				
Load Humidity	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.				
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring.				
	Dielectric Strength	Meets Initial Values (As Above)					

C0G (NP0) - Capacitance Range



PREFERRED SIZES ARE SHADED

		LD02 LD03																	
SIZE	= [LD02			LD	03				LD05					LD0	6		
Solder	ing	Re	eflow/Wa	ave		Reflow	v/Wave			Re	flow/Wa	ve				Reflow/\	Wave		
Packag			All Pape				aper				er/Embos				Pa	aper/Eml			
(L) Length	mm (in.)		.00 ± 0.1 040 ± 0.0				± 0.15 ± 0.006)				.01 ± 0.2 .79 ± 0.0				(3.20 ± (0.126 ± (
W) Width	mm (in.)		0.50 ± 0.1 020 ± 0.0				± 0.15 ± 0.006)				.25 ± 0.2 049 ± 0.0				(1.60 ± (0.063 ± (
(t) Terminal	mm	0	.25 ± 0.1	5		0.35	± 0.15			0	.50 ± 0.2	5				0.50 ± 0	0.25		
(7)	(in.) WVDC	16	010 ± 0.0 25	50	16	25	± 0.006) 50	100	16	25	020 ± 0.0 50	100	200	16	25	0.020 ± 0	100	200	500
Cap (pF)	0.5 1.0	C C	C	C	G	G G	G G	G	J	J	J	J	J J	J	J	J	J	J	J J
(P.)	1.2	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	1.5 1.8	C C	C	C	G	G G	G	G	J	J	J	J	J J	J	J	J	J	J	J J
	2.2	C C	C	C	G G	G G	G G	G G	J	J J	J J	J	J J	J	J	J	J J	J J	J J
	3.3	C	C	C	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	3.9 4.7	C C	C	C	G	G G	G G	G	J	J	J J	J	J J	J	J	J	J	J	J J
	5.6	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	6.8 8.2	C	C	C	G	G G	G G	G	J	J	J	J	J J	J	J	J	J	J	J
	10 12	C	C	C	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J
	15	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	18 22	C C	C	C	G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J J
	27	C	С	C	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	33 39	C	C	C	G	G G	G	G	J	J	J	J	J	J	J	J	J	J	J
	47 56	C	C	C	G	G G	G G	G	J	J	J	J	J J	J	J	J	J	J	J
	68	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	82 100	C C	C	C	G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J
	120 150	C C	C	C	G G	G G	G G	G	J	J	J	J	J	J	J	J	J	J	J J
	180	С	С	С	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J
	220 270	C	C	C	G	G G	G G	G	J	J	J	J	J M	J	J	J	J	J	M M
	330 390	C C	C	C	G G	G G	G G	G G	J	J	J	J	M M	J	J	J	J	J	M M
	470	C	C	C	G	G	G	J	J	J	J	J	М	J	J	J	J	J	М
	560 680				G G	G G	G G		J	J	J J	J	М	J	J	J	J	J	M P
	820 1000				G G	G G	G G		J	J	J	J		J	J	J	J	M Q	
	1200				G	G	9		J	J	J	J		J	J	J	J	Q	
	1500 1800								J	J	J			J	J	J M	M M	Q	
	2200 2700								J J	J J	N N			J J	J	M M	P P		
	3300								J	J	IN			J	J	М	Р		
	3900 4700								J	J				J	J	M M	P P		
	5600 6800													J M	J M	М			
	8200													М	М				
Cap (pF)	0.010 0.012													М	М				
	0.015		<u> </u>	_		4	l _	1											
	0.018 0.022		•	مركا-		W-	*												
	0.027		 			لل	Ĵ T −	+											
	0.039		`		<u> </u>														
	0.047 0.068					_	+												
	0.082 0.1																		
	WVDC	16	25	50	16	25	50	100	16	25	50	100	200	16	25	50	100	200	500
	SIZE	LD02 LD03				003		LD05					LD06						
Letter	A 0.33	C E G J 0.56 0.71 0.90 0.94				K 1.02						Q X Y Z							
Max. Thickness	(0.013)		0.56 0.71 0.90 (0.022) (0.028) (0.035)				.037)	(0.040)					1.78 2.29 2.54 2.79 (0.070) (0.090) (0.100) (0.110)						
		,	(0.022) (0.028) PAPER			. (3.	,		(, ,			BOSSED						

C0G (NP0) - Capacitance Range

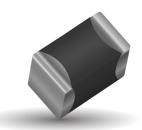


PREFERRED SIZES ARE SHADED

SIZ	7E			LD10					LD12				LD13			LD14	
Solde			F	Reflow On	ıly			R	Reflow Or	nly			Reflow Only			Reflow Only	
Packa	ging		Pap	er/Embo	ssed			Al	l Emboss	sed			All Embosse	d .		All Embossed	
(L) Length	mm (in.)			3.20 + 0.2 126 ± 0.0					1.50 ± 0.3 177 ± 0.0				4.50 ± 0.30 (0.177 ± 0.01)))		5.72 ± 0.25 0.225 ± 0.010)
W) Width	mm		- 2	2.50 ± 0.2 098 ± 0.0	.0			3	3.20 ± 0.2 126 ± 0.0	20			6.40 ± 0.40			6.35 ± 0.25 0.250 ± 0.010	,
(t) Terminal	(in.) mm			0.50 ± 0.2	:5				0.61 ± 0.3	36			0.252 ± 0.010 0.61 ± 0.36			0.64 ± 0.39	
(4)	(in.) WVDC	25	(0. 50	020 ± 0.0 100	200	500	25	50	024 ± 0.0 100	200	500	50	(0.024 ± 0.014 100	200	50	0.025 ± 0.015 100	200
Cap	0.5 1.0																
(pF)	1.2																
	1.5 1.8																
	2.2																₩ →
	2.7 3.3												+) <u>T</u> T
	3.9 4.7																1
	5.6															a-t	
	6.8 8.2																I
	10 12					J											
	15					J											
	18 22					J											
	27					J											
	33 39					J											
	47 56					J											
	68					J											
	82 100					J											
	120 150					J J											
	180					J											
	220 270					J											
	330 390					J											
	470					M M											
	560 680	J	J	J	J	M M											
	820	J	J	J	J	М	17	1/	1/	I/							D
	1000 1200	J	J	J	J M	M M	K K	K	K K	K K	M M	M M	M M	M M	M M	M M	P P
	1500 1800	J	J	J	M M	М	K K	K	K K	K K	M M	M M	M M	M M	M M	M M	P P
	2200	J	J	J	Q		K	K	K	K	Р	M	М	М	M	М	P
	2700 3300	J	J	J	Q		K	K	K K	P P	Q Q	M M	M M	M	M M	M M	P P
	3900 4700	J	J	M M			K K	K K	K K	P P	Q	M M	M M	M M	M M	M M	P P
	5600	J	J	IVI			K	K	М	Р	X	М	М	М	М	М	Р
	6800 8200	J J	J				K K	K M	M M	Χ		M M	M M	М	M M	M M	P P
Cap (pF)	0.010	J J	J				K	М	М			M	M M		M	М	P P
(pi)	0.012 0.015	J	J				K M	M M				M M	M		M M	M M	Υ
	0.018 0.022						M M	M M				P P	М		M M	M Y	Y Y
	0.027						М	М				Р			Р	Ý	Ϋ́
	0.033 0.039						M M	M M				P P			P P		
	0.047 0.068				\vdash		M M	M				Р			P P		
	0.082						M	M							Q		
	0.1 WVDC	25	50	100	200	500	25	50	100	200	500	50	100	200	Q 50	100	200
SIZ				LD10					LD12				LD13			LD14	
Letter	А	С		Е	G	J		K	М		N	Р		Х У	Z	_	
Max. Thickness	0.33 (0.013)	(0.02		0.71	0.90 (0.035)	0.9		1.02 (0.040)	1.27		.40 055)	1.52		.29 2.54			
												(0.060)		090) (0.100			

X8R - General Specifications





AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a "B" in the 12th position of the AVX Catalog Part Number. This fulfills AVX's commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special "B" termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination "B" products.

Not RoHS Compliant

PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

LD05	<u>5</u>	F	101	<u>J</u>	<u>A</u>	<u>B</u>	2	<u>A</u>
Size LD02 - 0402 LD03 - 0603 LD04 - 0504* LD05 - 0805 LD06 - 1206 LD10 - 1210 LD12 - 1812 LD13 - 1825 LD14 - 2225 LD20 - 2220	Voltage 6.3V = 6 10V = Z 16V = Y 25V = 3 35V = D 50V = 5 100V = 1 200V = 2 500V = 7	Dielectric X8R = F	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros	Capacitance Tolerance B = \pm .10 pF (<10pF) C = \pm .25 pF (<10pF) D = \pm .50 pF (<10pF) F = \pm 1% (\geq 10 pF) G = \pm 2% (\geq 10 pF) J = \pm 5% K = \pm 10% M = \pm 20%	Failure Rate A = Not Applicable	Terminations B = 5% min lead X = FLEXITERM® with 5% min lead** **X7R only	Packaging 2 = 7" Reel 4 = 13" Reel Contact Factory For Multiples*	Special Code A = Std. Product

LD04 has the same CV ranges as LD03.

See FLEXITERM® section for CV options

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.





Parame	ter/Test	X8R Specification Limits	Measuring	Conditions			
Operating Tem	perature Range	-55°C to +150°C	Temperature C	ycle Chamber			
Capac	itance	Within specified tolerance	 	·⊔¬ ± 10%			
Dissipati	on Factor	≤ 2.5% for ≥ 50V DC rating ≤ 3.5% for 25V DC and 16V DC rating	Voltage: 1.0				
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with 120 ± 5 secs @ roo				
Dielectric	: Strength	No breakdown or visual defects	Charge device with 250 1-5 seconds, w/charge limited to 50 Note: Charge device with for 500V	and discharge current mA (max) n 150% of rated voltage			
	Appearance	No defects	Deflectio	n: 2mm			
Resistance to	Capacitance Variation	≤ ±12%	Test Time: 3	80 seconds 7 1mm/sec			
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)					
	Insulation Resistance	≥ Initial Value x 0.3	90 r	mm			
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutection for 5.0 ± 0.	solder at 230 ± 5°C 5 seconds			
	Appearance	No defects, <25% leaching of either end terminal					
	Capacitance Variation	≤ ±7.5%					
Resistance to Solder Heat	Dissipation Factor	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room	temperature for 24 ± 2			
	Insulation Resistance	Meets Initial Values (As Above)	hours before measuring	g electrical properties.			
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes			
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes			
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes			
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes			
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ± 2 hours at ro	and measure after om temperature			
	Appearance	No visual defects					
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 r				
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	for 1000 hou				
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamb temperature for 24 ± 2 h				
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects					
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s 5% relative humidi				
Load Humidity	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated				
numicity	Insulation Resistance	≥ Initial Value x 0.3 (See Above)		d humidity for			
	Dielectric Strength	Meets Initial Values (As Above)	temperature and humidity for 24 ± 2 hours before measuring.				



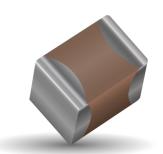


	SIZE WVDC				LDC)3			LD0	5		LD06			
		WVD	C	2	5V	50V		25V	Т	50)V	25V		50V	
271	Cap	270			G	G									
331	(pF)	330			G	G		J			J				
471		470			G	G		J		,	J				
681		680			G	G		J		,	J		Î		
102		1000			G	G		J		,	J	J		J	
152		1500			G	G		J		,	J	J		J	
182		1800			G	G		J		,	J	J		J	
222		2200			G	G		J		,	J	J		J	
272		2700			G	G		J		,	J	J		J	
332		3300			G	G		J		,	J	J		J	
392		3900			G	G		J		,	J	J		J	
472		4700			G	G		J		,	J	J		J	
562		5600			G	G		J		,	J	J		J	
682		6800			G	G		J		,	J	J		J	
822	Cap	8200			G	G		J		,	J	J		J	
103	(µF)	0.01			G	G		J		,	J	J		J	
123		0.012			G	G		J		,	J	J		J	
153		0.015		G		G		J			J	J		J	
183		0.018			G	G		J		,	J	J		J	
223		0.022			G	G		J		,	J	J		J	
273		0.027			G	G		J		,	J	J		J	
333		0.033			G	G		J			J	J		J	
393		0.039			G	G		J			J	J		J	
473		0.047			G	G		J			J	J		J	
563		0.056			G			N		N		М		М	
683		0.068			G			N			١	M		М	
823		0.082						N			١	M		М	
104		0.1						N			١	М		М	
124		0.12						N			1	М		М	
154		0.15						N		1	1	М		M	
184		0.18						N				М		М	
224		0.22						N				М		M	
274		0.27										М		М	
334		0.33		1								М		M	
394		0.39		1								М			
474		0.47										М			
684		0.68		1											
824		0.82													
105	1														
	WVDC			25V 50V				25V 50V)V	25V		50V		
	SIZE			LD03				LD05			LD06				
Letter	Α	ACE			J	K	MNN		P Q		0	l X	ΙΥ	Z	
LULLU	- / \			G		- 1		- ''		•	۷	^			

Letter	Α	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMB	OSSED			

X7R - General Specifications





AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a "B" in the 12th position of the AVX Catalog Part Number. This fulfills AVX's commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special "B" termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination "B" products.

Not RoHS Compliant

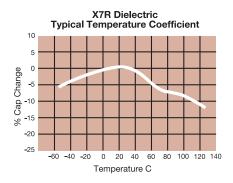
PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

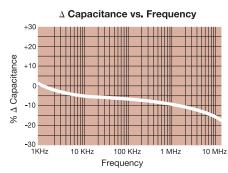
LD05	5	<u>c</u>	101	J	<u>A</u>	В	2	<u>A</u>
Size LD03 - 0603 LD04 - 0504* LD05 - 0805 LD06 - 1206 LD10 - 1210 LD12 - 1812 LD13 - 1825	Voltage 6.3V = 6 10V = Z 16V = Y 25V = 3 35V = D 50V = 5	Dielectric X7R = C	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros	Capacitance Tolerance B = ±.10 pF (<10pF) C = ±.25 pF (<10pF) D = ±.50 pF (<10pF) F = ±1% (≥ 10 pF) G = ±2% (≥ 10 pF) J = ±5%	Failure Rate A = Not Applicable	Terminations B = 5% min lead X = FLEXITERM® with 5% min lead** **X7R only	Packaging 2 = 7" Reel 4 = 13" Reel Contact Factory For Multiples*	Special Code A = Std. Product
LD12 - 1812	50V = 5			G = ±2% (≥ 10 pF)		**X7R only	Multiples*	

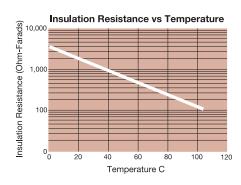
^{*}LD04 has the same CV ranges as LD03.

See FLEXITERM® section for CV options

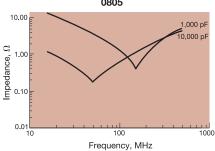
NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.



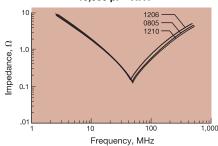




Variation of Impedance with Cap Value Impedance vs. Frequency 1,000 pF vs. 10,000 pF - X7R 0805







Impedance vs. Frequency
100,000 pF - X7R

10
1206
0805
1210
0.1

Frequency, MHz

Variation of Impedance with Chip Size





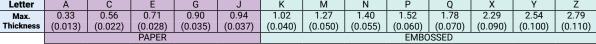
Parame	ter/Test	X7R Specification Limits	Measuring (Conditions
Operating Tem	perature Range	-55°C to +125°C	Temperature C	ycle Chamber
Capac	itance	Within specified tolerance		
Dissipati	on Factor	\leq 10% for \geq 50V DC rating \leq 12.5% for 25V DC rating \leq 12.5% for 25V and 16V DC rating \leq 12.5% for \leq 10V DC rating	Freq.: 1.0 k Voltage: 1.0'	
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with 120 ± 5 secs @ roo	
Dielectric	: Strength	No breakdown or visual defects	Charge device with 250 1-5 seconds, w/charge limited to 50 Note: Charge device with for 500V	and discharge current mA (max) n 150% of rated voltage
	Appearance	No defects	Deflectio	n: 2mm
Resistance to	Capacitance Variation	≤ ±12%	Test Time: 3	0 seconds 7 1mm/sec
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)		
	Insulation Resistance	≥ Initial Value x 0.3	90 n	
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic for 5.0 ± 0.5	
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±7.5%		
Resistance to Solder Heat	Capacitance Variation Dissipation Factor Insulation Resistance Dielectric Strength Appearance	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room	temperature for 24 ± 2
	Resistance	Meets Initial Values (As Above)	hours before measuring	g electrical properties.
		Meets Initial Values (As Above)		
		No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ± 2 hours at ro	
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 r	ated voltage (≤ 10V) in
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	test chamber set for 1000 hou	
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamb temperature for 24 ± 2 ho	
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s 5% relative humidi	
Load Humidity	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated	
numany	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature an	d humidity for
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours bef	ore measuring.

X7R - Capacitance Range



PREFERRED SIZES ARE SHADED

						LD03																				
SIZI	E		LD02	2				LD03	3						LD05	;						LD	06			
Solder	ing	Ref	flow/W	/ave			Ref	flow/W	/ave					Ref	flow/W	ave						Reflow	/Wave			
Packad	ina	Α	II Pap	er			A	II Par	er				F	Paper	/Emb	osse					Pai	per/Er	nbos	sed		
(L) Length	mm	1.0	00 ± 0	.10			1.0	60 ± 0	.15					2.	01 ± 0	.20						3.20 ±	0.20			
(2) 20.1gt.1	(in.)		40 ± 0					63 ± 0							79 ± 0 25 ± 0						(0.126 ±		8)		
W) Width	mm (in.)		50 ± 0 20 ± 0					81 ± 0 32 ± 0							25 ± 0 49 ± 0						(1.60 ± ± 0.063		R)		
(t) Terminal	mm	0.:	25 ± 0	.15			0.3	35 ± 0	.15					0.	50 ± 0	.25			0.50 ± 0.25							
` '	(in.)		10 ± 0		100	1.0		14 ± 0		400			1 40		20 ± 0		400	1000	6.0	1 40		0.020 ±				1.500
Cap	100	16	25	50	6.3	10	16	25	50	100	200	6.3	10	16	25	50	100	200	6.3	10	16	25	50	100	200	500
(pF)	150																									
(p.)	220			С																						
	330			С					G	G	G		J	J	J	J	J	J								K
	470			С					G	G	G		J	J	J	J	J	J								K
	680			С					G	G	G		J	J	J	J	J	J								K
	1000 1500			C					G G	G G	G		J	J	J	J	J	J						J		K M
	2200			C					G	G			J J	J	J	J	J	J		J	J	J	J	.1	J	M
	3300		С	C					G	G			J	J	J	J	J	J		J	J	J	J	J	J	M
	4700		С	С				İ	G	G		İ	J	J	J	J	J	J	ĺ	J	J	J	J	J	J	M
	6800	С	С						G	G			J	J	J	J	J	J		J	J	J	J	J	J	Р
Cap	0.010	С	С					0	G	G			J	J	J	J	J	J		J	J	J	J	J	J	Р
(µF)	0.015 0.022	C C						G	G				J	J	J	J	J	J		J	J	J	J	J	M	
	0.022	C						G	G				J	J	J	J	N	11		J	J	J	J	J	M	
	0.047			İ			G	G	G			İ	J	J	J	J	N		İ	J	J	J	J	J	М	l i
	0.068						G	G	G				J	J	J	J	N			J	J	J	J	J	Р	
	0.10		C*		•	G	G	G	G				J	J	J	J	N			J	J	J	J	Р	Р	
	0.15 0.22				G	G							J J	J	J N	N	N N			J	J	J	J	Q		
	0.22				G	G							N	N	N	N	N			J	J	M	P	Q		
	0.47							J*					N	N	N	N	N			M	М	M	P	Q		
	0.68												N	N	N					М	М	Q	Q	Q		
	1.0					J*	J*						N	N	N*					M	М	Q	Q	Q	ļ	
	1.5 2.2				J*										P*					P 0	Q	Q				
	3.3				J^										P"					Ų	Ų	Ų				\vdash
	4.7												P*	P*						Q*	Q*	Q*				
	10											P*	P							Q*	Q*	Q				
	22																		Q*							
	47																									
	100 WVDC	16	25	50	6.3	10	16	25	50	100	200	6.3	10	16	25	50	100	200	6.3	10	16	25	50	100	200	500
	SIZE		LD02		0.0		0	LD03		100		0.0			LD05		100		0.0			LD		100		, 550
Letter	Α		С		Е		G		J		K		M	N		Р		Q		Χ		Υ		Z		
Max.	0.33		0.56		0.71		0.90).94		.02		27	1.4		1.5		1.78		2.29		2.54		2.79		
Thickness	(0.013)) (0	0.022		0.028)		.035)	(0	.037)	(0.0	040)	(0.0	050)	(0.0	55)	(0.06		(0.07	0) ((0.090	0) (0.100)) (0).110 <u>)</u>		
				D	APFR											CI	MBO.S	CLI								





= Under Development



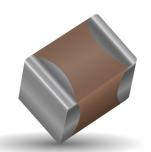


PREFERRED SIZES ARE SHADED

SIZE					LD10					LD	12		LD	13		LD	20		LD	14
Solder	ina			R	eflow Onl	v				Reflov	v Only		Reflox	w Only		Refloy	w Only		Reflov	v Only
Packac	-				er/Embos	<u> </u>				All Emi				bossed			bossed			oossed
	mm				3.20 + 0.20					4.50				± 0.30			± 0.50		5.72	
(L) Length	(in.)				126 ± 0.00					(0.177 :				± 0.012)			± 0.020)		(0.225 ±	
W) Width	mm				2.50 ± 0.20					3.20 :				± 0.40			± 0.40		6.35	
	(in.) mm				098 ± 0.00					0.126 :				± 0.016) ± 0.36		(0.197 :			(0.250 ± 0.64 ±	
(t) Terminal	(in.)			(0.0	020 ± 0.01	10)				(0.024 :	0.014)		(0.024 :	± 0.014)		(0.025 :	± 0.015)		(0.025 ±	± 0.015)
WVD		10	16	25	50	100	200	500	50	100	200	500	50	100	25	50	100	200	50	100
Cap (pF)	100 150																' >	·	W_	
(pi)	220															*	<u> </u>			
	330															┌ `			1).	ŢT 1
	470																_	1		_
	680															┡		الحرا		
	1000 1500	J	J	J	J	J	J	М										Tt		
	2200	J	J	Ĵ	Ĵ	J	J	M									1	I		
	3300	J	J	J	J	J	J	М												
	4700	J	J	J	J	J	J	M												
0	6800 0.010	J J	J	J	J	J	J	M	K	K	K	K	М	М		Х	X	Х	М	Р
Cap (µF)	0.010	J	J	J	J	J	J	P	K	K	K	P	M	M		X	X	×	M	P
(μ.)	0.022	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Ĵ	Q	ĸ	K	K	P	M	M		X	x	x	M	P
	0.033	J	J	J	J	J	J	Q	K	K	K	Х	М	М		Х	Х	Х	М	Р
	0.047	J	J	J	J	J	J		K	K	K	Z	M	М		X	X	X	М	Р
	0.068	J J	J	J	J	J	M M		K	K K	K	Z	M	M M		X	X	X	M M	P P
	0.10	J	J	J	J	M	Z		K	K	P		M	M		x	l x	x	M	P
	0.22	J	J	J	J	Р	Z		K	K	Р		М	М		X	X	Х	М	Р
	0.33	J	J	J	٦	Q			K	М	X		М	М		X	X	Х	М	Р
	0.47	M	M	M P	M	Q			K	P			M	M P		X	X	Х	M	P P
	0.68	M N	M N	P	X	Z		+	M	Q X			M M	P		X	X		M M	P
	1.5	N	N	Z	Z	Z			Z	Ž			M			X	x		M	X
	2.2	Х	X	Z	Z	Z			Z	Z						X	X		М	
	3.3	X	X	Z	Z				Z Z							X	Z			
	4.7 10								Z							X Z	Z Z			
	22	2 Z Z						+							Z					\vdash
	47																			
	100	10	10	0.5	50	100	000	500	- 50	100	000	500	- 50	100	0.5	- 50	100	000	50	100
SIZE	WVDC	10	16	25	50 LD10	100	200	500	50	100 LD	200	500	50 LD	100 13	25	50 LD	100	200	50 LD	100 1.4
SIZE		LDIO								LD	14		LD	13		LD	20		LD	1-7
Letter	A C E G J							K	М		1	Р	Q		X	Υ	Z			
Max.	0.33	0.5		0.71	0.90	0.9		1.02	1.27		40	1.52	1.78		29	2.54	2.79			
Thickness	(0.013)	(0.0		0.028)	(0.035) (0.0	37)	(0.040)	(0.050	0.0))55) (0.060)	(0.070	0.0)	090) ((0.100)	(0.110	0)		
		(0.022) (0.028) (0.035) (0.037) (0.040) (0.050) (0.055) (0.060) (0.070) (0.090) (0.100) (0.110)																		

X5R - General Specifications





AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a "B" in the 12th position of the AVX Catalog Part Number. This fulfills AVX's commitment to providing a full range of products to our customers. AVX has provided in the following pages a full range of values that we are currently offering in this special "B" termination. Please contact the factory if you require additional information on our MLCC Tin/Lead Termination "B" products.

Not RoHS Compliant

PART NUMBER (SEE PAGE 4 FOR COMPLETE PART NUMBER EXPLANATION)

LD05	<u>5</u>	D	101	<u>1</u>	<u>A</u>	<u>B</u>	2	<u>A</u>
Size LD02 - 0402 LD03 - 0603 LD04 - 0504* LD05 - 0805 LD06 - 1206 LD10 - 1210 LD12 - 1812 LD13 - 1825 LD14 - 2225 LD20 - 2220	Voltage 6.3V = 6 10V = Z 16V = Y 25V = 3 35V = D 50V = 5 100V = 1 200V = 2 500V = 7	Dielectric X5R = D	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros	Capacitance Tolerance $B = \pm .10 \text{ pF} (<10 \text{pF})$ $C = \pm .25 \text{ pF} (<10 \text{pF})$ $D = \pm .50 \text{ pF} (<10 \text{pF})$ $F = \pm 1\% (\ge 10 \text{ pF})$ $G = \pm 2\% (\ge 10 \text{ pF})$ $J = \pm 5\%$ $K = \pm 10\%$ $M = \pm 20\%$	Failure Rate A = Not Applicable	Terminations B = 5% min lead X = FLEXITERM® with 5% min lead** **X7R only	Packaging 2 = 7" Reel 4 = 13" Reel Contact Factory For Multiples*	Special Code A = Std. Product

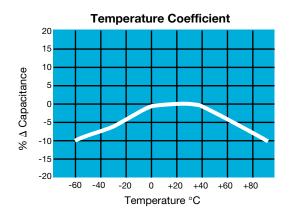
^{*}LD04 has the same CV ranges as LD03.

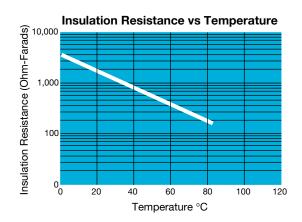
See FLEXITERM® section for CV options

NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.

Contact factory for non-specified capacitance values.

TYPICAL ELECTRICAL CHARACTERISTICS









Parame	ter/Test	X5R Specification Limits	Measuring (Conditions
	perature Range	-55°C to +85°C	Temperature C	ycle Chamber
Capac	itance on Factor	Within specified tolerance ≤ 2.5% for ≥ 50V DC rating ≤ 3.0% for 25V, 35V DC rating ≤ 12.5% Max. for 16V DC rating and lower Contact Factory for DF by PN	Freq.: 1.0 k Voltage: 1.0 For Cap > 10 µF, 0	Vrms ± .2V
Insulation	Resistance	10,000MΩ or 500MΩ - μF, whichever is less	Charge device with 120 ± 5 secs @ roo	
Dielectric	Strength	No breakdown or visual defects	Charge device with 250 1-5 seconds, w/charge limited to 50	and discharge current
	Appearance	No defects	Deflectio	
Resistance to Flexure	Capacitance Variation	≤ ±12%	Test Time: 3	0 seconds 1 mm/sec
Stresses	Dissipation Factor	Meets Initial Values (As Above)		
	Insulation Resistance	≥ Initial Value x 0.3	90 r	
Solder	ability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutection for 5.0 ± 0.5	
	Appearance	No defects, <25% leaching of either end terminal		
	Capacitance Variation	≤ ±7.5%		
Resistance to Solder Heat	Dissipation Factor Insulation Resistance	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room	temperature for 24 ± 2
00.00.		Meets Initial Values (As Above)	hours before measuring	g electrical properties.
	Dielectric Strength	Meets Initial Values (As Above)		
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +85°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ± 2 hours at ro	
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 chamber set at 85°C: (+48, -0). Note: Contac	± 2°C for 1000 hours
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	specification part numl	pers that are tested at
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamb	· ·
	Dielectric Strength	Meets Initial Values (As Above)	temperature for 24 ± 2 h	
	Appearance	No visual defects		
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s 5% relative humidi	
Load	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated	yoltage applied.
Humidity	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature an	d humidity for
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours bef	ore measuring.

X5R - Capacitance Range



PREFERRED SIZES ARE SHADED

											=							1					[
SIZ	Έ			LI	D02	:				L	.D0	3					LD	05					LD	06					ı	LD10)				LD	12	
Solde	ring		R	eflo	w/W	ave				Reflo	w/V	Vave	•			Re	flow	/Wa	/e			Re	eflow	/Wa	ve			_	Refl	ow/V	Vave	:					
Packa	ging			All I	Pap	er				All	Pa	oer			P	ape	r/Er	nbo	sse	d	Р	ape	r/Er	nbo	sse	d		Pa	per/	/Emb	oss	ed					
(L) Length	mm			1.00					"	1.60			٠,					0.2					1.20					,		0 ± 0							
\A(\) \A(\) deb	(in.) mm			0.50		004) 10			((0.8			0)					0.0					126 ± .60					(6 ± 0 0 ± 0		5)		+			
W) Width	(in.)					004)			((0.032			6)					0.0					063 ±					(8 ± 0		3)		1			
(t) Terminal	mm (in.)			0.25 .010		.15 .006)			((0.3! 0.014			5)					0.2					0.50 ± 020 ±					(0 ± 0 0 ± 0))					
WVE	oc (25	50	4						50	6.3					50	6.3					50	4						50	6.3	10	25	50
Cap	100		ļ															ļ	ļ																		
(pF)	150 220						С																														
	330						С																			Н	'	,			>		_	↓	Ι Ι N		1
	470						C																					~	~			_	_	₹;	`,≥	_	
	680	L	L	L	L		С		L	L	L	L	L	L	L			L	L	L				L	L				(_	$\overline{}$	7			ノ、	ŢT	
	1000						С																						•	_		4	_			_	1
	1500						С																									1					
	2200 3300		┢		-		C					_							┢							Н	_				1						- {
	4700					С	C							G																							
	6800					C								G																							
Сар	0.010					С								G																		T	T		П		
(μF)	μF) 0.015 C									G	G	G																									
	0.022 C C					_				G	G	G						N						Ш						\bot	_		Ш		Ш		
	0.033 C										G G	G	G						N												ļ						
	0.033 0.047 0.068								G	G	G						N																				
	0.10			С	С	С						G		G				N		N												+	t		Н		
	0.15		İ									G						N	N													İ					
	0.22		C*								G	G						N	N							Q						\perp	┖		Ш		Ш
	0.33										G	G						N						_	_												
	0.47 0.68	C*	C*								G							N N						Q	Q								X				
	1.0	C*	C*	C*				\vdash	G	G	G	J*					N	N		P*				Q	Q	Н		_			Х	X	X		Н	М	Н
	1.5																			•				~	~							,	,				
	2.2	C*						G*	G*	J*	J*					Ν	N	N					Q	Q							Z	X			Ш		Ш
	3.3					J*	J*	J*	J*				N	N					Х	Х																	
	4.7					J*	J*	J*					N P	N	N*	N*			X	X	X	X					V	Q	Z					7			
	22				\vdash	K*		\vdash					P*	Р	Р		\vdash	\vdash	X	X	X	X		Н	\vdash	Z	X Z	Z	Z		+	+	\vdash	Z	Н		
	47																				X				1			Z*	_	_	_						
	100					L	L	L		L						L									L		Z*	Z								L	
	WVDC	4	6.3	_	_	25	50	4	6.3		_	_	35	50	6.3	10	16	25	35	50	6.3	10			35	50	4	6.3	10	16	25	35	50	6.3			50
	SIZE			LI	D02					L	.D0	3					LD	05					LD	06					L	LD10)				LD	12	
	- H																																				
Letter								G			J			K		N			N			Р			Q			Χ		'			Z				
Max. Thickness							0.90			0.94			.02		1.1			1.4		4	1.5			1.78	- 1		.29	,	2.			2.7					
THICKNESS	(0.013)		(U.C	122)		(0.0)		1 ((0.03	ɔ)	(0	.03	/)	(U	.040	<u> </u>	(0.0	100)	(0.0	55)	1 (0.06	о <u>о)</u> ИВС		.070	U)	(U.	090	<u>') </u>	(0.1	UU)		(0.1	10)	1	
	PAPE																						LI	VIDC	133	LU										J	

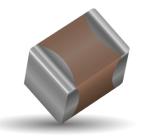
*Optional Specifications - Contact factory

NOTE: Contact factory for non-specified capacitance values

Automotive MLCC

General Specifications



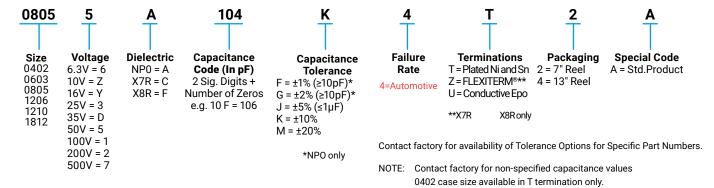


GENERAL DESCRIPTION

AVX Corporation has supported the Automotive Industry requirements for Multilayer Ceramic Capacitors consistently for more than 25 years. Products have been developed and tested specifically for automotive applications and all manufacturing facilities are QS9000 and VDA 6.4 approved.

AVX is using AECQ200 as the qualification vehicle for this transition. A detailed qualification package is available on request and contains results on a range of part numbers.

HOW TO ORDER



COMMERCIAL VS AUTOMOTIVE MLCC PROCESS COMPARISON

	Commercial	Automotive
Administrative	Standard Part Numbers. No restriction on who purchases these parts.	Specific Automotive Part Number. sed to control supply of product to Automotive customers.
Design	Minimum ceramic thickness of 0.020"	Minimum Ceramic thickness of 0.029" (0.74mm) on all X7R product.
Dicing	Side & End Margins = 0.003" min	Side & End Margins = 0.004" min Cover Layers = 0.003" min
Lot Qualification (Destructive Physical Analysis - DPA)	As per EIA RS469	Increased sample plan stricter criteria.
Visual/Cosmetic Quality	Standard process and inspection	100% inspection
Application Robustness	Standard sampling for accelerated wave solder on X7R dielectrics	Increased sampling for accelerated wave solder on X7R and NP0 followed by lot by lot reliability testing.

All Tests have Accept/Reject Criteria 0/1

Automotive MLCC

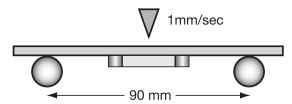
NP0/X7R Dielectric



FLEXITERM FEATURES

a) Bend Test

The capacitor is soldered to the PC Board as shown:



Typical bend test results are shown below:

Style	Conventional	Soft Term
0603	>2mm	>5
0805	>2mm	>5
1206	>2mm	>5

a) Temperature Cycle testing FLEXITERM® has the ability to withstand at least 1000 cycles between -55°C and +125°C

Automotive MLCC-NP0





_	ZE	04	02			03				0805						206		
Sold		Reflow	//Wave		Reflow	//Wave			Re	flow/Wa	ive				Reflov	v/Wave		
WV	DC	25V	50V	25V	50V	100V	200V	25V	50V	100V	200V	250V	25V	50V	100V	200V	250V	500V
0R5	0.5	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
1R0	1.0	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
1R2	1.2	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
1R5	1.5	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
1R8	1.8	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
2R2	2.2	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
2R7	2.7	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
3R3	3.3	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
3R9	3.9	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
4R7	4.7	C	C C	G G	G G	G G	G G	J	J	J	N N	N N	J	J	J	J	J	J
5R6 6R8	5.6 6.8	С	C	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
8R2	8.2	С	C	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
100	10.0	C	C	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
120	12	С	C	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
150	15	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
180	18	C	C	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
220	22	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
270	27	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
330	33	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
390	39	С	С	G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
470	47			G	G	G	G	J	J	J	N	N	J	J	J	J	J	J
510	51			G	G	G	G	J	J	J	N	N	J	J	J	J		
560	56			G	G	G	G	J	J	J	N	N	J	J	J	J		
680	68			G	G	G	G	J	J	J	N	N	J	J	J	J		
820	82			G	G	G	G	J	J	J	N	N	J	J	J	J		
101	100			G	G	G	G	J	J	J	N	N	J	J	J	J		
121	120			G G	G G	G G		J J	J	J	N N	N N	J	J	J	J		
151 181	150 180			G	G	G		J	J	J	N	N	J	J	J	J		
221	220			G	G	G		J	J	J	N	N	J	J	J	J		
271	270			G	G	G		J	J	J	N	N	J	J	J	J		
331	330			G	G	G		J	J	J	N	N	J	J	J	J		
391	390			G	G			J	J	J			J	J	J	J		
471	470			G	G			J	J	J			J	J	J	J		
561	560			G	G			J	J	J			J	J	J	J		
681	680			G	G			J	J	J			J	J	J	J		
821	820							J	J	J			J	J	J	J		
102	1000							J	J	J			J	J	J	J		
122	1200																	oxdot
152	1500																	
182	1800																	
222	2200																	\sqcup
272	2700													-				
332	3300													-				\vdash
392 472	3900 4700													-				\vdash
103	4/00 10nF													-				
WV		25V	50V	25V	50V	100V	200V	25V	50V	100V	200V	250V	25V	50V	100V	200V	250V	500V
				234			2007	23 V	307		2007	230 V						
Si	ze	04	02		06	03				0805					12	200		

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBC	SSED			

Automotive MLCC - X7R





Selfow/Name		SIZE		0402					060	3					0	805						120	6				12	210		1	812		2220	
1221 Cap 220 C C C C C C C C C	So	Idering	Ref	low/W	ave			Re	flow/\	Nave					Reflo	w/Wa	ve .				Re	eflow/\	Wave				Reflo	w Onl	у	Reflo	ow Only	Ref	low 0	nly
271 672 70	١	NVDC	16V	25V	50V	10V	16V	25V	50V	100V	200V	250V	16V	25V	50V	100V	200V	250V	16V	25V	50V	100V	200V	250V	500V	16V	25V	50V	100V	50V	100V	25V	50V	100V
331 330 C C C C	221	Cap 220	С	С	С											С																	П	
391 390 C C C C C C C C C	271	(pF) 270	С	С	С																												П	
A77	331	330	С	С	С																													
561 560 C C C C C C C C C	391	390	С	С	С																													
B81	471	470	С	С	С																												П	
B21 B20 C C C C G G G G G G	561	560	С	С	С																												П	
102 1000 C C C C G G G G G G G G G G J J J J J J	681	680	С	С	С																												\Box	
182 1800	821	820	С	С	С																													
222 2200			_		_	_	G	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	J	J			-			-		Ш	
332 3300 C C C C G G G G G G G G G G G J J J J J	_				_	_			-	-	-		J	J	J	J	J	J	J	J	J	J	J	J	J			+		K	+		Ш	
472 4700 C C C C G G G G G G			_		_	_	_	_		-	-	_	_	-	-	_		_	-		_	_	_	_	_			+	-		-		${\color{orange} ightarrow}$	<u> </u>
T03	_		_		_	_	_			-	-	_	-		-		_	_			_		-	_	_	_			-		-		\sqcup	
123 (F) 0.012 C G G G G G G G J J J J N N N N J J J J J			_	С	С	_	_			_	-	_	-	-	-	_		_	_	-	_	_	_	_	_	_					-		$\vdash \vdash$	<u> </u>
153			_			_	_		-		G	G	_		-	_		_		-	-		-		J			-					$\vdash \vdash$	<u> </u>
183		` '		_					-		_			_	-					_			-	_				-	_		-		\longmapsto	<u> </u>
223 0.022 C			_			_	_		_	-	_			-	_		_			-	_	_	_	_		_		-	-		-		\vdash	
273 0.027 C			_		_	_	_		_	-	-		_	_	-		_			_	_		-	_		_		-	-		-		$\vdash \vdash$	
333 0.033 C			_	_		_	_		_	G				_	-					-	_	-	_	-		_		-	-	_			\vdash	
473 0.047			_			_	_		_				_	-	-		_			-	-	_	_	_				-	_				\vdash	
Section Sect			U			_		-	-					_	-					_	_		_	_						-	-		\vdash	$\overline{}$
683								-	-						-		IN	IN		_	_			_					_		-		\vdash	
823						_	_	_	_					-	-	_			_		-		_	_			_	-	_	_			\vdash	
104 0.1						_	_	_	_					-	-	_			_	_	-		_	_					-	_			\Box	
154 0.15						G	G	G	G					_	_	N			J	J	J	М	Р	Р		К		-	М		К		П	
224 0.22	124	0.12				G							J	J	N	N			J	J	М	М	Q	Q		К	К	К	Р	К	К		П	
334 0.33	154	0.15				G							М	N	N	N			J	J	М	М	Q	Q		К	К	K	Р	K	К		П	
474 0.47	224	0.22				G							М	N	N	N			J	М	М	Q	Q	Q		М	М	М	Р	М	М		П	
684 0.68	334	0.33											N	N	N	N			J	М	Р	Q				Р	Р	Р	Q	Х	Х			
105 1	474	0.47											N	N	N	N			М	М	Р	Q				Р	Р	Р	Q	Х	X			
155 1.5	684	0.68											N	N	N				М	Q	Q	Q				Р	Р	Q	_				\Box	
225 2.2													N		N				М	Q	Q					-	_						\longrightarrow	Z
335 3.3													_						_ `	_ `	_					-	_	-	_	_			-	Z
4.75 4.7													N	N						-	_	Q							_		Z		\longrightarrow	Z
106 10									<u> </u>											-	_								_				$\overline{}$	Z
226 22													<u> </u>						Q	Q	Q								Z				$\overline{}$	Z
WVDC 16V 25V 50V 10V 16V 25V 50V 100V 25V 50V 100V 200V 25V 50V 100V 200V 25V 50V 100V 200V 25V 50V 100V 200V 25V 50V 100V 200V 25V 50V 100V 25V 50V 100V 25V 50V													<u> </u>													Z	Z	Z		Z			Z	Z
	-		16\/	OEV.	EOV /	10\/	16\/	OE) /	EOV.	1001	20017	2501/	16\/	OEV.	EOV.	1001	2001	2501	16\/	OEV.	EOV.	1001	2005	2501/	E00) /	16\/	OEV.	EOV.	1001/	EOV.	100)/		EOV.	1001
Size 0402 0603 0805 1206 1210 1812 222			167		507	107	167	25V			200V	250V	167	25V			200V	25UV	167	25V	15UV			250V	5007	167			100V			25V	2220	

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			DADER						FMR	199FD			

Automotive MLCC - X8R

Capacitance Range



S	SIZE	06	03	0	805	12	06
Sol	dering	Reflow	/Wave	Reflo	w/Wave	Reflow	/Wave
WVDC	WVDC	25V	50V	25V	50V	25V	50V
271	Cap 270	G	G				
331	(pF) 330	G	G	J	J		
471	470	G	G	J	J		
681	680	G	G	J	J		
102	1000	G	G	J	J	J	J
152	1500	G	G	J	J	J	J
182	1800	G	G	J	J	J	J
222	2200	G	G	J	J	J	J
272	2700	G	G	J	J	J	J
332	3300	G	G	J	J	J	J
392	3900	G	G	J	J	J	J
472	4700	G	G	J	J	J	J
562	5600	G	G	J	J	J	J
682	6800	G	G	J	J	J	J
822	8200	G	G	J	J	J	J
103	Cap 0.01	G	G	J	J	J	J
123	(F) 0.012	G	G	J	J	J	J
153	0.015	G	G	J	J	J	J
183	0.018	G	G	J	J	J	J
223	0.022	G	G	J	J	J	J
273	0.027	G	G	J	J	J	J
333	0.033	G	G	J	J	J	J
393	0.039	G	G	J	J	J	J
473	0.047	G	G	J	J	J	J
563	0.056	G		N	N	М	М
683	0.068	G		N	N	M	M
823	0.082			N	N	М	M
104	0.1			N	N	М	M
124	0.12			N	N	М	М
154	0.15			N	N	M	M
184	0.18			N		М	М
224	0.22			N		М	М
274	0.27					М	М
334	0.33					М	М
394	0.39					М	
474	0.47					М	
684	0.68						
824	0.82						
105	1						
WVDC	WVDC	25V	50V	25V	50V	25V	50V
S	SIZE	06	03	0	805	12	06

	Letter	Α	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z
	Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
İ	Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
-				PAPER						EMBC	SSED			

APS for COTS+ High Reliability Applications



General Specifications Surface Mount NP0, X7R and X8R/L MLCCs



AVX's APS COTS+ series of multilayer ceramic capacitors offers the customer a high reliability solution with an ultralow failure rate, <1ppb, in a variety of case sizes and voltages. The APS range encompasses a wide range of dielectric types to meet the customer's requirements from low temperature/voltage capacitance change dielectric, NPO, to high preforming capacitance voltage X7R to high temperature reliability dielectrics, X8R/L.

APS capacitors have a wider capacitance range than MIL spec parts that satisfies the need for higher CV demands and board space saving requirements. Each production lot is extensively tested and removes the requirement for customer specific drawings. The testing regime uses many of the MIL-STD test methods as per MIL-PRF-55681 and has a field failure rate of less than 1 ppb. The APS testing series uses AVX's unique in-house maverick testing detection system that eliminates infant mortality failures.

Applications suitable for APS include Industrial, Telecommunications, Aviation, and Military. The APS is available with a range of different termination finishes, Flexiterm®, Nickel / Tin and Tin with Pb1. Flexiterm® technology delivers improved thermo-mechanical stress resistance.

AVX'S APS RELIABILITY TEST SUMMARY

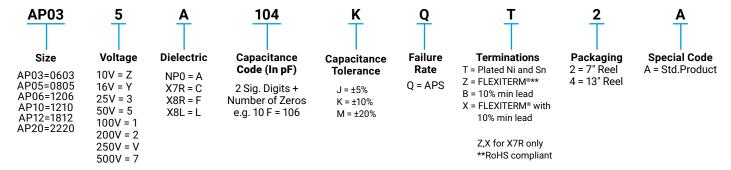
- · 100% Visual Inspection
- DPA
- · IR, DF, Cap, DWV
- · Maverick Lot Review
- Thermal Shock
- 85/85 Testing
- · Additional Life Testing
- · C of C with every Order
- · Quarterly Data Package

FEATURES

- The APS range has been extensively reliability tested as standard resulting in an ultralow failure rate, ≤1ppb
- The APS range is available with Flexiterm® that deliver's high thermo-mechanical stress resistance.
- High CV range enabling board space saving requirements.

Dielectric	Temperature/Percentage Cap Change
NP0	-30ppm +30ppm from -55°C + 125°C
X7R	-15% +15% from -55°C to + 125°C
X8R	-15% +15% from -55°C to + 150°C
X8L	-15% +40% from -55°C to + 150°C

HOW TO ORDER



 ${\tt NOTE: Contact \ factory \ for \ availability \ of \ Termination \ and \ Tolerance \ Options \ for \ Specific \ Part \ Number.}$

APS COTS+ NP0 Series





Size	AP	03 = 060	03	AP	05 = 08	05		AF	P06 = 12	06			AP10	= 1210	
WVDC	25V	50V	100V	25V	50V	100V	25V	50V	100V	200V	500V	25V	50V	100V	200V
100 10pF	G	G	G	J	J	J	J	J	J	J	J				
120 12	G	G	G	J	J	J	J	J	J	J	J				
150 15	G	G	G	J	J	J	J	J	J	J	J				
180 18	G	G	G	J	J	J	J	J	J	J					
220 22	G	G	Ð	J	J	J	J	J	J	J					
270 27	G	G	G	J	J	J	J	J	J	J					
330 33	G	G	G	J	J	J	J	J	J	J					
390 39	G	G	G	J	J	J	J	J	J	J					
470 47	G	G	G	J	J	J	J	J	J	J					
510 51	G	G	G	J	J	J	J	J	J	J					
560 56	G	G	G	J	J	J	J	J	J	J					
680 68	G	G	G	J	J	J	J	J	J	J					
820 82	G	G	G	J	J	J	J	J	J	J					
101 100	G	G	G	J J J			J	J	J	J					
121 120	G	G	G	J J J		J	J	J	J						
151 150	G	G	G				J	J	J	J					
181 180	G	G	G	J J J		J	J	J	J						
221 220	G	G	G	J	J	J	J	J	J	J					
271 270	G	G	G	J	J	J	J	J	J	J					
331 330	G	G	G				J	J	J	J					
391 390	G	G		J J J			J	J	J	J					
471 470	G	G		J	J	J	J	J	J	J					
561 560				J	J	J	J	J	J	J					
681 680				J	J	J	J	J	J	J					
821 820				J	J	J	J	J	J	J					
102 1000				J	J	J	J	J	J	J		J	J	J	J
122 1200												J	J	M	М
152 1500												J	J	M	M M
182 1800												J	J	M	M
222 2200 272 2700												J	J	IVI	IVI
332 3300															
392 3900															
472 4700															
103 10nF															
WVDC	25V	50V	100V	25V	50V	100V	25V	50V	100V	200V	500V	25V	50V	100V	200V
Size		03 = 060		_	05 = 08		Z3V		206 = 12		J 300V	Z3V		= 1210	_ Z00V
3126	AF	05 - 000	,,	AF	00 - 00	00		Ar	00 - 12	00			AP 10	- 1210	



Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBO	SSED			

APS COTS+ X7R Series





	Size		AP	03 = 06	503			AP	05 = 0	805			-	AP06 =	= 1206				AP10 =	= 1210)	AP12=	= 1812	AP	20 = 22	220
١	WVDC	16V	25V	50V	100V	200V	16V	25V	50V	100V	200V	16V	25V	50V	100V	200V	500V	16V	25V	50V	100V	50V	100V	25V	50V	100V
102	Cap 1000	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	К			
182	(pF) 1800	G	G	G	G		J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	К			
222	2200	G	G	G	G		J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K			
332		G	G	G	G		J	J	J	J	J	J	J	J	J	J	٦	K	K	K	K	K	K			
472		G	G	G	G		J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K			
103		G	G	G	G		J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K			
123		G	G	G			J	J	J	М		J	J	J	J	J		K	K	K	K	K	K			
153		G	G	G			J	J	J	М		J	J	J	J	J		K	K	K	K	K	K			
183		G	G	G			J	J	J	М		J	J	J	J	J		K	K	K	K	K	K			
223	0.022	G	G	G			J	J	J	М		J	J	J	J	J		K	K	K	K	K	K			
273		G	G	G			J	J	J	М		J	J	J	J	J		K	K	K	K	K	K			
333		G	G	G			J	J	J	М		J	J	J	J	J		K	K	K	K	K	K			
473		G	G	G			J	J	J	М		J	J	J	М	J		K	K	K	K	K	K			
563		G	G	G			J	J J J M J		J	J	J	М	J		K	K	K	М	K	K					
683		G	G	G			J	I J J M			J	J	J	М	J		K	K	K	М	K	K				
823		G	G	G			J	J	J	М		J	J	J	М	J		K	K	K	М	K	K			
104		G	G	G			J	J	М	М		J	J	J	М	J		K	K	K	М	K	K			
124	0.12						J	J	М	N		J	J	М	М			K	K	K	P	K	K			
154							М	N	М	N		J	J	М	М			K	K	K	P	K	K			
224							М	N	М	N		J	М	М	Q			М	М	М	Р	М	М			
334							N	N	М	N		J	М	Р	Q			P	Р	Р	Q	Х	Х			
474							N	N	М	N		М	М	Р	Q			Р	Р	Р	Q	Х	Х			
684							N	N	N			М	Q	Q	Q			Р	Р	Q	X	Х	Х			
105							N	N	N*			М	Q	Q	Q*			Р	Q	Q	Z*	Х	Х			
155												Q	Q	Q				Р	Q	Z	Z	Х	Х			
225												Q	Q	Q				X	Z	Z	Z*	Z	Z			
335												Q						Х	Z	Z	Z	Z				
475												Q						Х	Z	Z		Z*				
106																	Z	Z*						Z	Z*	
226								25V 50V 100V 200V 16																Z		
	NVDC	16V				200V	16V							50V	100V	200V	500V	16V	25V	50V	100V		100V	25V		100V
	Size		AP	03 = 06	503			AP05 = 0805					/	AP06 =	1206				AP10 =	= 1210)	AP12=	=1812	AP2	20 = 22	220

^{*}Not currently available with lead plating finish, contact plant for further information.

Letter	Α	С	E	G	J	K	М	N	Р	Q	X	Y	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBO	SSED			

APS COTS+ X8R/L Series





X8R

	0175	4000	0600	4005	0005	4000	1006
	SIZE	AP03 =	: 0603	AP05	= 0805	AP06 =	1206
1	WVDC	25V	50V	25V	50V	25V	50V
331	Cap 330	G	G	_	J		
471	(pF) 470	G	G	_	J		
681	680	G	G	J	J		
102	1000	G	G	J	J	J	J
152	1500	G	G	J	J	J	J
222	2200	G	G	J	J	J	J
332	3300	G	G	J	J	J	J
472	4700	G	G	J	J	J	J
682	6800	G	G	J	J	J	J
103	Cap 0.01	G	G	J	J	J	J
153	(µF) 0.015	G	G	J	J	J	J
223	0.022	G	G	J	J	J	J
333	0.033	G	G	J	J	J	J
473	0.047	G	G	7	J	J	J
683	0.068	G		N	N	M	М
104	0.1			N	N	M	М
154	0.15			N	N	M	М
224	0.22			N		M	М
334	0.33					M	М
474	0.47					M	
684	0.68						
105	1						
	WVDC	25V	50V	25V	50V	25V	50V
	SIZE	060	03	08	05	120	6

X8L

	SIZE	ı	AP03 = 0603	3		AP05 = 080	5		AP06	= 1206	
	WVDC	25V	50V	100V	25V	50V	100V	16V	25V	50V	100V
331	Cap 330		G	G		J	J				
471	(pF) 470		G	G		J	J				
681	680		G	G		J	J				
102	1000		G	G		J	J				
152	1500		G	G		J	J			J	J
222	2200		G	G		J	J			J	J
332	3300		G	G		J	J			J	J
472	4700		G	G		J	J			J	J
682	6800		G	G		J	J			J	J
103	Cap 0.01		G	G		J	J			J	J
153	(μF) 0.015	G	G		J	J	J			J	J
223	0.022	G	G		J	J	J			J	J
333	0.033	G	G		J	J	N			J	J
473	0.047	G	G		J	J	N			J	J
683	0.068	G	G		J	J				J	J
104	0.1	G	G		J	J				J	M
154	0.15				J	N		J	J	J	Q
224	0.22				N	N		J	J	J	Q
334	0.33				N			J	М	Р	Q
474	0.47				N			М	М	Р	
684	0.68							М			
105	1							М			
	WVDC	25V	50V	100V	25V	50V	100V	16V	25V	50V	100V
	SIZE		0603					12	06		



Letter	Α	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBO	SSED			

MLCC with FLEXITERM®

General Specifications



GENERAL DESCRIPTION

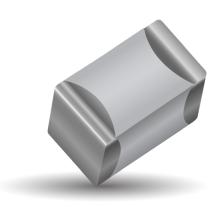
With increased requirements from the automotive industry for additional component robustness, AVX recognized the need to produce a MLCC with enhanced mechanical strength. It was noted that many components may be subject to severe flexing and vibration when used in various under the hood automotive and other harsh environment applications.

To satisfy the requirement for enhanced mechanical strength, AVX had to find a way of ensuring electrical integrity is maintained whilst external forces are being applied to the component. It was found that the structure of the termination needed to be flexible and after much research and development, AVX launched FLEXITERM®. FLEXITERM® is designed to enhance the mechanical flexure and temperature cycling performance of a standard ceramic capacitor with an X7R dielectric. The industry standard for flexure is 2mm minimum. Using FLEXITERM®, AVX provides up to 5mm of flexure without internal cracks. Beyond 5mm, the capacitor will generally fail "open".

As well as for automotive applications FLEXITERM® will provide Design Engineers with a satisfactory solution when designing PCB's which may be subject to high levels of board flexure.

PRODUCT ADVANTAGES

- High mechanical performance able to withstand, 5mm bend test guaranteed.
- Increased temperature cycling performance, 3000 cycles and beyond.
- · Flexible termination system.
- · Reduction in circuit board flex failures.
- · Base metal electrode system.
- · Automotive or commercial grade products available.



APPLICATIONS

High Flexure Stress Circuit Boards

 e.g. Depanelization: Components near edges of board.

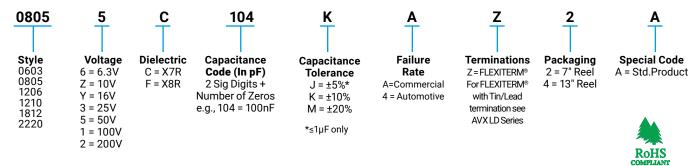
Variable Temperature Applications

- Soft termination offers improved reliability performance in applications where there is temperature variation.
- e.g. All kind of engine sensors: Direct connection to battery rail.

Automotive Applications

- · Improved reliability.
- Excellent mechanical performance and thermo mechanical performance.

HOW TO ORDER



NOTE: Contact factory for availability of Tolerance Options for Specific Part Numbers.

MLCC with FLEXITERM®

Specifications and Test Methods

PERFORMANCE TESTING

AEC-Q200 Qualification:

Created by the Automotive Electronics Council

Specification defining stress test qualification for passive components

Testing:

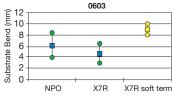
Key tests used to compare soft termination to AEC-Q200 qualification:

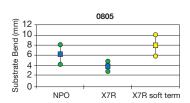
- Bend Test
- Temperature Cycle Test

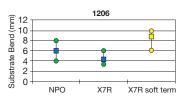


BOARD BEND TEST RESULTS

AEC-0200 Vrs AVX FLEXITERM® Bend Test







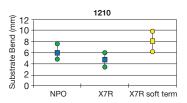


TABLE SUMMARY

Typical bend test results are shown below:

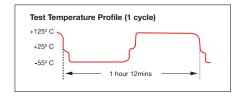
Style	Conventional Termination	FLEXITERM®
0603	>2mm	>5mm
0805	>2mm	>5mm
1206	>2mm	>5mm

TEMPERATURE CYCLE TEST PROCEDURE

Test Procedure as per AEC-Q200:

The test is conducted to determine the resistance of the component when it is exposed to extremes of alternating high and low temperatures.

- Sample lot size quantity 77 pieces
- TC chamber cycle from -55°C to +125°C for 1000 cycles
- Interim electrical measurements at 250, 500, 1000 cycles
- Measure parameter capacitance dissipation factor, insulation resistance



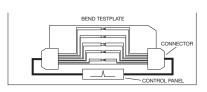
BOARD BEND TEST PROCEDURE

According to AEC-Q200

Test Procedure as per AEC-Q200: 20 components Sample size:

Span: 90mm Minimum deflection spec: 2 mm

- · Components soldered onto FR4 PCB (Figure 1)
- Board connected electrically to the test equipment



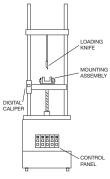


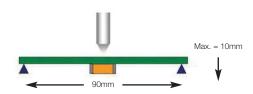
Fig 1 - PCB layout with electrical connections

Fig 2 - Board Bend test equipment

AVX ENHANCED SOFT TERMINATION BEND TEST PROCEDURE

Bend Test

The capacitor is soldered to the printed circuit board as shown and is bent up to 10mm at 1mm per second:



- · The board is placed on 2 supports 90mm apart (capacitor side down)
- The row of capacitors is aligned with the load stressing knife

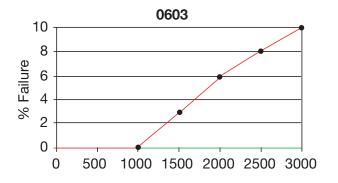


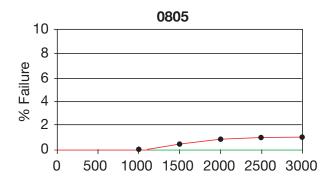
- · The load is applied and the deflection where the part starts to crack is recorded (Note: Equipment detects the start of the crack using a highly sensitive current detection circuit)
- The maximum deflection capability is 10mm

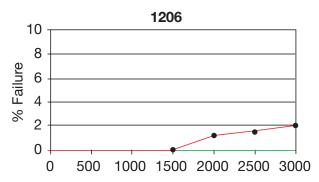
Specifications and Test Methods

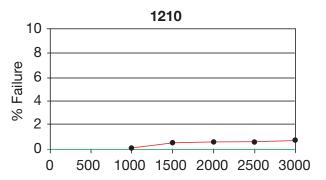


BEYOND 1000 CYCLES: TEMPERATURE CYCLE TEST RESULTS









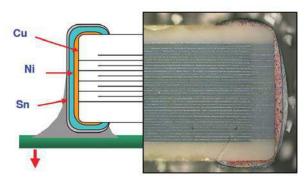
Soft Term - No Defects up to 3000 cycles

AEC-Q200 specification states 1000 cycles compared to AVX 3000 temperature cycles.

FLEXITERM® TEST SUMMARY

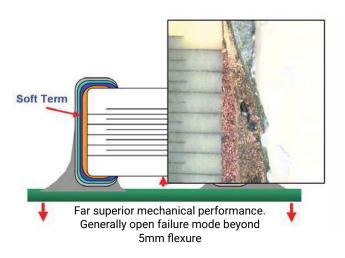
- Qualified to AEC-Q200 test/specification with the exception of using AVX 3000 temperature cycles (up to +150°C bend test guaranteed greater than 5mm).
- FLEXITERM® provides improved performance compared to standard termination systems.
- Board bend test improvement by a factor of 2 to 4 times.
- Temperature Cycling:
 - 0% Failure up to 3000 cycles
- No ESR change up to 3000 cycle

WITHOUT SOFT TERMINATION



Major fear is of latent board flex failures.

WITH SOFT TERMINATION



MLCC with FLEXITERM®



Capacitance Range X8R Dielectric

	SIZE	06	03	08	305	12	206
S	oldering	Reflow	//Wave	Reflov	v/Wave	Reflov	v/Wave
	WVDC	25V	50V	25V	50V	25V	50V
271	Cap 270	G	G				
331	(pF) 330	G	G	J	J		
471	470	G	G	J	J		
681	680	G	G	J	J		
102	1000	G	G	J	J	J	J
152	1500	G	G	J	J	J	J
182	1800	G	G	J	J	J	J
222	2200	G	G	J	J	J	J
272	2700	G	G	J	J	J	J
332	3300	G	G	J	J	J	J
392	3900	G	G	J	J	J	J
472	4700	G	G	J	J	J	J
562	5600	G	G	J	J	J	J
682	6800	G	G	J	J	J	J
822	8200	G	G	J	J	J	J
103	Cap 0.01	G	G	J	J	J	J
123	(µF) 0.012	G	G	J	J	J	J
153	0.015	G	G	J	J	J	J
183	0.018	G	G	J	J	J	J
223	0.022	G	G	J	J	J	J
273	0.027	G	G	J	J	J	J
333	0.033	G	G	J	J	J	J
393	0.039	G	G	J	J	J	J
473	0.047	G	G	J	J	J	J
563	0.056	G		N	N	М	M
683	0.068	G		N	N	М	М
823	0.082			N	N	M	M
104	0.1			N	N	M	M
124	0.12			N	N	М	М
154	0.15			N	N	М	М
184	0.18			N		М	М
224	0.22			N		М	М
274	0.27					М	М
334	0.33					М	М
394	0.39					М	
474	0.47					М	
684	0.68						
824	0.82						
105	1						
	WVDC	25V	50V	25V	50V	25V	50V
	SIZE	06	03	08	305	12	206

Letter	Α	С	Е	G	J	K	М	N	Р	Q	Х	Υ	Z
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)
			PAPER						EMBO	SSED			

MLCC with FLEXITERM®



Capacitance Range X7R Dielectric

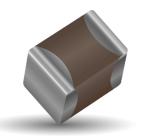
	Size	!	(040	2				06	03					0	805						120	6				12	210		18	12		2220	0
s	olderi	ng		eflov Nav				Re	eflow	//Wa	/e			F	Reflo	w/Wa	ave				Ret	flow/	Wave	9			Reflo	w Onl	у		low nly	Ref	flow (Only
	WVDC	2	16V	25V	50V	10V	160	25V	50V	100 V	200V	250V	16V	25V	50V	100 V	200V	250V	16V	25V	50V	100 V	200V	250V	500V	16V	25V	50V	100 V	50V	100 V	25V	50V	100 V
221	Cap	220	С	С	С																													
271	(pF)	270	С	С	С		İ															İ	İ											
331	"	330	С	С	С		T																											
391		390	С	С	С		İ																											
471		470	С	С	С		İ		T .				İ					i –				İ	İ											
561		560	С	С	С																													
681		680	C	С	C				1		İ																					T		
821		820	c	c	C		1		1				<u> </u>					1														\vdash		
102		1000	c	C	C		G	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	J	J	К	K	К	К	K	К			
182	$\neg \uparrow$	1800	Č	C	Č		G	Ğ	Ğ	G	G	G	Ĵ	J	Ĵ	J	J	J	J	J	J	J	Ĵ	J	J	K	K	K	K	K	K	\vdash	\vdash	
222		2200	c	C	c		G	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K			<u> </u>
332	$\neg \dagger$	3300	c	c	Č		G	G	Ğ	G	G	G	Ĵ	J	J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K		\vdash	<u> </u>
472		4700	c	C	c		G	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K	_		
103	Сар	0.01	c		Ŭ		G	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	J	J	K	K	K	K	K	K			
123	(µF)	0.012	c			\vdash	G	Ğ	Ğ	۲	Ť		J	J	Ĵ	М	J	J	J	J	J	J	J	J		K	K	K	K	K	K	\vdash		
153	(Pr.)	0.015	c			\vdash	G	G	G				J	J	J	М	J	J	J	J	J	J	J	J		K	K	K	K	K	K			
183	-	0.018	c				G	Ğ	Ğ				J	Ĵ	J	М	J	J	J	J	J	J	Ĵ	J		K	K	K	K	K	K	\vdash		
223		0.022	C				G	G	G				J	J	J	J	J	J	J	J	J	J	J	J		K	K	K	K	K	K			
273		0.027	c				G	Ğ	Ğ				J	J	J	М	J	J	J	J	J	J	J	J		K	K	K	K	K	K			
333	\rightarrow	0.033	_				G	G	G				J	J	J	М	J	J	J	J	J	J	J	J		K	K	K	K	K	K		 	
473		0.047					G	G	G				J	J	J	М	J	J	J	J	J	М	J	J		K	K	K	K	K	K			
563	-+	0.056				\vdash	G	G	Ğ				J	J	J	M	J	1 3	J	J	J	M	J	J		K	K	K	M	K	K	\vdash		
683		0.068				1	G	G	G				J	J	J	М		1	J	J	J	М	J	J		K	K	K	M	K	K	\vdash		
823		0.082					G	G	G				J	J	J	M			J	J	J	M	J	J		K	K	K	M	K	K	\vdash		
104		0.002	С			\vdash	G	G	G				J	J	М	М		1	J	J	J	М	J	J		K	K	K	M	K	K	<u> </u>		
124		0.12						-	0				J	J	M	N			J	J	М	M	J J	3		K	K	K	P	K	K	\vdash		
154		0.15				\vdash	+		+				М	Ň	М	N		 	J	J	М	М				K	K	K	P	K	K	<u> </u>		
224	-+	0.13				G		+					M	N	M	N			J	М	M	0				М	M	М	P	М	М	\vdash		
334	-+	0.33				0			+				N	N	M	N		 	J	M	P	Q				P	P	P	Q	X	X	\vdash		
474		0.47			\vdash	\vdash	+	+	1	t -			N	N	M	N			M	M	P	Q				P	P	P	Q	X	X	\vdash		t -
684	\dashv	0.47		\vdash	\vdash	\vdash	+	+	+	+	\vdash		N	N	N	1.4		\vdash	M	Q	Q	Q		\vdash		P	Р	Q	X	X	X	\vdash	\vdash	+
105		1			\vdash	\vdash	+	+	1	+			N	N	N		\vdash	 	M	Q	Q	Q		1		P	Q	Q	Z	X	X	\vdash	\vdash	+
155		1.5		\vdash	\vdash		+	+	+	+	-		IN	IN	IN		+	<u> </u>	0	Q	0	Ų		1		P	0	Z	Z	X	X		\vdash	+
225	\dashv	2.2			\vdash	1	+	+	+	+	<u> </u>	 	\vdash	\vdash	\vdash	 	 	!	0	Q	0		 	 		X	Z	Z	Z	Ž	Z	\vdash	\vdash	+
335		3.3				\vdash	+	+	+	+	 		\vdash	\vdash	 	1	 	<u> </u>	0	Q	Ų		 	 		X	Z	Z	Z	Z		\vdash	\vdash	+
475	-+	4.7	<u> </u>	\vdash	\vdash	\vdash	+	+	+	+	+		\vdash	\vdash	\vdash	\vdash	\vdash	_	0	0	\vdash			\vdash		X	Z	Z	Z	Z	\vdash	\vdash	\vdash	Z
106	-+	10			\vdash	\vdash	+	+	+	+	-		\vdash	\vdash	\vdash	1	 		Ų	Ų		-	-	1		Z	Z	1			-	\vdash	Z	Z
226	-+	22	<u> </u>	\vdash	\vdash	\vdash	+	+	+	\vdash	\vdash	_	\vdash	\vdash	\vdash	\vdash	\vdash	 	\vdash	\vdash	\vdash	_	_	\vdash						<u> </u>	\vdash	Z		
	WVDC		16\/	25\/	50\/	10\/	116\/	/ 25\/	1501	100 \	200V	250\/	16\/	251/	50\/	100 \	2001	2501/	161/	25\/	50\/	100 \	12001	2501/	500\/	16\/	25\/	50\/	100 V	501/	100 V		50\/	100 V
	Size			040		100	1100	1200		03	12001	230 V	100	1200		805	12001	12301	100	200	J 30 V	120		1230V	J000V	107			1 100 V		112	-	2220	
	Size		· ·	J4U	_				Ub	03					U	803						120	0				- 12	210		18	12		2220	U

Letter	А	С	E	G	J	K	М	N	Р	Q	Х	Υ	Z		
Max.	0.33	0.56	0.71	0.90	0.94	1.02	1.27	1.40	1.52	1.78	2.29	2.54	2.79		
Thickness	(0.013)	(0.022)	(0.028)	(0.035)	(0.037)	(0.040)	(0.050)	(0.055)	(0.060)	(0.070)	(0.090)	(0.100)	(0.110)		
		PAPER					EMBOSSED								

FLEXISAFE MLC Chips

General Specifications and Capacitance Range For Ultra Safety Critical Applications





AVX have developed a range of components specifically for safety critical applications.

Utilizing the award-winning FLEXITERM™ layer in conjunction with the cascade design previously used for high voltage MLCCs, a range of ceramic capacitors is now available for customers who require components designed with an industry leading set of safety features.

The FLEXITERM™ layer protects the component from any damage to the ceramic resulting from mechanical stress during PCB assembly or use with end customers. Board flexure type mechanical damage accounts for the majority of MLCC failures. The addition of the cascade structure protects the component from low insulation resistance failure resulting from other common causes for failure; thermal stress damage, repetitive strike ESD damage and placement damage. With the inclusion of the cascade design structure to complement the FLEXITERM™ layer, the FLEXISAFE range of capacitors has unbeatable safety features.

HOW TO ORDER

0805	<u>5</u>	<u>C</u>	<u>104</u>	K T	Q T	z 	2 T	<u>A</u>
Size FS03 = 0603 FS05 = 0805 FS06 = 1206 FS10 = 1210	Voltage 16V = Y 25V = 3 50V = 5 100V = 1	Dielectric X7R = C	Capacitance Code (In pF) 2 Sig. Digits + Number of Zeros e.g. 10µF =106	Capacitance Tolerance $J = \pm 5\%$ $K = \pm 10\%$ $M = \pm 20\%$	Failure Rate A = Commercial 4 = Automotive Q = APS	Terminations Z = FLEXITERMTM *X = FLEXITERMTM with 5% min lead *Not RoHS Compliant	Packaging 2 = 7" Reel 4 = 13" Reel	Special Code A = Std.Product

FLEXISAFE X7R RANGE

Capacitance Code Soldering WVDC		FS03 = 0603			FS05 = 0805 Reflow/Wave				FS06 = 1206 Reflow/Wave			FS10 = 1210 Reflow Only			
		Reflow/Wave													
		16	25	50	100	16	25	50	100	16	25	50	16	25	50
102	μF 0.001														
182	0.0018														
222	0.0022														
332	0.0033														
472	0.0047														
103	0.01														
123	0.012														
153	0.015														
183	0.018														
223	0.022														
273	0.027														
333	0.033														
473	0.047														
563	0.056														
683	0.068														
823	0.082														
104	0.1														
124	0.12														
154	0.15														
224	0.22														
334	0.33														
171	0.47			1			1					1			





Capacitor Array

Capacitor Array (IPC)



BENEFITS OF USING CAPACITOR ARRAYS

AVX capacitor arrays offer designers the opportunity to lower placement costs, increase assembly line output through lower component count per board and to reduce real estate requirements.

Reduced Costs

Placement costs are greatly reduced by effectively placing one device instead of four or two. This results in increased throughput and translates into savings on machine time. Inventory levels are lowered and further savings are made on solder materials, etc.

Space Saving

Space savings can be quite dramatic when compared to the use of discrete chip capacitors. As an example, the 0508 4-element array offers a space reduction of >40% vs. 4×0402 discrete capacitors and of >70% vs. 4×0603 discrete capacitors. (This calculation is dependent on the spacing of the discrete components.)

Increased Throughput

Assuming that there are 220 passive components placed in a mobile phone:

A reduction in the passive count to 200 (by replacing discrete components with arrays) results in an increase in throughput of approximately 9%.

A reduction of 40 placements increases throughput by 18%.

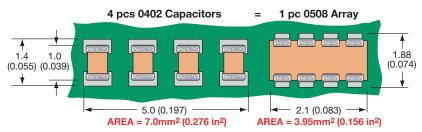
For high volume users of cap arrays using the very latest placement equipment capable of placing 10 components per second, the increase in throughput can be very significant and can have the overall effect of reducing the number of placement machines required to mount components:

If 120 million 2-element arrays or 40 million 4-element arrays were placed in a year, the requirement for placement equipment would be reduced by one machine.

During a 20Hr operational day a machine places 720K components. Over a working year of 167 days the machine can place approximately 120 million. If 2-element arrays are mounted instead of discrete components, then the number of placements is reduced by a factor of two and in the scenario where 120 million 2-element arrays are placed there is a saving of one pick and place machine.

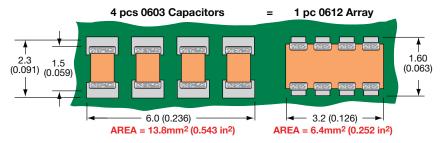
Smaller volume users can also benefit from replacing discrete components with arrays. The total number of placements is reduced thus creating spare capacity on placement machines. This in turn generates the opportunity to increase overall production output without further investment in new equipment.

W2A (0508) Capacitor Arrays



The 0508 4-element capacitor array gives a PCB space saving of over 40% vs four 0402 discretes and over 70% vs four 0603 discrete capacitors.

W3A (0612) Capacitor Arrays



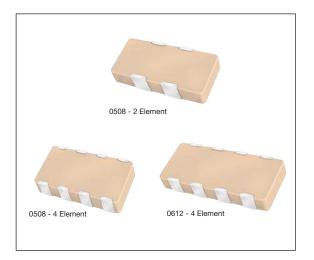
The 0612 4-element capacitor array gives a PCB space saving of over 50% vs four 0603 discretes and over 70% vs four 0805 discrete capacitors.



Capacitor Array

Capacitor Array (IPC)





GENERAL DESCRIPTION

AVX is the market leader in the development and manufacture of capacitor arrays. The array family of products also includes the 0612 4-element device as well as 0508 2-element and 4-element series, all of which have received widespread acceptance in the marketplace.

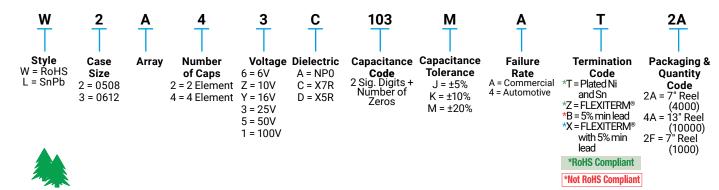
AVX capacitor arrays are available in X5R, X7R and NP0 (C0G) ceramic dielectrics to cover a broad range of capacitance values. Voltage ratings from 6.3 Volts up to 100 Volts are offered. AVX also now offers a range of automotive capacitor arrays qualified to AEC-Q200 (see separate table).

Key markets for capacitor arrays are Mobile and Cordless Phones, Digital Set Top Boxes, Computer Motherboards and Peripherals as well as Automotive applications, RF Modems, Networking Products, etc.

AVX Capacitor Array - W2A41A***K S21 Magnitude 0 -5 -10 -15 S21 mag. (dB) -20 5pF 10pF -25 15pF 22pF -30 33pF 39pF 68pF -35 -40 0.01 Frequency (GHz)

HOW TO ORDER

COMPLIANT



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.



Capacitor Array

Capacitance Range - NP0/C0G



S	IZE		W	2 = 05	08	W	3 = 061	12			
# Ele	men	ts		4		4					
	dering		Re	flow/Wa	ave	Re	flow/Wa	IVE			
	kaging			er/Embos			er/Embos				
1 40	качпч	mm		1.30 ± 0.1			.60 ± 0.15				
Length		(in.)		051 ± 0.0		(0.063 ± 0.006)					
-		mm		2.10 ± 0.1		3.20 ± 0.20					
Width		(in.)		0.0 ± 0.1			126 ± 0.0				
Maria		mm	(0.	0.94	00)	(0.	1.35	06)			
Max. Thickness		(in.)		(0.037)			(0.053)				
	VDC	(111.)	16	25	50	16	25	50			
1R0		1.0	10	2.5	30	10	2.5	30			
1R0	Cap	1.2									
1R5	(pF)	1.5									
1R8	-	1.8									
2R2		2.2									
2R7		2.7									
3R3		3.3									
3R9		3.9									
4R7		4.7									
5R6		5.6									
6R8		6.8									
8R2		8.2									
100		10									
120		12									
150		15									
180		18									
220		22									
270		27									
330		33									
390		39									
470		47									
560		56									
680		68									
820		82									
101		100									
121		120									
151		150									
181		180									
221		220									
271		270									
331		330									
391		390									
471		470									
561		560									
681		680									
821		820									
102		1000									
122		1200									
152		1500									
182		1800									
222		2200									
272		2700									
332		3300									
392		3900									
472		4700									
562		5600									
682		6800									
822		8200			<u> </u>	<u> </u>		<u> </u>			

= Supported Values

Capacitor Array

Capacitance Range - X7R



	SIZE			W2 = 0508 W2 = 050								08 W3 = 0612									
#	Elemen	ıts				2			4						4						
- "	Soldering					v/Wav	'e		Reflow/Wave						Reflow/Wave						
	Packagino					Paper			Paper/Embossed						Paper/Embossed						
		mm				± 0.15			1.30 ± 0.15						1.60 ± 0.150						
Lengt	h	(in.)		(0		± 0.00						± 0.00			(0.063 ± 0.006)						
Width		mm			2.10	± 0.15					2.10 :	± 0.15					3.20 :	± 0.20			
Width		(in.)		(0	0.083	± 0.00	6)			(0	.083 :	± 0.00	6)			((0.126 :	± 0.00	8)		
Мах.		mm			0.	.94						94					1.	35			
Thick		(in.)				037))37)					$\overline{}$)53)			
	WVDC		6	10	16	25	50	100	6	10	16	25	50	100	6	10	16	25	50	100	
101	Сар	100																			
121	(PF)	120																			
151 181		150 180																			
221		220																			
271		270																			
331		330																			
391		390																			
471		470																			
561		560																			
681		680																			
821		820																			
102		1000 1200																			
122 152		1500																			
182		1800																			
222		2200																			
272		2700																			
332		3300																			
392		3900																			
472		4700																			
562		5600																			
682		6800																			
822	0	8200												-							
103 123		0.010 0.012																			
153		0.012																			
183		0.018																			
223		0.022																			
273		0.027																			
333		0.033																			
393		0.039																			
473		0.047										_		_						Ш	
563 683		0.056 0.068																			
823		0.082																			
104		0.10																			
124		0.12										1							1		
154		0.15					Ш											ļ		Ш	
184		0.18																			
224		0.22																			
274 334		0.27				 	$\vdash \vdash$			<u> </u>		 	<u> </u>	 		<u> </u>	-	-	-	\vdash	
474		0.33 0.47																			
564		0.47																			
684		0.68																		Н	
824		0.82																			
105		1.0				L_			L		L		L_	L_	L	L_	<u> </u>	L	<u> </u>		
125		1.2																			
155		1.5																			
185		1.8				<u> </u>	Ш			<u> </u>		<u> </u>		<u> </u>		<u> </u>	-	-	-	Ш	
225		2.2																			
335 475		3.3 4.7																			
106		10					Н						\vdash	\vdash	\vdash	\vdash				\vdash	
226		22																			
476		47																			
107		100																			

Capacitor Array

Automotive Capacitor Array (IPC)









0612 - 4 Element

As the market leader in the development and manufacture of capacitor arrays AVX is pleased to offer a range of AEC-Q200 qualified arrays to compliment our product offering to the Automotive industry. Both the AVX 0612 and 0508 4-element capacitor array styles are qualified to the AEC-Q200 automotive specifications.

AEC-Q200 is the Automotive Industry qualification standard and a detailed qualification package is available on request.

All AVX automotive capacitor array production facilities are certified to ISO/TS 16949:2002.

HOW TO ORDER

<u>W</u>	3	<u>A</u>	4	<u>Y</u>	C	<u>104</u>	<u>K</u>	4	<u>T</u>	2 A
Style W = RoHS L = SnPb	Case Size 2 = 0508 3 = 0612	Array	Number of Caps	Voltage Z = 10V Y = 16V 3 = 25V 5 = 50V 1 = 100V	Dielectric A = NP0 C = X7R F = X8R	Capacitance Code (In pF) Significant Digits + Number of Zeros e.g. 10µF=106	Capacitance Tolerance *J = ±5% *K = ±10% *M = ±20%	Failure Rate 4 = Automotive	Terminations *T = Plated Ni and Sn *Z = FLEXITERM® B = 5% min lead X = FLEXITERM® with 5% min lead *RoHS Compliant	Packaging & Quantity Code 2A = 7" Reel (4000) 4A = 13" Reel (10000) 2F = 7" Reel (1000)

^{*}Contact factory for availability by part number for $K = \pm 10\%$ and $J = \pm 5\%$ tolerance.

NPO/COG

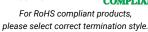
SIZE W2 = 0508 W	V3 = 0612
No. of Elements 4 F	Reflow/Wave
WVDC 16 25 50 100 16	25 50 100
1R0 Cap 1.0	
1R2 (pF) 1.2	
1R5 1.5	
1R8 1.8	
2R2 2.2	i i
2R7 2.7	i i
3R3 3.3	
3R9 3.9	
4R7 4.7	
5R6 5.6	
6R8 6.8	
8R2 8.2	
100 10	
120 12	
150 15	
180 18	
220 22	
270 27	
330 33	
390 39	
470 47 560 56	
680 68	
820 82	
101 100	
121 120	
151 150	
181 180	
221 220	
271 270	
331 330	
391 390	
471 470	
561 560	
681 680	
821 820	
102 1000	
122 1200	
152 1500	
182 1800	
222 2200	
272 2700	
332 3300	
392 3900	
472 4700	
562 5600 682 6800	
1 002 0800	

W2 = 0508 W2 = 0508 W3 = 0612 No. of Elements
 16
 25
 50
 100
 16
 25
 50
 100
 10
 16
 25
 50
 100
 WVDC (pF) 120 151 181 221 150 180 220 271 331 391 270 330 390 470 1000 1200 1500 1800 222 2200 392 3900 472 562 682 4700 5600 6800 822 103 123 153 8200 Cap 0 010 (µF) 0.012 0.015 273 333 393 473 563 0.039 683 0.068 823 104 124 0.082 0.10 0.12

*Not RoHS Compliant







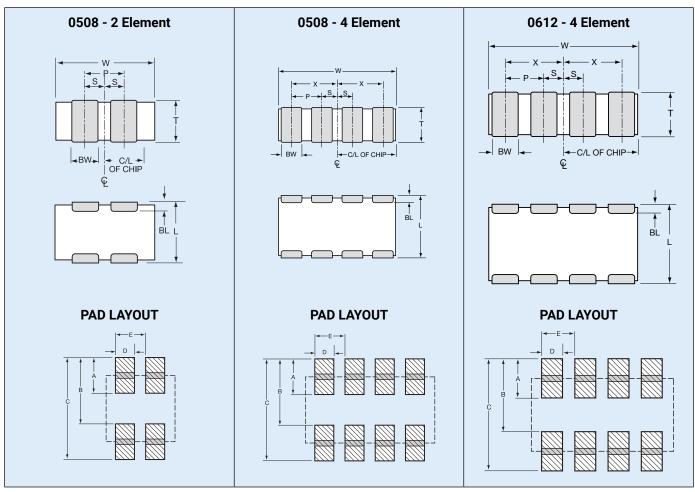


= NPO/COG



PART & PAD LAYOUT DIMENSIONS

millimeters (inches)



PART DIMENSIONS

0508 - 2 Element

L	W	T	BW	BL	Р	S
1.30 ± 0.15	2.10 ± 0.15	0.94 MAX	0.43 ± 0.10	0.33 ± 0.08	1.00 REF	0.50 ± 0.10
(0.051 ± 0.006)	(0.083 ± 0.006)	(0.037 MAX)	(0.017 ± 0.004)	(0.013 ± 0.003)	(0.039 REF)	(0.020 ± 0.004)

0508 - 4 Element

Į	L	W	Т	BW	BL	Р	X	S
ı	1.30 ± 0.15	2.10 ± 0.15	0.94 MAX	0.25 ± 0.06	0.20 ± 0.08	0.50 REF	0.75 ± 0.10	0.25 ± 0.10
Į	(0.051 ± 0.006)	(0.083 ± 0.006)	(0.037 MAX)	(0.010 ± 0.003)	(0.008 ± 0.003)	(0.020 REF)	(0.030 ± 0.004)	(0.010 ± 0.004)

0612 - 4 Element

L	W	Т	BW	BL	Р	Х	S
1.60 ± 0.20	3.20 ± 0.20	1.35 MAX	0.41 ± 0.10	0.18 +0.25	0.76 REF	1.14 ± 0.10	0.38 ± 0.10
(0.063 ± 0.008)	(0.126 ± 0.008)	(0.053 MAX)	(0.016 ± 0.004)	(0.007+0.010)	(0.030 REF)	(0.045 ± 0.004)	(0.015 ± 0.004)

PAD LAYOUT DIMENSIONS

0508 - 2 Element

Α	В	С	D	E
0.68	1.32	2.00	0.46	1.00
(0.027)	(0.052)	(0.079)	(0.018)	(0.039)

0508 - 4 Element

Α	В	С	D	E
0.56	1.32	1.88	0.30	0.50
(0.022)	(0.052)	(0.074)	(0.012)	(0.020)

0612 - 4 Element

Α	В	С	D	E		
0.89	1.65	2.54	0.46	0.76		
(0.035)	(0.065)	(0.100)	(0.018)	(0.030)		

Low Inductance Capacitors

Introduction



The signal integrity characteristics of a Power Delivery Network (PDN) are becoming critical aspects of board level and semiconductor package designs due to higher operating frequencies, larger power demands, and the ever shrinking lower and upper voltage limits around low operating voltages. These power system challenges are coming from mainstream designs with operating frequencies of 300MHz or greater, modest ICs with power demand of 15 watts or more, and operating voltages below 3 volts.

The classic PDN topology is comprised of a series of capacitor stages. Figure 1 is an example of this architecture with multiple capacitor stages.

An ideal capacitor can transfer all its stored energy to a load instantly. A real capacitor has parasitics that prevent instantaneous transfer of a capacitor's stored energy. The true nature of a capacitor can be modeled as an RLC equivalent circuit. For most simulation purposes, it is possible to model the characteristics of a real capacitor with one capacitor, one resistor, and one inductor. The RLC values in this model are commonly referred to as equivalent series capacitance (ESC), equivalent series resistance (ESR), and equivalent series inductance (ESL).

The ESL of a capacitor determines the speed of energy transfer to a load. The lower the ESL of a capacitor, the faster that energy can be transferred to a load. Historically, there has been a tradeoff between energy storage (capacitance) and inductance (speed of energy delivery). Low ESL devices typically have low capacitance. Likewise, higher capacitance devices typically have higher ESLs. This tradeoff between ESL (speed of energy delivery) and capacitance (energy storage) drives the PDN design topology that places the fastest low ESL capacitors as close to the load as possible. Low Inductance MLCCs are found on semiconductor packages and on boards as close as possible to the load.

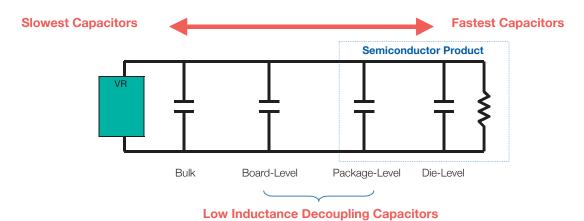


Figure 1 Classic Power Delivery Network (PDN) Architecture

LOW INDUCTANCE CHIP CAPACITORS

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL. A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer side of its rectangular shape.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL then an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with an LICC versus a standard MLCC.

INTERDIGITATED CAPACITORS

The size of a current loop has the greatest impact on the ESL characteristics of a surface mount capacitor. There is a secondary method for decreasing the ESL of a capacitor. This secondary method uses adjacent opposing current loops to reduce ESL. The InterDigitated Capacitor (IDC) utilizes both primary and secondary methods of reducing inductance. The IDC architecture shrinks the distance between terminations to minimize the current loop size, then further reduces inductance by creating adjacent opposing current loops.

An IDC is one single capacitor with an internal structure that has been optimized for low ESL. Similar to standard MLCC versus LICCs, the reduction in ESL varies by EIA case size. Typically, for the same EIA size, an IDC delivers an ESL that is at least 80% lower than an MLCC.

Low Inductance Capacitors

Introduction



LAND GRID ARRAY (LGA) CAPACITORS

Land Grid Array (LGA) capacitors are based on the first Low ESL MLCC technology created to specifically address the design needs of current day Power Delivery Networks (PDNs). This is the 3rd low inductance capacitor technology developed by AVX. LGA technology provides engineers with new options. The LGA internal structure and manufacturing technology eliminates the historic need for a device to be physically small to create small current loops to minimize inductance.

The first family of LGA products are 2 terminal devices. A 2 terminal 0306 LGA delivers ESL performance that is equal to or better than an 0306 8 terminal IDC. The 2 terminal 0805 LGA delivers ESL performance that approaches the 0508 8 terminal IDC. New designs that would have used 8 terminal IDCs are moving to 2 terminal LGAs because the layout is easier for a 2 terminal device and manufacturing yield is better for a 2 terminal LGA versus an 8 terminal IDC.

LGA technology is also used in a 4 terminal family of products that AVX is sampling and will formerly introduce in 2008. Beyond 2008, there are new multi-terminal LGA product families that will provide even more attractive options for PDN designers.

LOW INDUCTANCE CHIP ARRAYS (LICA®)

The LICA® product family is the result of a joint development effort between AVX and IBM to develop a high performance MLCC family of decoupling capacitors. LICA was introduced in the 1980s and remains the leading choice of designers in high performance semiconductor packages and high reliability board level decoupling applications.

LICA® products are used in 99.999% uptime semiconductor package applications on both ceramic and organic substrates. The C4 solder ball termination option is the perfect compliment to flip-chip packaging technology. Mainframe class CPUs, ultimate performance multi-chip modules, and communications systems that must have the reliability of 5 9's use LICA®.

LICA® products with either Sn/Pb or Pb-free solder balls are used for decoupling in high reliability military and aerospace applications. These LICA® devices are used for decoupling of large pin count FPGAs, ASICs, CPUs, and other high power ICs with low operating voltages.

When high reliability decoupling applications require the very lowest ESL capacitors, LICA® products are the best option.

470 nF 0306 Impedance Comparison

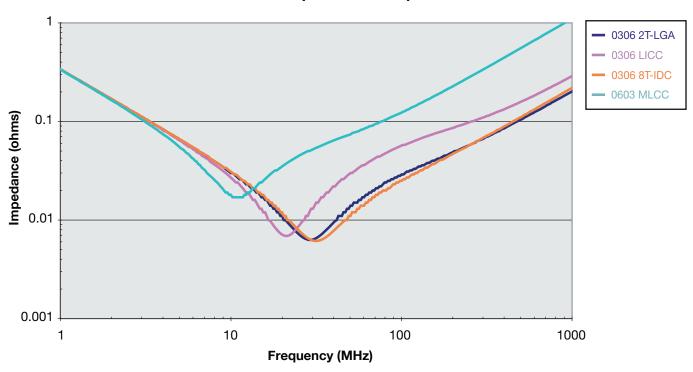


Figure 2 MLCC, LICC, IDC, and LGA technologies deliver different levels of equivalent series inductance (ESL).

Low Inductance Ceramic Capacitors



LICC (Low Inductance Chip Capacitors) 0306/0508/0612 RoHS Compliant

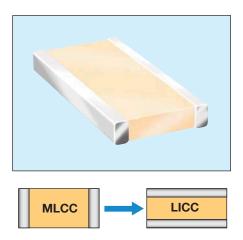
GENERAL DESCRIPTION

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL.

A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer sides of its rectangular shape. The image on the right shows the termination differences between an MLCC and an LICC.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL then an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with an LICC versus a standard MLCC.

AVX LICC products are available with a lead-free finish of plated Nickel/Tin.

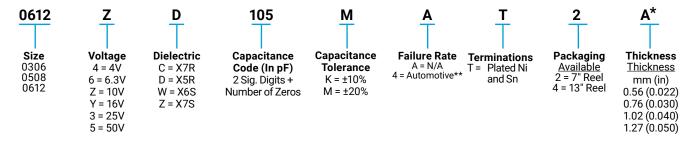


PERFORMANCE CHARACTERISTICS

Capacitance Tolerances	K = ±10%; M = ±20%
Operation Temperature Range	X7R = -55°C to +125°C X5R = -55°C to +85°C X7S = -55°C to +125°C
Temperature Coefficient	X7R, X5R = ±15%; X7S = ±22%
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	4V, 6.3V = 6.5% max; 10V = 5.0% max; 16V = 3.5% max; 25V = 3.0% max
Insulation Resistance (@+25°C, RVDC)	100,000MΩ min, or 1,000MΩ per μF min.,whichever is less



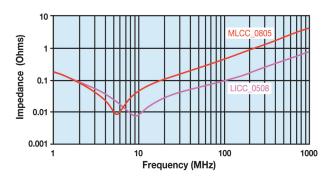
HOW TO ORDER

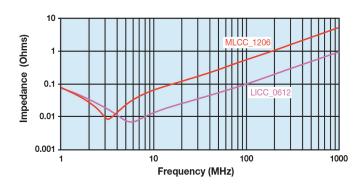


^{*}See the thickness tables on the next page.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

TYPICAL IMPEDANCE CHARACTERISTICS







^{**}Select voltages for Automotive version, contact factory

Low Inductance Ceramic Capacitors



LICC (Low Inductance Chip Capacitors) 0306/0508/0612 RoHS Compliant

S	SIZE			0306	•			(0508	3		0612					
Pac	kaging		En	nboss	ed			Embossed					Embossed				
Length	mm (in.)		0.81 + 0.15 (0.032 ± 0.006)					1.27 + 0.25 (0.050 ± 0.010)				1.60 + 0.25 (0.063 ± 0.010)					
Width	mm (in.)		1.60 + 0.15 (0.063 ± 0.006)						00 + 0. 30 ± 0.					20 + 0 26 ± 0			
Cap Code	WVDC	4	6.3	10	16	25	6.3	10	16	25	50	6.3	10	16	25	50	
102	Cap 0.001		Α	Α	Α	Α	S	S	S	S	V	S	S	S	S	V	
222	(μF) .0022		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧	
332	0.0033		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧	
472	0.0047		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧	
682	0.0068		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧	
103	0.01		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	٧	
153	0.015		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	W	
223	0.022		Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	W	
333	0.033		Α	Α	Α		S	S	S	٧	٧	S	S	S	S	W	
473	0.047		Α	Α	Α		S	S	S	٧	Α	S	S	S	S	W	
683	0.068		Α	Α	Α		S	S	S	Α	Α	S	S	S	٧	W	
104	0.1		Α	Α	///		S	S	٧	Α	Α	S	S	S	٧	W	
154	0.15		Α	Α			S	S	٧			S	S	S	W	W	
224	0.22		Α	Α			S	S	Α			S	S	٧	W		
334	0.33						٧	٧	Α			S	S	٧			
474	0.47						٧	٧	/N/			S	S	٧			
684	0.68						Α	Α				V	٧	W			
105	1	A					Α	Α				V	٧	Α			
155	1.5											W	W				
225	2.2											Α	Α				
335	3.3																
475	4.7																
685	6.8																
106	10																

Solid = X7R





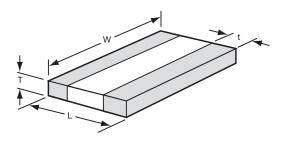


mm (in.) 0306 Code Thickness A 0.56 (0.022)

	mm (in.)
	0508
Code	Thickness
S	0.56 (0.022)
V	0.76 (0.030)
A	1.02 (0.040)

	mm (in.)				
	0612				
Code	Thickness				
S	0.56 (0.022)				
V	0.76 (0.030)				
W	1.02 (0.040)				
Α	1.27 (0.050)				

PHYSICAL DIMENSIONS AND **PAD LAYOUT**



PHYSICAL DIMENSIONS

mm (in.)

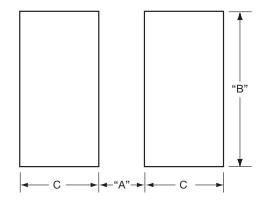
Size	L	W	t
0206	0.81 ± 0.15	1.60 ± 0.15	0.13 min.
0306	(0.032 ± 0.006)	(0.063 ± 0.006)	(0.005 min.)
0508	1.27 ± 0.25	2.00 ± 0.25	0.13 min.
0508	(0.050 ± 0.010)	(0.080 ± 0.010)	(0.005 min.)
0612	1.60 ± 0.25	3.20 ± 0.25	0.13 min.
0012	(0.063 ± 0.010)	(0.126 ± 0.010)	(0.005 min.)

T - See Range Chart for Thickness and Codes

PAD LAYOUT DIMENSIONS

mm (in)

			111111 (111.)
Size	Α	В	С
0306	0.31 (0.012)	1.52 (0.060)	0.51 (0.020)
0508	0.51 (0.020)	2.03 (0.080)	0.76 (0.030)
0612	0.76 (0.030)	3.05 (0.120)	0.635 (0.025)



Low Inductance Capacitors with SnPb Terminations

LD16/LD17/LD18 Tin-Lead Termination "B"



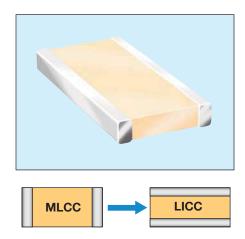
GENERAL DESCRIPTION

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL.

A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer sides of its rectangular shape. The image on the right shows the termination differences between an MLCC and an LICC.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL then an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with an LICC versus a standard MLCC.

AVX LICC products are available with a lead termination for high reliability military and aerospace applications that must avoid tin whisker reliability

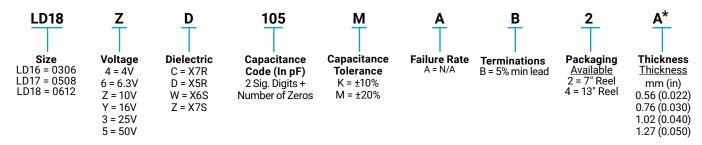


PERFORMANCE CHARACTERISTICS

Capacitance Tolerances	K = ±10%; M = ±20%
Operation Temperature Range	X7R = -55°C to +125°C X5R = -55°C to +85°C X7S = -55°C to +125°C
Temperature Coefficient	X7R, X5R = ±15%; X7S = ±22%
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	4V, 6.3V = 6.5% max; 10V = 5.0% max; 16V = 3.5% max; 25V = 3.0% max
Insulation Resistance (@+25°C, RVDC)	100,000MΩ min, or 1,000MΩ per μF min.,whichever is less

*Not RoHS Compliant

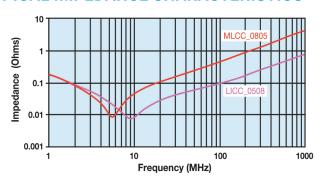
HOW TO ORDER

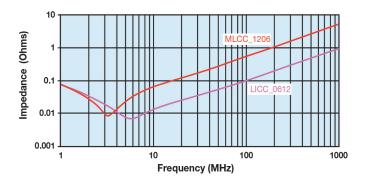


*See the thickness tables on the next page.

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

TYPICAL IMPEDANCE CHARACTERISTICS







Low Inductance Capacitors with SnPb Terminations

LD16/LD17/LD18 Tin-Lead Termination "B"



S	IZE		(03					LD17 0508					LD18 0612		
Pac	kaging		Embossed		Embossed			Embossed							
Length	mm (in.)	(0.81 ± 0.032 ±	0.006	5)		1.27 ± 0.25 (0.050 ± 0.010)			1.60 ± 0.25 (0.063 ± 0.010)					
Width	mm (in.)		1.60 ± 0.063 ±		5)			00 ± 0. 30 ± 0.				3.20 ± 0.25 (0.126 ± 0.010)			
Cap Code	WVDC	6.3	10	16	25	6.3	10	16	25	50	6.3	10	16	25	50
102	Cap 0.001	Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	V
222	(μF) .0022	Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	V
332	0.0033	Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	V
472	0.0047	Α	Α	Α	Α	S	S	S	S	>	S	S	S	S	V
682	0.0068	Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	V
103	0.01	Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	V
153	0.015	Α	Α	Α	Α	S	S	S	S	>	S	S	S	S	W
223	0.022	Α	Α	Α	Α	S	S	S	S	٧	S	S	S	S	W
333	0.033	Α	Α	Α		S	S	S	٧	٧	S	S	S	S	W
473	0.047	Α	Α	Α		S	S	S	٧	Α	S	S	S	S	W
683	0.068	Α	Α	Α		S	S	S	Α	Α	S	S	S	٧	W
104	0.1	Α	Α	//		S	S	٧	Α	Α	S	S	S	٧	W
154	0.15	Α	Α			S	S	٧			S	S	S	W	W
224	0.22	Α	Α			S	S	Α			S	S	V	W	
334	0.33					٧	٧	Α			S	S	٧		
474	0.47					٧	٧	/M //			S	S	٧		
684	0.68					Α	Α				V	٧	W		
105	1					Α	Α				V	٧	Α		
155	1.5					/ <u>k</u> //					W	W			
225	2.2										Α	Α			
335	3.3										//				
475	4.7														
685	6.8														
106	10														

Solid = X7R



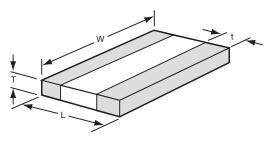


mm (in.)				
LD16				
(0306)				
Code	Thickness			
Α	0.56 (0.022)			

mm (in.)		
	LD17	
((0508)	
Code	Thickness	
S	0.56 (0.022)	
V	0.76 (0.030)	
Α	1.02 (0.040)	

	mm (in.)		
	LD18		
(0612)			
Code	Thickness		
S	0.56 (0.022)		
V	0.76 (0.030)		
W	1.02 (0.040)		
Α	1.27 (0.050)		

PHYSICAL DIMENSIONS AND **PAD LAYOUT**



PHYSICAL DIMENSIONS

mm (in.)

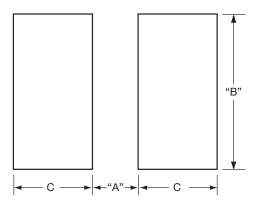
Size	L	W	t
LD16	0.81 ± 0.15	1.60 ± 0.15	0.13 min.
(0306)	(0.032 ± 0.006)	(0.063 ± 0.006)	(0.005 min.)
LD17	1.27 ± 0.25	2.00 ± 0.25	0.13 min.
(0508)	(0.050 ± 0.010)	(0.080 ± 0.010)	(0.005 min.)
LD18	1.60 ± 0.25	3.20 ± 0.25	0.13 min.
(0612)	(0.063 ± 0.010)	(0.126 ± 0.010)	(0.005 min.)

T - See Range Chart for Thickness and Codes

PAD LAYOUT DIMENSIONS

mm (in.)

			,
Size	Α	В	С
LD16 (0306)	0.31 (0.012)	1.52 (0.060)	0.51 (0.020)
LD17 (0508)	0.51 (0.020)	2.03 (0.080)	0.76 (0.030)
LD18 (0612)	0.76 (0.030)	3.05 (0.120)	0.635 (0.025)



IDC Low Inductance Capacitors (RoHS)

IDC (InterDigitated Capacitors) 0306/0612/0508



GENERAL DESCRIPTION

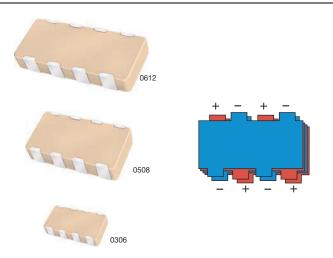
Inter-Digitated Capacitors (IDCs) are used for both semiconductor package and board level decoupling. The equivalent series inductance (ESL) of a single capacitor or an array of capacitors in parallel determines the response time of a Power Delivery Network (PDN). The lower the ESL of a PDN, the faster the response time. A designer can use many standard MLCCs in parallel to reduce ESL or a low ESL Inter-Digitated Capacitor (IDC) device. These IDC devices are available in versions with a maximum height of 0.95mm or 0.55mm.

IDCs are typically used on packages of semiconductor products with power levels of 15 watts or greater. Inter-Digitated Capacitors are used on CPU, GPU, ASIC, and ASSP devices produced on $0.13\mu, 90nm, 65nm,$ and 45nm processes. IDC devices are used on both ceramic and organic package substrates. These low ESL surface mount capacitors can be placed on the bottom side or the top side of a package substrate. The low profile 0.55mm maximum height IDCs can easily be used on the bottom side of BGA packages or on the die side of packages under a heat spreader.

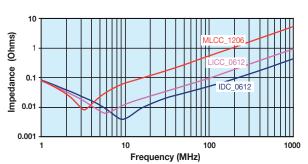
IDCs are used for board level decoupling of systems with speeds of 300MHz or greater. Low ESL IDCs free up valuable board space by reducing the number of capacitors required versus standard MLCCs. There are additional benefits to reducing the number of capacitors beyond saving board space including higher reliability from a reduction in the number of components and lower placement costs based on the need for fewer capacitors.

The Inter-Digitated Capacitor (IDC) technology was developed by AVX. This is the second family of Low Inductance MLCC products created by AVX. IDCs are a cost effective alternative to AVX's first generation low ESL family for high-reliability applications known as LICA (Low Inductance Chip Array).

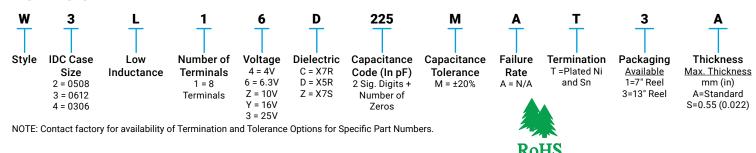
AVX IDC products are available with a lead-free finish of plated Nickel/Tin.



TYPICAL IMPEDANCE



HOW TO ORDER



PERFORMANCE CHARACTERISTICS

Capacitance Tolerance	±20% Preferred
Operation Temperature Range	X7R = -55°C to +125°C X5R = -55°C to +85°C X7S = -55°C to +125°C
Temperature Coefficient	±15% (0VDC), ±22% (X7S)
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	≤ 6.3V = 6.5% max; 10V = 5.0% max; ≥ 16V = 3.5% max
Insulation Resistance (@+25°C, RVDC)	100,000MΩ min, or 1,000MΩ per μF min.,whichever is less

Dissipation Factor	No problems observed after 2.5 x RVDC for 5 seconds at 50mA max current
CTE (ppm/C)	12.0
Thermal Conductivity	4-5W/M K
Terminations Available	Plated Nickel and Solder

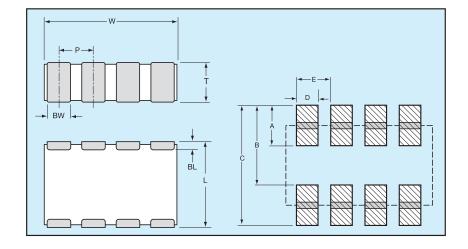
IDC Low Inductance Capacitors (RoHS)





SIZE	W4 =	0306		W2 =	Thin	0508	3		W2	2 = 05	508		W	3= Th	nin 06	12		W3	3 = 06	512		W3	= TH	CK 0	612
Max. mm		55			0.55.						0.95 0.55 0.95					0.95 (0.037)			.22						
Thickness (in.))22)	-	_	(0.022					(0.037					022)				•	í —				48)	
WVDC	4	6.3	4	6.3	10	16	25	4	6.3	10	16	25	4	6.3	10	16	4	6.3	10	16	25	4	6.3	10	16
Cap (μF) 0.010																									
0.022																									
0.033																									
0.047																									
0.068																									
0.10																									
0.22																									
0.33																									
0.47																									
0.68																									
1.0																									
1.5																									
2.2																									
3.3																									

PHYSICAL DIMENSIONS AND PAD LAYOUT



Consult factory for additional requirements



PHYSICAL CHIP DIMENSIONSMILLIMETERS (INCHES)

SIZE	W	L	BW	BL	Р
0306	1.60 ± 0.20	0.82 ± 0.10	0.25 ± 0.10	0.20 ± 0.10	0.40 ± 0.05
0306	(0.063 ± 0.008)	(0.032 ± 0.006)	(0.010 ± 0.004)	(0.008± 0.004)	(0.015 ± 0.002)
0500	2.03 ± 0.20	1.27 ± 0.20	0.30 ± 0.10	0.25 ± 0.15	0.50 ± 0.05
0508	(0.080 ± 0.008)	(0.050 ± 0.008)	(0.012 ± 0.004)	(0.010± 0.006)	(0.020 ± 0.002)
0612	3.20 ± 0.20	1.60 ± 0.20	0.50 ± 0.10	0.25 ± 0.15	0.80 ± 0.10
0012	(0.126 ± 0.008)	(0.063 ± 0.008)	(0.020 ± 0.004)	(0.010 ± 0.006)	(0.031 ± 0.004)

PAD LAYOUT DIMENSIONS

SIZE	Α	В	С	D	Е
0306	0.38	0.89	1.27	0.20	0.40
	(0.015)	(0.035)	(0.050)	(0.008)	(0.015)
0508	0.64	1.27	1.91	0.28	0.50
	(0.025)	(0.050)	(0.075)	(0.011)	(0.020)
0612	0.89	1.65	2.54	0.45	0.80
	(0.035)	(0.065)	(0.010)	(0.018)	(0.031)

IDC Low Inductance Capacitors (SnPb)

IDC (InterDigitated Capacitors) 0306/0612/0508



GENERAL DESCRIPTION

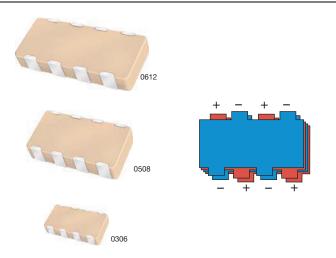
Inter-Digitated Capacitors (IDCs) are used for both semiconductor package and board level decoupling. The equivalent series inductance (ESL) of a single capacitor or an array of capacitors in parallel determines the response time of a Power Delivery Network (PDN). The lower the ESL of a PDN, the faster the response time. A designer can use many standard MLCCs in parallel to reduce ESL or a low ESL Inter-Digitated Capacitor (IDC) device. These IDC devices are available in versions with a maximum height of 0.95mm or 0.55mm.

IDCs are typically used on packages of semiconductor products with power levels of 15 watts or greater. Inter-Digitated Capacitors are used on CPU, GPU, ASIC, and ASSP devices produced on $0.13\mu, 90nm, 65nm,$ and 45nm processes. IDC devices are used on both ceramic and organic package substrates. These low ESL surface mount capacitors can be placed on the bottom side or the top side of a package substrate. The low profile 0.55mm maximum height IDCs can easily be used on the bottom side of BGA packages or on the die side of packages under a heat spreader.

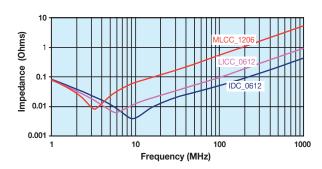
IDCs are used for board level decoupling of systems with speeds of 300MHz or greater. Low ESL IDCs free up valuable board space by reducing the number of capacitors required versus standard MLCCs. There are additional benefits to reducing the number of capacitors beyond saving board space including higher reliability from a reduction in the number of components and lower placement costs based on the need for fewer capacitors.

The Inter-Digitated Capacitor (IDC) technology was developed by AVX. This is the second family of Low Inductance MLCC products created by AVX. IDCs are a cost effective alternative to AVX's first generation low ESL family for high-reliability applications known as LICA (Low Inductance Chip Array).

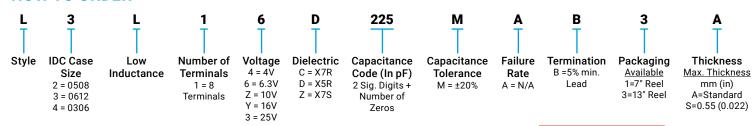
AVX IDC products are available with a lead termination for high reliability military and aerospace applications that must avoid tin whisker reliability issues.



TYPICAL IMPEDANCE



HOW TO ORDER



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

*Not RoHS Compliant

PERFORMANCE CHARACTERISTICS

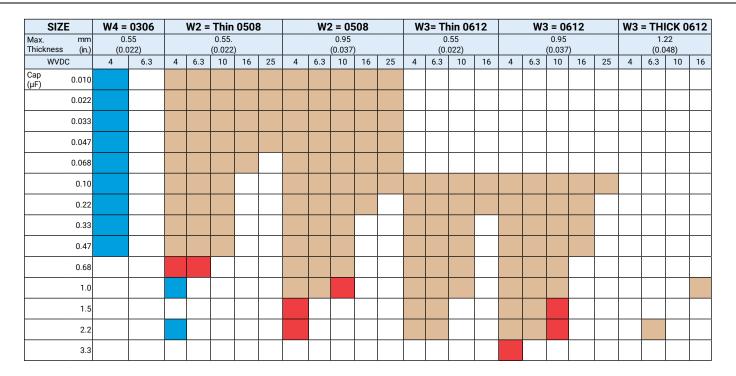
Capacitance Tolerance	±20% Preferred
Operation Temperature Range	X7R = -55°C to +125°C X5R = -55°C to +85°C X7S = -55°C to +125°C
Temperature Coefficient	±15% (0VDC), ±22% (X7S)
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	≤ 6.3V = 6.5% max; 10V = 5.0% max; ≥ 16V = 3.5% max
Insulation Resistance (@+25°C, RVDC)	100,000MΩ min, or 1,000MΩ per μF min.,whichever is less

Dissipation Factor	No problems observed after 2.5 x RVDC for 5 seconds at 50mA max current
CTE (ppm/C)	12.0
Thermal Conductivity	4-5W/M K
Terminations Available	Plated Nickel and Solder

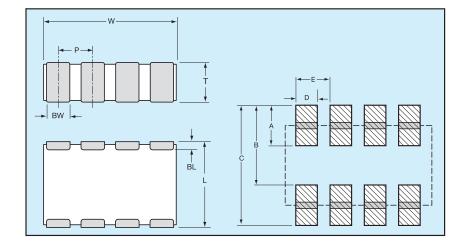
IDC Low Inductance Capacitors (SnPb)



IDC (InterDigitated Capacitors) with Sn/Pb Termination 0306/0612/0508



PHYSICAL DIMENSIONS AND PAD LAYOUT



Consult factory for additional requirements



PHYSICAL CHIP DIMENSIONSMILLIMETERS (INCHES)

SIZE	W	L	BW	BL	Р
0306	1.60 ± 0.20	0.82 ± 0.10	0.25 ± 0.10	0.20 ± 0.10	0.40 ± 0.05
0306	(0.063 ± 0.008)	(0.032 ± 0.006	(0.010 ± 0.004)	(0.008± 0.004)	(0.015 ± 0.002)
0500	2.03 ± 0.20	1.27 ± 0.20	0.30 ± 0.10	0.25 ± 0.15	0.50 ± 0.05
0508	(0.080 ± 0.008)	(0.050 ± 0.008)	(0.012 ± 0.004)	(0.010± 0.006)	(0.020 ± 0.002)
0612	3.20 ± 0.20	1.60 ± 0.20	0.50 ± 0.10	0.25 ± 0.15	0.80 ± 0.10
0012	(0.126 ± 0.008)	(0.063 ± 0.008)	(0.020 ± 0.004)	(0.010 ± 0.006)	(0.031 ± 0.004)

PAD LAYOUT DIMENSIONS

SIZE	Α	В	С	D	Е
0306	0.38	0.89	1.27	0.20	0.40
	(0.015)	(0.035)	(0.050)	(0.008)	(0.015)
0508	0.64	1.27	1.91	0.28	0.50
	(0.025)	(0.050)	(0.075)	(0.011)	(0.020)
0612	0.89	1.65	2.54	0.45	0.80
	(0.035)	(0.065)	(0.010)	(0.018)	(0.031)

LGA Low Inductance Capacitors

0204/0306 Land Grid Array





Land Grid Array (LGA) capacitors are the latest family of low inductance MLCCs from AVX. These new LGA products are the third low inductance family developed by AVX. The innovative LGA technology sets a new standard for low inductance MLCC performance.

Our initial 2 terminal versions of LGA technology deliver the performance of an 8 terminal IDC low inductance MLCC with a number of advantages including:

- · Simplified layout of 2 large solder pads compared to 8 small pads for IDCs
- Opportunity to reduce PCB or substrate contribution to system ESL by using multiple parallel vias in solder pads
- Advanced FCT manufacturing process used to create uniformly flat terminations on the capacitor that resist "tombstoning"
- · Better solder joint reliability

APPLICATIONS

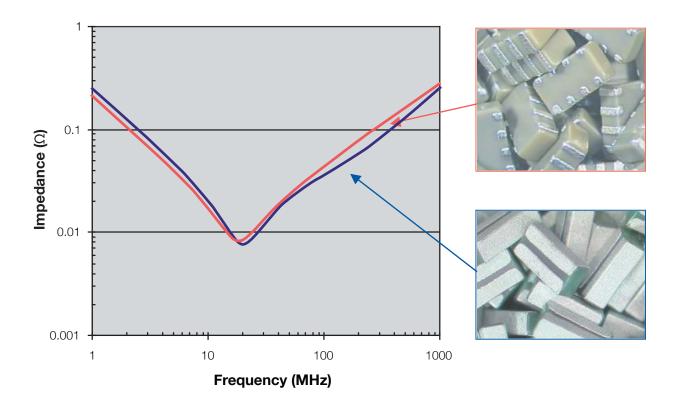
Semiconductor Packages

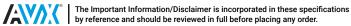
- · Microprocessors/CPUs
- · Graphics Processors/GPUs
- · Chipsets
- FPGAs
- ASICs

Board Level Device Decoupling

- · Frequencies of 300 MHz or more
- · ICs drawing 15W or more
- · Low voltages
- · High speed buses

0306 2 TERMINAL LGA COMPARISON WITH 0306 8 TERMINAL IDC

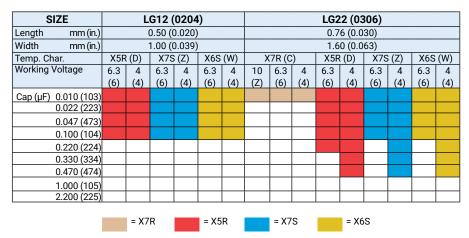




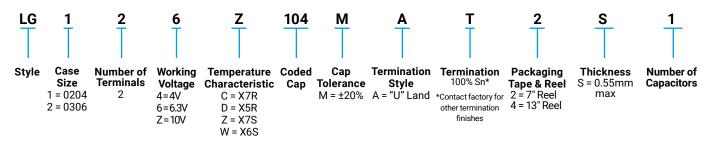
LGA Low Inductance Capacitors

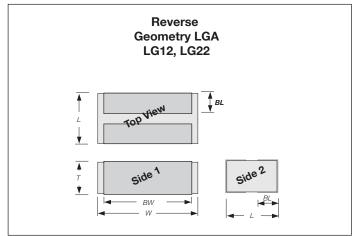






HOW TO ORDER





PART DIMENSIONS

MM (INCHES)

Series	L	W	Т	BW	BL
LG12 (0204)	0.5 ± 0.05	1.00 ± 0.10	0.50 ± 0.05	0.8 ± 0.10	0.13 ± 0.08
	(0.020±0.002)	(0.039 ± 0.004)	(0.020 ± 0.002)	(0.031 ± 0.004)	(0.005 ± 0.003)
LG22 (0306)	0.76 ± 0.10	1.60 ± 0.10	0.50 ± 0.05	1.50 ±0.10	0.28 ± 0.08
	(0.030 ± 0.004)	(0.063 ± 0.004)	(0.020 ± 0.002)	(0.059 ± 0.004)	(0.011 ± 0.003)



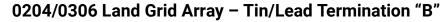
RECOMMENDED SOLDER PAD DIMENSIONS



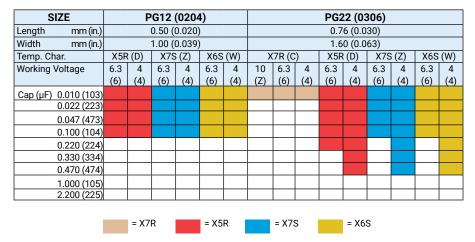
Series	PL	PW1	G
LG12 (0204)	0.50 (0.020)	1.00 (0.039)	0.20 (0.008)
LG22 (0306)	0.65 (0.026)	1.50 (0.059)	0.20 (0.008)

MM (INCHES)

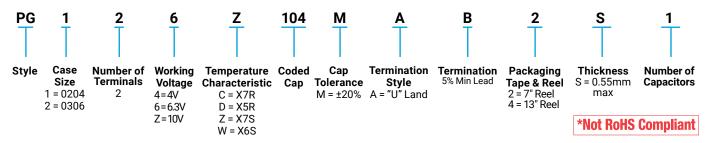
LGA Low Inductance Capacitors

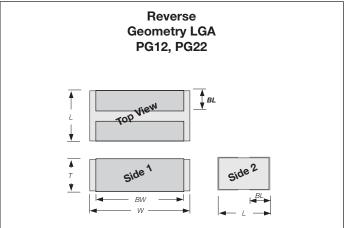






HOW TO ORDER





PART DIMENSIONS

MM (INCHES)

Series	L	W	Т	BW	BL
PG12 (0204)	0.5 ± 0.05 (0.020±0.002)	1.00 ± 0.10 (0.039 ± 0.004)	0.50 ± 0.05 (0.020 ± 0.002)	0.8 ± 0.10 (0.031 ± 0.004)	0.13 ± 0.08 (0.005 ± 0.003)
PG22 (0306)	0.76 ± 0.10 (0.030 ± 0.004)	1.60 ± 0.10 (0.063 ± 0.004)	0.50 ± 0.05 (0.020 ± 0.002)	1.50 ±0.10 (0.059 ± 0.004)	0.28 ± 0.08 (0.011 ± 0.003)

RECOMMENDED SOLDER PAD DIMENSIONS

MM (INCHES)



Series	PL	PW1	G
PG12 (0204)	0.50 (0.020)	1.00 (0.039)	0.20 (0.008)
PG22 (0306)	0.65 (0.026)	1.50 (0.059)	0.20 (0.008)

AT Series - 200°C & 250°C Rated





Present military specifications, as well as a majority of commercial applications, require a maximum operating temperature of 125°C. However, the emerging market for high temperature electronics demands capacitors operating reliably at temperatures beyond 125°C. AVX's high temperature chip capacitor product line, has been extended with the BME COG chip. All AT chips have verified capabilities of long term operation up to 250°C for applications in both military and commercial businesses. These capacitors demonstrate high volumetric efficiency, high insulation resistance and low ESR/ESL for the most demanding applications, such as "down-hole" oil exploration and aerospace programs.

HOW TO ORDER

AT10	3	Т	104	K	Α	Т	2	Α
	T	T		T	T	T	T	Τ
AVX	Voltage	Temperature	Capacitance Code	Capacitance	Test Level	Termination	Packaging	Special
Style	Code	Coefficient	(2 significant digits	Tolerance	A = Standard	1 = Pd/Ag	2 = 7" Reel	Code
AT03 = 0603	16V = Y	PME	+ no. of zeros)	$J = \pm 5\%$		T = 100% Sn Plated	4 = 13" Reel	A = Standard
AT05 = 0805	25V = 3	C0G 250°C = A	101 = 100pF	$K = \pm 10\%$		(RoHS Compliant)	9 = Bulk	
AT06 = 1206	50V = 5	COG 200°C = 2	102 = 1nF	$M = \pm 20\%$		7 = Ni/Au Plated		
AT10 = 1210		VHT 250°C = T	103 = 10nF			(For 250°C BME		
AT12 = 1812		VHT 200°C = 4	104 = 100nF			COG Only)		
AT14 = 2225		BME	105 = 1μF			• •		
		C0G 250°C = 5	·					
		COG 200°C = 3						

ELECTRICAL SPECIFICATIONS

Temperature Coefficient

PME COG 0±30ppm/°C, -55C to 250°C BME COG 0±30ppm/°C, -55C to 200°C

See TCC Plot for +250°C

VHT: T ±15%,-55°C to +150°C

See TCC Plot for +250°C

Capacitance Test (MIL-STD-202, Method 305) 25°C, 1.0 ± 0.2 Vrms (open circuit voltage) @ 1kHz

Dissipation factor 25°C

COG: 0.15% Max at 1.0 ± 0.2 Vrms (open circuit voltage) @ 1kHz VHT: 2.5% Max at 1.0 ± 0.2 Vrms (open circuit voltage) @ 1kHz

Insulation Resistance 25°C (MIL-STD-202, Method 302)

100GΩ or 1000MΩ- μ F (whichever is less)

Insulation Resistance 125°C (MIL-STD-202, Method 302)

 $10G\Omega$ or $100M\Omega$ -μF (whichever is less)

Insulation Resistance 200°C (MIL-STD-202, Method 302)

1GΩ or 10MΩ- μ F (whichever is less)

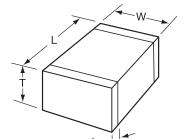
Insulation Resistance 250°C (MIL-STD-202, Method 302)

100MΩ or 1MΩ- μ F (whichever is less)

Direct Withstanding Voltage 25°C (Flash Test)

250% rated voltage for 5 seconds with 50mA max charging current

DIMENSIONS



MILLIMETERS (INCHES)

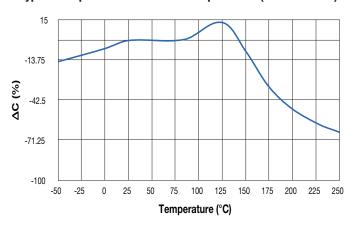
Size	AT03 = 0603	AT05= 0805	AT06=1206	AT10=1210	AT12=1812	AT14=2225
(L) Length	1.60 ± 0.15	2.01 ± 0.20	3.20 ± 0.20	3.20 ± 0.20	4.50 ± 0.30	5.72 ± 0.25
	(0.063 ± 0.006)	(0.079 ± 0.008)	(0.126 ± 0.008)	(0.126 ± 0.008)	(0.177 ± 0.012)	(0.225 ± 0.010)
(W) Width	0.81 ± 0.15	1.25 ± 0.20	1.60 ± 0.20	2.50 ± 0.20	3.20 ± 0.20	6.35 ± 0.25
	(0.032 ± 0.006)	(0.049 ± 0.008)	(0.063 ± 0.008)	(0.098 ± 0.008)	(0.126 ± 0.008)	(0.250 ± 0.010)
(T) Thickness Max.	1.02	1.30	1.52	1.70	2.54	2.54
	(0.040)	(0.051)	(0.060)	(0.067)	(0.100)	(0.100)
(t) min.	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)
terminal max.	0.75 (0.030)	0.75 (0.030)	0.75 (0.030)	0.75 (0.030)	1.02 (0.040)	1.02 (0.040)

AT Series - 200°C & 250°C Rated

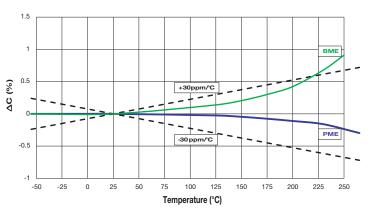


PERFORMANCE CHARACTERISTICS

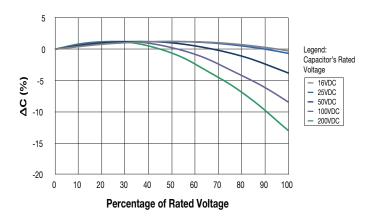
Typical Temperature Coefficient of Capacitance (VHT Dielectric)



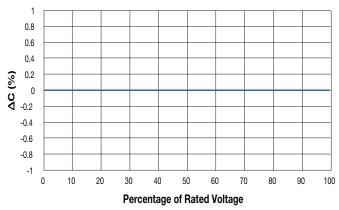
Typical Temperature Coefficient of Capacitance (COG Dielectric)



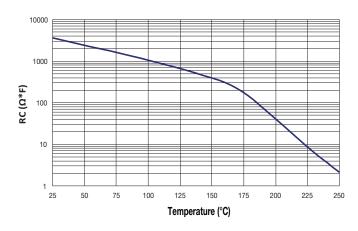
Typical Voltage Coefficient of Capacitance (VHT Dielectric)



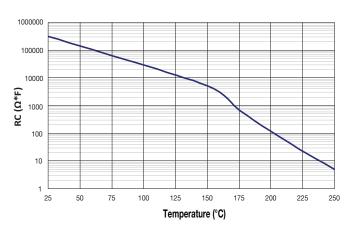
Typical Voltage Coefficient of Capacitance (COG Dielectric)



Typical RC vs Temperature (VHT Dielectric)



Typical RC vs Temperature (COG Dielectric)

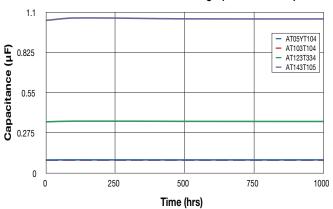


AT Series - 200°C & 250°C Rated



RELIABILITY

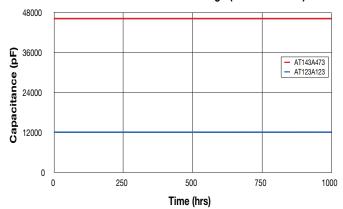




VHT - Failure Rate @ 90% Confidence Level (%/1000 hours)								
Temperature (°C)	50% Rated Voltage	100% Rated Voltage						
200	0.002	0.017						
250	0.026	0.210						

^{*}Typical 1210, 1812, 2225 Failure Rate Analysis based on 250°C testing and voltage ratings specified on the following page.

250°C Life Test @ 2x Rated Voltage (C0G Dielectric)

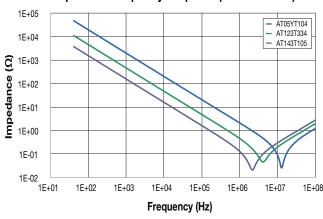


COG - Failure Rate @ 90% Confidence Level (%/1000 hours)								
Temperature (°C)	Temperature (°C) 50% Rated Voltage 100% Rated Voltage							
200	0.006	0.047						
250	0.074	0.590						

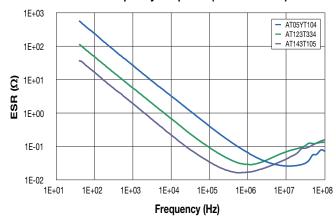
^{*}Typical 1812 and 2225 Failure Rate Analysis based on 250°C testing and voltage ratings specified on the following page.

FREQUENCY RESPONSE

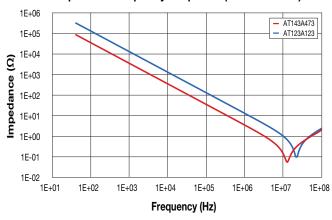
Impedance Frequency Response (VHT Dielectric)



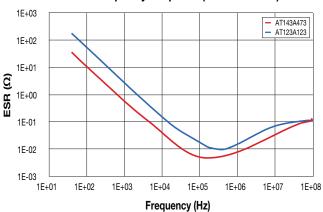
ESR Frequency Response (VHT Dielectric)



Impedance Frequency Response (COG Dielectric)



ESR Frequency Response (COG Dielectric)



The Important Information/Disclaimer is incorporated in these specifications by reference and should be reviewed in full before placing any order.

AT Series - 200°C & 250°C Rated



CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

			AT03 =	ATO		200°C)6 =	AT1	0 =	AT12 =	AT14 =	
(Case Size		0603	08			06		10	1812	2225	
-	Solderi	ina	Reflow/Wave				//Wave		v Only	Reflow Only	Reflow Only	
		mm	1.60±0.15	2.01 :	±0.20	3.20±0.20		3.20±	±0.20	4.50±0.30	5.72±0.25	
` ′		(in.)	(0.063±0.006)	(0.079			±0.008)	(0.126	0.008)	(0.177±0.012)	(0.225±0.010)	
(W)	Width	mm	0.81 ± 0.15		±0.20	_	±0.20	2.50:		3.20±0.20	6.35±0.25	
		(in.)	(0.032±0.006)	(0.049:			±0.008)	(0.098±		(0.126±0.008)	(0.250 ± 0.010)	
T(T)	Thickness		1.02	1.3			52	1.3		2.54	2.54	
(A) 7	F	(in.)	(0.040))51)		060)	(0.0		(0.100)	(0.100)	
(t) I	Terminal	min max	0.25 (0.010) 0.75 (0.030)	0.25(0.010) 0.030)	0.25(0		0.25(0.010)	0.25 (0.010) 1.02 (0.040)	
D۵	tod Tom		200	0.75(0000)		0.030)	0.75(0		1.02 (0.040) 200	200	
Rated Temp. (°C) Temp. Coefficeint			4	20			4	20		4	4	
Voltage (V)												
		~	25	25	50	25	50	25	50	50	50	
Cap pF)		102						<u> </u>				
. /	1200	122						<u> </u>				
	1500	152										
	1800	182						$ldsymbol{f eta}$				
	2200	222										
	2700	272				\Box						
	3300	332										
	3900	392										
	4700	472										
	5600	562										
	6800	682										
2	8200	822										
Cap (µF)	0.010	103										
μ.,	0.012	123										
	0.015	153										
	0.018	183										
	0.022	223										
	0.027	273										
	0.033	333										
	0.039	393										
	0.047	473										
	0.056	_										
	_	563										
	0.068	683										
	0.082	823										
	0.100	104										
	0.120	124										
	0.150	154										
	0.180	184										
	0.220	224										
	0.270	274										
	0.330	334										
	0.390	394										
	0.470	474										
	0.560	564										
	0.680	684										
	0.820	824										
		_						\vdash				
_	1.000	105	0.5	0.5	F0	0.5	F^	0.5	F^	F0	F0	
	Voltage		25	25	50	25	50	25	50	50	50	
Ra	ted Temp	p. (°C)	200		00		00	20		200	200	
(Case S	ize	AT03 = 0603)5 = 05)6 = :06	AT1	0 = 10	AT12 = 1812	AT14 = 2225	

	Case	Size	AT03 = 0603	AT(08)5 = 05		06 = 106	AT1		AT12 = 1812	AT14 = 2225
	Solde	rina	Reflow/Wave		/Wave		Reflow/Wave		v Only	Reflow Only	Reflow Only
(L) I	Length	mm	1.60±0.15	2.01:	±0.20	3.20	±0.20	3.20:		4.50±0.30	5.72±0.25
	-	(in.)	(0.063±0.006)	(0.079	£0.008)	(0.126:	±0.008)	(0.126±	£0.008)	(0.177±0.012)	(0.225±0.010
W)	Width	mm	0.81 ± 0.15		±0.20	1.60	±0.20	2.50:	£0.20	3.20±0.20	6.35±0.25
		(in.)	(0.032±0.006)	(0.049:			±0.008)	(0.098±		(0.126±0.008)	(0.250±0.010
T)1	Thickness	mm (in)	1.02	1.			52	1.		2.54	2.54
(4) T	Farmain al	(in.)	(0.040))51)		060)	(0.0		(0.100)	(0.100)
(1)	Terminal	min max	0.25(0.010) 0.75(0.030)	0.25(0.010) 0.030)	0.25(0		0.25 (0.010) 1.02 (0.040)	0.25 (0.010) 1.02 (0.040)
_	Rated Ter		250	2.			50	-	50	250	250
	Temp. Co		T	-		_	<u>оо</u> Т			T	T
_											
Сар	Voltag		16	16	25	16	25	16	25	25	25
pF)	1000	102						_			
. /	1200	122						\vdash			
	1500	152						lacksquare			
	1800	182						igspace			
	2200	222									
	2700	272									
	3300	332									
	3900	392									
	4700	472									
	5600	562									
	6800	682									
`	8200	822									
Cap (µF)	0.010	103									
۲۰ /	0.012	123									
	0.015	153									
	0.018	183									
	0.022	223									
	0.027	273									
	0.033	333									
	0.039	393									
	0.047	473									
	0.056	563									
	0.068	683									
	0.000	823									
	0.100	104									
	0.120	124									
	0.150	154									
	0.180	184									
	0.220	224									
	0.270	274									
	0.330	334									
	0.390	394									
	0.470	474									
	0.560	564									
	0.680	684									
	0.820	824									
	1.000	105									
۲	Voltag		16	16	25	16	25	16	25	25	25
-	Rated Ter		250		50	_	50		50	250	250
_'	nateu rei	np. (*C)	AT03 =)5 =		06 =		0 =	AT12 =	AT14 =
	Case	Size	0603)5 = 05		.06 .06	I	10 = 10	1812 =	2225

Voltage rating per table. Capacitance values specified at 25°C, derate capacitance value based on TCC and VCC Plots on page 107. NOTE: Contact factory for non-specified capacitance values.

AT Series - 200°C & 250°C Rated



CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

BME COG Temp. Coefficient: 4 200°C Rated										
Ca	se Siz	e	AT03=	0603	1	AT05=	:0805	AT06	=1206	
So	olderin	g	Reflow	Wave	Reflow/Wave			Reflow/Wave		
L) Le		mm	1.60±	0.15	2.01±0.20			3.20±0.20		
		(in.)	(0.063±		(0.079±			±0.008)	
W) W		mm (in.)	0.81±0.15 (0.032±0.006)			1.25± 0.049±			±0.20 ±0.008)	
T) Thi		mm	(0.032±0.006) 1.02			1.3			.52	
,		(in.)	(0.04			(0.0			060)	
t) Ter		min	0.25(0	.010)		0.25(0	.010)		(0.010)	
	max		0.75(0			0.75(0			(0.030)	
	d Temp.	(°C)	20	0		20	10	2	.00	
	Temp. oefficein	.	3			3			3	
	Itage (\		25	50	25	П	50	25	50	
ар		390								
oF)	47	470								
r	_	560								
r	$\overline{}$	680								
F		820								
F	$\overline{}$	101								
F	$\overline{}$	121								
F	\rightarrow	151								
F	$\overline{}$	181								
F		221								
F	$\overline{}$	271				_				
H	$\overline{}$	331								
F		391								
H		471								
H		561								
H	-	681								
H	-	821								
H		102								
-		122								
-		152								
	$\overline{}$	182								
-	_	222								
-		272								
-	$\overline{}$	332								
- ⊢		392								
-	$\overline{}$	472								
-	_	562								
-	$\overline{}$	682								
-	-	822								
ар	$\overline{}$	103								
ıF)		123								
	_	153								
	\rightarrow	183								
-	$\overline{}$	223								
-	\rightarrow	273				T				
T	0.033	333								
T	0.039	393	j							
-	\rightarrow	473								
-		563								
-	\rightarrow	683				T				
-		823								
-	0.100	\rightarrow								
		\rightarrow	25	50	25	i	50	25	50	
Vo	ltage (\	'/ '				30	200 200			

Caca	Sizo	AT03=0603	AT05=0805	AT06 = 1206			
Case Size Soldering							
Solde	ring	Reflow/Wave	Reflow/Wave	Reflow/Wave			
L) Length		1.60±0.15	2.01 ± 0.20	3.20±0.20			
	(in.)	(0.063±0.006)	(0.079±0.008)	(0.126±0.008)			
W) Width		0.81 ± 0.15	1.25±0.20	1.60±0.20			
	(in.)	(0.032±0.006)	(0.049±0.008)	(0.063±0.008)			
T)	mm	1.02	1.30	1.52			
Thickness	(in.)	(0.040)	(0.051)	(0.060)			
(t)	min	0.25 (0.010)	0.25(0.010)	0.25 (0.010)			
	max	0.75 (0.030)	0.75(0.030)	0.75 (0.030)			
Rated Ten	ıp. (°C)	250	250	250			
Tem		5	5	5			
Coeffic		J	3				
Voltage	(V)	25	25	25			
Cap 39	390						
pF) 47	470						
	_						
56	560						
68	680						
82	820						
100	101						
120	121						
<u> </u>	-						
150	151						
180	181						
220	221						
270	271						
330	331						
-							
390	391			+			
470	471						
560	561						
680	681						
820	821						
-	-						
1000	102						
1200	122						
1500	152						
1800	182						
2200	222		1				
2700	272						
_	\vdash			-			
3300	332						
3900	392						
4700	472						
5600	562						
6800	682			1			
8200	822			+			
	-			+			
O.010	103						
0.012	123						
0.015	153						
	183						
0.022	223			+			
-	\vdash		+	+			
0.027	-						
0.033	333						
0.039	393						
0.047	473						
0.056	\vdash						
-	\vdash			+			
	683			+			
	823						
	104						
Voltage	(V)	25	25	25			
Rated Ten	1p. (°C)	250	250	250			
Case Size		AT03=0603	AT05=0805	AT06=1206			

Voltage rating per table. Capacitance values specified at 25°C, derate capacitance value based on TCC and VCC Plots on page 107. NOTE: Contact factory for non-specified capacitance values.

AT06=1206



AT05=0805

Case Size

AT03=0603

AT Series - 200°C & 250°C Rated



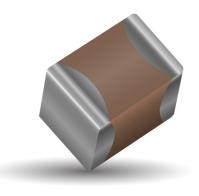
CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

PI	ME	CO	G Temp.	Coefficient: 2	200°C Ra	ted		PI	ME	CO	G Temp.	Coefficient: A	250°C Ra	ted	
C	ase Si	ze	AT05 = 0805	AT06 = 1206	AT10 = 1210	AT12 = 1812	AT14 = 2225	C	ase S	ize	AT05 = 0805	AT06 = 1206	AT10 = 1210	AT12 = 1812	AT14 = 2225
S	Solderi	na	Reflow/Wave	Reflow/Wave	Reflow Only	Reflow Only	Reflow Only	S	Solderi	na	Reflow/Wave	Reflow/Wave	Reflow Only	Reflow Only	Reflow Only
		mm	2.01 ± 0.20	3.20±0.20	3.20±0.20	4.50±0.30	2.75±0.25			mm	2.01 ± 0.20	3.20±0.20	3.20±0.20	4.50±0.30	2.75±0.25
(L) L	ength	(in.)	(0.079±0.008)	(0.126±0.008)	(0.126±0.008)	(0.177±0.012)	(0.225±0.010)	(L) L	ength	(in.)	(0.079±0.008)	(0.126±0.008)	(0.126±0.008)	(0.177±0.012)	(0.225±0.010)
(w) v	Width	mm	1.25±0.20	1.60±0.20	2.50±0.20	3.20±0.20	6.35±0.25	(W) \	Width	mm	1.25±0.20	1.60±0.20	2.50±0.20	3.20±0.20	6.35±0.25
(,		(in.) mm	(0.049±0.008) 1.30	(0.063±0.008) 1.52	(0.098±0.008) 1.70	(0.126±0.008) 2.54	(0.250±0.010) 2.54	(,		(in.) mm	(0.049±0.008) 1.30	(0.063±0.008) 1.52	(0.098±0.008) 1.70	(0.126±0.008) 2.54	(0.250±0.010) 2.54
(T)Th	hickness	(in.)	(0.051)	(0.060)	(0.067)	(0.100)	(0.100)	(T) Th	hickness	(in.)	(0.051)	(0.060)	(0.067)	(0.100)	(0.100)
(+) T.	erminal	min	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	(+) T.	erminal	min	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)	0.25 (0.010)
٠,		max	0.75 (0.030)	0.75 (0.030)	0.75 (0.030)	1.02 (0.040)	1.02 (0.040)	. ,		max	0.75 (0.030)	0.75 (0.030)	0.75 (0.030)	1.02 (0.040)	1.02 (0.040)
	ted Temp		200	200	200	200	200		ted Temp		250	250	250	250	250
	mp. Coeff		2	2	2	2	2		mp. Coef		Α	A	A	A	Α
<u> </u>	Voltage (50	50	50	50	50	\	Voltage		25	25	25	25	25
l	100								100	101					ļ
	120								120	121					
	150	151							150	151					
	180	181							180	181					
	220	221							220	221					
	270	271							270	271					
	330	331							330	331					
	390	391							390	391					
	470	471							470	471					
	560	561							560	561					
١.	680	681							680	681					
Cap (pF)	820	821						Cap (pF)	820	821					
(pr-)	1000	102						(pr)	1000	102					
	1200	122							1200	122					
	1500	152							1500	152					
	1800	182							1800						
1	2200	222							2200	222					
	2700	272							2700	272					
	3300								3300						
	3900	392							3900	392					
	4700 5600	472							4700						
l	6800								5600						
		682							6800			l	l		
\vdash	8200				 				8200		-				
l	0.010								0.010			-	-		
1	0.012				 				0.012		1				
1	0.015			-	 				0.015						
1	0.018				 				0.018		1				
1	0.022				 	1			0.022	273	1				
Cap	0.027	333			 			Cap	0.027	333		 	 		
(μF)	0.033				 	 		(µF)	0.039		1				
	0.039				 				0.039						
l	0.047				 				0.047			 	 		
l	0.056				 	 			0.056						
l	0.000				 	1			0.082		1				
l	0.062				 				0.100						
1/	oltage		50	50	50	50	50	W	oltage		25	25	25	25	25
			200	200	200	200	200				250	250	250	250	250
	ed Temp								ed Tem						
_ C	ase Si	ze	A105 = 0805	AT06 = 1206	<u> Al 10 = 1210 </u>	A112 = 1812	A114 = 2225	C	ase S	ıze	A105 = 0805	AT06 = 1206	A110 = 1210	A112 = 1812	Ai 14 = 2225

Voltage rating per table. Capacitance values specified at 25°C, derate capacitance value based on TCC and VCC Plots on page 107. NOTE: Contact factory for non-specified capacitance values.

For 600V to 5000V Applications





High value, low leakage and small size are difficult parameters to obtain in capacitors for high voltage systems. AVX special high voltage MLC chip capacitors meet these performance characteristics and are designed for applications such as snubbers in high frequency power converters, resonators in SMPS, and high voltage coupling/dc blocking. These high voltage chip designs exhibit low ESRs at high frequencies.

Larger physical sizes than normally encountered chips are used to make high voltage MLC chip products. Special precautions must be taken in applying these chips in surface mount assemblies. The temperature gradient during heating or cooling cycles should not exceed 4°C per second. The preheat temperature must be within 50°C of the peak temperature reached by the ceramic bodies through the soldering process. Chip sizes 1210 and larger should be reflow soldered only. Capacitors may require protective surface coating to prevent external arcing.

For 1825, 2225 and 3640 sizes, AVX offers leaded version in either thru-hole or SMT configurations (for details see section on high voltage leaded MLC chips)

NEW 630V RANGE

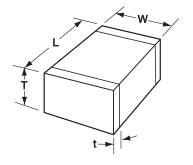
HOW TO ORDER

1808	A 	A 	<u>271</u>	<u>M</u>	<u>A</u>	1 	<u>2</u>	<u>A</u>
AVX Style 0805 1206 1210 1808 1812 1825 2220 2225 3640	Voltage 600V/630V = C 1000V = A 1500V = S 2000V = G 2500V = W 3000V = H 4000V = J 5000V = K	Temperature Coefficient NPO (COG) = A X7R = C	Capacitance Code (2 significant digits + no. of zeros) Examples: 10 pF = 100 100 pF = 101 1,000 pF = 102 22,000 pF = 223 220,000 pF = 224 1 µF =105	Capacitance Tolerance C0G: J = ±5% K = ±10% M = ±20% X7R: K = ±10% M = ±20% Z = +80%, -20%		Termination ² 1 = Pd/Ag T = Plated Ni and Sn (RoHS Compli	1 or 2 = 7" Reel** 3 or 4 = 13" Reel	Special Code A = Standard

*Note: Terminations with 5% minimum lead (Pb) is available, see pages 100 and 101 for LD style. Leaded terminations are available, see pages 102-106.

Notes: Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Contact plant for recommendations. Contact factory for availability of Termination and Tolerance options for Specific Part Numbers.

^{***} AVX offers nonstandard chip sizes. Contact factory for details.





DIMENSIONS

MILLIMETERS (INCHES)

SIZE	0805	1206	1210*	1808*	1812*	1825*	2220*	2225*	3640*
(L) Length	2.10 ± 0.20	3.30 ± 0.30	3.30 ± 0.40	4.60 ± 0.50	4.60 ± 0.50	4.60 ± 0.50	5.70 ± 0.50	5.72 ± 0.25	9.14 ± 0.25
	(0.083 ± 0.008)	(0.130 ± 0.012)	(0.130 ± 0.016)	(0.181 ± 0.020)	(0.181 ± 0.020)	(0.181 ± 0.020)	(0.224 ± 0.020)	(0.225 ± 0.010)	(0.360 ± 0.010)
(W) Width	1.25 ± 0.20	1.60 ± 0.20	2.50 ± 0.30	2.00 ± 0.20	3.20 ± 0.30	6.30 ± 0.40	5.00 ± 0.40	6.35 ± 0.25	10.2 ± 0.25
	(0.049 ±0.008)	(0.063 ± 0.008)	(0.098 ± 0.012)	(0.079 ± 0.008)	(0.126 ± 0.012)	(0.248 ± 0.016)	(0.197 ± 0.016)	(0.250 ± 0.010)	(0.400 ± 0.010)
(T) Thickness	1.35	1.80	2.80	2.20	2.80	3.40	3.40	2.54	2.54
Max.	(0.053)	(0.071)	(0.110)	(0.087)	(0.110)	(0.134)	(0.134)	(0.100)	(0.100)
(t) terminal min.	0.50 ± 0.20	0.60 ± 0.20	0.75 ± 0.35	0.75 ± 0.35	0.75 ± 0.35	0.75 ± 0.35	0.85 ± 0.35	0.85 ± 0.35	0.76 (0.030)
max.	(0.020 ± 0.008)	(0.024 ± 0.008)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.033 ± 0.014)	(0.033 ± 0.014)	1.52 (0.060)

^{*}Reflow Soldering Only

^{**}The 3640 Style is not available on 7" Reels.

For 600V to 5000V Applications



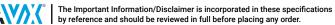
NPO (COG) DIELECTRIC - PERFORMANCE CHARACTERISTICS

Capacitance Range	10 pF to 0.100 μF (25°C, 1.0 ±0.2 Vrms at 1kHz, for ≤ 1000 pF use 1 MHz)
Capacitance Tolerances	±5%, ±10%, ±20%
Dissipation Factor	0.1% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz, for ≤ 1000 pF use 1 MHz)
Operating Temperature Range	-55°C to +125°C
Temperature Characteristic	0 ±30 ppm/°C (0 VDC)
Voltage Ratings	600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)
Insulation Resistance (+25°C, at 500 VDC)	100K MΩ min. or 1000 MΩ - μF min., whichever is less
Insulation Resistance (+125°C, at 500 VDC)	10K MΩ min. or 100 MΩ - μF min., whichever is less
Dielectric Strength	Minimum 120% rated voltage for 5 seconds at 50 mA max, current

NPO (COG) CAPACITANCE RANGE - PREFERRED SIZES ARE SHADED

Case	Size	1	0805	5			1206					12	210						18	808							18	12			
Solde		Ref	low/V				low/W	ave				Reflo		у					Reflo		у						Reflov		,		
(L) Length	mm	2.	10 ± 0	.20			30 + 0.				,,		+ 0.40						4.60	+ 0.50)						4.60 +		٠,		
W) Width	(in.) mm		85 ± 0 25 ± 0				30 + 0. +0.30/				((2.50	+ 0.01 + 0.30					((0.181 · 2.00 ·	+ 0.02 + 0.20	(0)					(().177 + 3.20 +		2)		
W) Width	(in.)		49 ± 0		(0).063 +					((0.098							2.00).079 ·							(0	0.126 +		3)		
(T) Thickness	шщ		1.35				1.80						80						2.	20							2.8	30			
(t) Terminal	(in.) mm		(0.053 50 + 0		-		(<u>0.071</u> 50 + 0.						110) + 0.35)87) + 0.35							0.75				
(t) Terrimai	(in.)		20 + C				4 + 0.						0.014							0.014							(.030 (
Voltage	e (V)	600	630	1000	600	630	1000	1500	2000	600	630	1000	1500	2000	3000	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000
Cap (pF)	.5 OR5		Α	C			-																								
	1.0 1R0 1.2 1R2	\vdash	A	C		-		-	-		-			-	-	_	-		-			-	-			-		_			
	1.5 1R5	Α	A	C	Х	Х	Χ	Χ	Х																						
	1.8 1R8	Α	Α	С	Х	Х	Х	Х	Х																						
	2.2 2R2		Α	C	X	X	X	X	X								С	C		С		С	С								
	2.7 2R7 3.3 3R3	A	A	C	X	X	X	X	X						-		C	C	C	C	C	C	C								
	3.9 3R9		A	C	X	X	X	X	Ŷ						 		C	C	C	C	C	C	C								
	4.7 4R7	Α	A	C	X	X	X	X	X								C	C	C	C	C	C	C								
	5.6 5R6	Α	Α	С	Χ	Х	Χ	Χ	Х								С	С	С	С	С	С	С								
	6.8 6R8	Α	A	C	X	X	X	X	X			_	_	-	-		C	C	С	C	C	C	C		_	-	_	_	<u> </u>		
	8.2 8R2 10 100	A	A	C	X	X	X	X	X		M	М	D	М	F	С	C	C	C	C	C	C	C	С	С	С	_		С		F
	12 120		A	C	X	X	X	X	X	C	M	M	D	M	F	C	C	C	C	C	C	C	C	C	C	C	C	C	C	C	E
	15 150		A	C	X	X	X	X	X	C	М	М	D	М	F	C	C	C	C	C	C	C	C	C	C	C	C	C	C	C	Ē
	18 180		Α	С	X	X	X	Х	X	С	М	М	D	М	F	С	С	С	С	С	С	С	С	С	С	С	С	С	С	С	E
	22 220 27 270		A	C	X	X	X	X	X	C	M	M	D D	M	F	C	C	C	C	C	C	C	E F	С	C	C	C	C F	C	C C	E
	33 330		A	C	X	X	X	D	M	C	M	M	D	M	F	С	C	C	C	C	C	C	F	C	C	C	C	F	C	С	F
	39 390		A	Č	X	X	X	D	M	Č	M	M	D	M	F	C	C	Č	Č	C	Č	Č	F	Č	Č	Č	Č	F	Č	C	Ē
	47 470	Α	Α	С	Х	Х	С	D	М	С	М	М	D	М	F	С	С	С	С	С	С	С	С	С	С	С	С	F	С	С	Е
	56 560		Α	C	X	X	С	С	C	С	М	М	С	C	F	С	C	C	С	C	C	С		С	С	C	C	F	С	С	F
	68 680 82 820		X	C	X	X	C	C	C	C	M	M	C	C	F	СС	C	C	C	C	C	C		C	C	C	C	F	C	C C	<u> </u>
	100 101		X	X	X	X	C	C	C	C	M	C	C	C	F	C	C	C	C	C	F	F		C	C	C	C	F	C	С	F
	120 121		Ĉ	Ĉ	X	X	Č	Ē	Ē	Č	M	Č	Č	Č	F	Č	Č	Č	Č	Č	F	F		C	Č	Č	Č	F	C	C	G
	150 151		С	С	Χ	Х	С	Е	Е	С	М	С	Е	Е	F	С	С	С	F	F	F	F		С	С	С	С	F	С	С	G
	180 181		C	C	X	X	E	E	E	С	M	E	E	E	I F	С	C	C	F	F	F	F		С	С	C	C	F	F	F	
	220 221 270 271		C	C	C	X M	E	E	E	C	M	E	E	E	G	C	C F	C	F	F	F	F		C	C	C	C	F	F	F	
	330 331		C	C	C	M	Ē	Ē	È	C	M	Ē	Ē	È	-	C	F	F	Ė	F	Ė	F		C	C	C	F	F	F	F	
	390 391	С	С	С	С	М	Е	Е	Е	С	М	Е	Е	Е		С	F	F	F	F	F	F		С	С	С	F	F	F	F	
	470 471		C		C	M	E	Е	Е	C	М	Ē	Ē	Ę		С	F	F	F	F	F	F		С	C	F	F	F	F	F	
	560 561 680 681		C		C	C	E			C	M	E	E F	E		C	F	F	F	F		F		C	C	F	F	F	F G	G	
	750 751		Č		E	E	È			C	M	Ė	G	늍		C	F	F	F	F				C	C	F	F	F	G	G	
	820 821		С		Е	Е	Е			С	М	Е	G	Е		С	F	F	Е	F				С	С	F	F	F	G	G	
	1000 102		С		Е	E	Е			С	С	Е	F	F		С	F	F	Е	F				С	С	F	F	F	G	G	
	1200 122 1500 152		C		E	E	Е		-	C	C	E F		F G		C E	F	F	Е	F				C	C	F	E F	E F			
	1800 182		C		E	E				C	C	G		G		E	F	F		F				C	C	F	G	F			
	2200 222		Č		Ē	Ē				Ē	Č	G				Ē	F	F						С	Č	Ė	Ğ	G			
	2700 272				E	E				E	С	G				Е	F	F						С	С	E	G	G			
	3300 332 3900 392	-		-	Е	E	-	<u> </u>	-	E F	C	G		-	-	E	F	F	<u> </u>		-		<u> </u>	C	C	F		G			
	4700 472	\vdash	\vdash	\vdash	\vdash	F		\vdash	\vdash	F	C	G		\vdash	\vdash	F	F		\vdash	\vdash	\vdash		\vdash	C	C	G			\vdash		
	5600 562					E				E	E					E	F							C	C	G					
	6800 682										E					F	F							С	С						
Cap (uE)	8200 822	\vdash	\vdash	\vdash	\vdash	-	-	<u> </u>	-	-	F		-	-	-	-	F	-	\vdash	<u> </u>	-	-	\vdash	E	C	-	-	<u> </u>	<u> </u>		
Cap (µF)	0.010 103 0.012 123		 	<u> </u>	+	1	+	\vdash	 		G		 	 	 				\vdash	 			\vdash	F	F				 		
	0.012 123																							G	G						
	0.018 183																							G	G						
	0.022 223			_	_	_				_					_									oxdot	F						
	0.027 273 0.033 333		-	1	1	1	1	<u> </u>	-		-	-	-	-	1	-		-	-		-		-	\vdash	G G		-				
	0.033 333						†																		J						
	0.056 563																														
	0.068 683																														
Volt	0.100 104	600	620	1000	600	620	1000	1500	2002	600	620	1000	1500	2000	3000	600	620	1000	1500	2002	2502	2000	4000	600	620	1000	1500	2000	2502	2000	4000
Voltage Case S	Size	000	0805		000		1206	1300	12000	000	030		11500 210	12000	13000	000	030	1000		12000 108	12300	13000	-+ 000	000	030	1000	18		12300	3000	4000

Letter	Α	С	E	F	G	Χ	7
Max.	0.813	1.448	1.8034	2.2098	2.794	0.940	3.30
Thickness	(0.032)	(0.057)	(0.071)	(0.087)	(0.110)	(0.037)	(0.130)







NP0 (C0G) CAPACITANCE RANGE - PREFERRED SIZES ARE SHADED

Soldering																						2225	,								3640	,			
1 mm	-			Reflov									ow C									low (flow (
(L) Length (in.)				4.60 : 0.181 :									70 0.5 24 0.0									70 ± 0. 25 ± 0.									14 ± 0 60 ± 0				
W) Width mm				6.30 : 0.248 :	± 0.40							5.0	00 0.4 97 0.0	-0							6	.30 0.4 50 ± 0.	40							1	0.2 ± 0 100 ± 0	.25			
(in.)				3.	40	0)							3.40	110)								3.40								(0.4	2.54				
Thickness (in.)				0.75	± 0.35				 			0.	0.134) 85 0.3	5							0.8	(0.100 35 ± 0.	.35				1			0.	(0.100 76 (0.0	30)			=
(t) Terminal mm max Voltage (V)	600	630	((0.030	± 0.014	4)	Isono	14000	600	620	1000	(0.03	3 ± 0.	014)	Isono	4000	Ennn	600	1 620	1000	(0.03	33 ± 0.	.014)	Isono	14000	Ennn	600	620	I 1000	1.	52 (0.0	160)	2000	4000	E000
Cap(pF) 1.5 1R5	000	030	1000	1300	2000	2300	3000	4000	000	030	1000	1300	2000	2300	3000	4000	3000	000	030	1000	1300	2000	2300	3000	4000	3000	000	0.00	1000	1300	2000	2500	3000	4000	3000
1.8 1R8																																			
22 2R2 27 2R7	\vdash	1	-	-	-		-															Н									-			\vdash	$\vdash\vdash$
3.3 3R3																																			
3.9 3R9 4.7 4R7		-	-	-	-	-	-	-	\vdash						_				_			Н		_	_		-				_	_		\vdash	$\vdash\vdash\vdash$
5.6 5R6																																			H
6.8 6R8																																			
82 8R2 10 100	F	F	G	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F	F					-			\vdash	$\vdash\vdash\vdash$
12 120	Е	E	G	E	F	E	F	F	E	E	E	E	E	Ē	E	E	E	E	E	E	E	E	E	E	F	F									
15 150 18 180		E	G	Е	F	E	F	F	п	п	E F	п	E F	E	E F	E F	E	E	E	E	Е	п	E F	E	F	F									\Box
22 220		E	G	E	F	E	F	F	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F	F				\vdash		\vdash		\vdash	H
27 270	Е	Е	G	Е	F	Е	F	F	Е	E	Е	E	E	Е	E	Е	Е	E	Е	Е	Е	Е	Е	Е	F	F									
33 330 39 390		E	G G	E	F	E	F	F	E	E	E	E	E	E	E	E	E	E	E F	E	E	E	E	E	F	F								\vdash	$\vdash\vdash$
47 470		E	G	E	F	E	F	F	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F	G									G
56 560		Е	G	Е	F	Е	F	F	Е	Е	E	Е	E	Е	Е	E	E	Е	E	E	Е	Е	Е	Е	F	G									G
68 680 82 820		E	G	E	F	E	F	F	E	E F	E	E	E	E	E	E	E F	E F	E F	E F	E	E	E F	E	F	G G								\vdash	G G
100 101		E	G	E	F	E	F	F	E	E	E	E	E	E	E	E	E	E	E	E	Е	E	E	Е	G	G				G	G	G	G	G	G
120 121 150 151	E	E	G	E	F	E	F	F	E	E	E	E	E	E	E	E	E	E	E	E F	E	E	E	E	G	G				G	G	G	G	G	G
180 181	E	E	G	E	F	E	F	F	E	E	E	E	E	E	E	F	F	E	E	E	E	E	E	E	G	G G				G	G	G	G G	G G	G
220 221		Е	G	Е	F	Е	F	F	Ε	Е	Е	Е	Е	Е	Е	F	F	Е	Е	Ε	Е	Е	Е	Е	G	G				G	G	G	G	G	G
270 271 330 331	E	E	G	E	F	E	F	F	E	п	E F	E	E	E	E			E	E	E F	E F	E	E	E	G	G				G	G	G	G G	G G	G
390 391	E	E	G	E	F	E	F		E	E	E	E	E	E	E			E	E	E	E	E	E	E	G					G	G	G	G	G	G
470 471	Е	Е	G	Е	F	Е	F		Е	Е	E	Е	E	Е	Е			Е	Е	E	Е	E	Е	Е	G					G	G	G	G	G	G
560 561 680 681	E	E	G	E	F	E F	G		E	E	E	E	E	E F	E			E	E	E	E	E	E	E	G					G	G	G	G G	G G	G G
750 751	Е	Е	G	Е	F	F	G		Е	Е	Е	Е	Е	F	F			Е	Е	Е	Е	Е	Е	Е						G	G	G	G	G	G
820 821 1000 102		E	G	E	F	F	G		E	E	E	E	E	F	F			E	E	E	E	E	F E	E			G	G	G	G	G	G	G	G G	G
1200 122		E	G	E	F	G	G		E	E	E	E	E	G	G			E	E	E	E	E	F	F			G	G	G	G	G	G	G	G	\Box
1500 152		Е	G	F	G	G	G		Е	E	E	F	F	G	G			Е	E	Е	Е	E	F	F			G	G	G	G	G	G	G		
1800 182 2200 222		E	G	F G	G	G	G		E	E	E	F G	F G	G	G			E	E F	E	E	E	G	G			G	G	G	G	G	G	G G		$\vdash\vdash$
2700 272	Е	Е	G	G	G		G		Е	Е	Е	G	G					Е	Е	Е	F	F					G	G	G	G	G	G	G		
3300 332		E	G	G	G G		-	-	E F	E F	E	G	G G					E F	E	E F	F	F G					G	G	G	G	G	G		\vdash	$\vdash\vdash$
3900 392 4700 472		E	G	G	G				E	E	E	G	G					F	F	F	G	G					G	G	G	G	G	G			H
5600 562		F	G	G	G				F	F	F	G	G					F	F	F	G	G					G	G	G	G	G				
6800 682 8200 822		F G	G		G G		-		F G	F G	F G							F G	F G	F G	G	G					G G	G G	G	G	G			\vdash	$\vdash\vdash\vdash$
Cap (µF) 0.010 103		E	G						7	7	7							G	G	G		П					G	G	G	G					М
0.012 123		Е	G															G	G								G	G	G						
0.015 153	+	Е																G	G								G	G	G						
0.018 183		Е																G	G								G	G	G						ш
0.022 223	-	E				-		<u> </u>										G	G	_	_	Н	_				G	G	G	_				$\vdash\vdash$	$\vdash \vdash$
0.027 273	\vdash	F		-	-	-	-	\vdash	\vdash			\vdash		-				G	G	-		Н					G	G	\vdash	\vdash	-			$\vdash\vdash$	$\vdash\vdash$
0.039 393	\vdash	G		\vdash	\vdash	\vdash	\vdash	\vdash	\vdash								\vdash	G	G	\vdash		\vdash		\vdash			U	0		\vdash	\vdash			\vdash	$\vdash\vdash$
0.047 473	-	G				\vdash		\vdash										G	G								G	G							
0.056 563		G																G	G			П													
0.068 683	L	G																G	G																
0.100 104																																			
Voltage (V)	600	630	1000			2500	3000	4000	600	630	1000			2500	3000	4000	5000	600	630	1000				3000	4000	5000	600	630	1000	1500			3000	4000	5000
Case Size				18	25								2220									2225				fied c					3640)			

Letter	A	С	E	F	G	Χ	7
Max.	0.813	1.448	1.8034	2.2098	2.794	0.940	3.30
Thickness	(0.032)	(0.057)	(0.071)	(0.087)	(0.110)	(0.037)	(0.130)

For 600V to 5000V Applications



X7R Dielectric

Performance Characteristics

Capacitance Range	10 pF to 0.82 μF (25°C, 1.0 ±0.2 Vrms at 1kHz)
Capacitance Tolerances	±10%; ±20%; +80%, -20%
Dissipation Factor	2.5% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)
Operating Temperature Range	-55°C to +125°C
Temperature Characteristic	±15% (0 VDC)
Voltage Ratings	600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)
Insulation Resistance (+25°C, at 500 VDC)	100K MΩ min. or 1000 MΩ - μF min., whichever is less
Insulation Resistance (+125°C, at 500 VDC)	10K MΩ min. or 100 MΩ - μF min., whichever is less
Dielectric Strength	Minimum 120% rated voltage for 5 seconds at 50 mA max. current

X7R CAPACITANCE RANGE - PREFERRED SIZES ARE SHADED

Case Size			0805				1206					1210						18	308							18	12			
Soldering	+		ow/W	ave			low/W	lave				flow O	nly						w Only	,						Reflo				
(L) Length mr		(0.08	10 0.2 35 ± 0.0	0 008)		3. (0.1	30 ± 0. 30 ± 0.	30 012)			(0.1	.30 0.4 30 0.0	0 16)					4.60 (0.181	± 0.50 ± 0.020	1)						4.60 (0.177	± 0.50 ± 0.012	!)		
W) Width mr (in.	m	1.2	25 ± 0.2 19 ± 0.0	20	(+0.30/	-0.10)		2	.50 0.3 98 0.0	0					2.00	0.20 ± 0.008	0						3.20 (0.126	± 0.30	0		
(T) Thickness mr	m		1.35	550)	<u> </u>		1.80		,			2.80	12)					2.	.20	,						2.	80	,		
` (III.		0.5	0.053) 50 ± 0.2	20		0.	(0.071) 60 ± 0.	20			0	(0.110) .75 0.3	5					0.75	087) ± 0.35							0.75	± 0.35			
(t) Terminal ma	ax	(0.02	630 630	008)	600	(0.0	24 ± 0.	008)	2000	600	(0.0	30 ± 0.0 1000	014)	2000	600	620		(0.030)	± 0.014		2000	4000	600	I 620		1500	± 0.014		2000	14000
	101	X	X	C	C	C	E	E	E	E	E	E	E	E	000	030	1000	1300	2000	2300	3000	4000	000	030	1000	1300	2000	2300	3000	4000
120	121	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е																
	151	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е																
	181	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е																
	221 271	Х	X	С	С	С	E	E	E	E	E	E	E	E						-	-		_	-	-	_	_			<u> </u>
-	331	X	X	C	C	C	E	E	E	E	E E	E	E	E	Е	Е	Е	Е	Е	Е	Е		E	E	E	E	E			-
	391	X	X	С	С	С	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F		E	E	E	E	E			<u> </u>
470	471	Х	Х	С	С	С	E	Е	E	Е	E	E	E	E	Е	Е	E	E	E	E	F		Е	E	E	E	E	Е	Е	
	561	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	Е	Е	
	681	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	F	F	
	751	Х	Х	С	С	С	E	E	E	E	E	E	E	E	E	Е	E	E	E	F	F		E	E	E	E	E	F	F	
	821 102	X	X	C X	C C	C	E	E	E	E	E E	E	E	E	E	E	E	E	E	F	F		E E	E	E	E	E	F	F	
	122	Х	X	X	C	С	E	E	E	E	E	E	E	E	E	E	E	E	E	F	F		E	E	E	E	E	F	F	
	152	Х	X	Х	С	С	E	E	E	E	E	E	E	E	E	E	E	E	E	F	F		E	E	E	E	E	G	G	
1800	182	Х	Х	Х	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	G	G	
	222	Χ	Х	Х	C	С	Е	Е	Е	Е	Е	Е	F	Е	Е	Е	Е	F	F	F			Е	Е	Е	Е	Е	G	G	
	272	С	С		С	С	Е	Е		Е	Е	Е	F	Е	Е	Е	Е	F	F				Е	Е	Е	Е	Е	G	G	
	332 392	С	С		С	С	E			E	E	E	F	Е	E	E	E	F	F		-		E	E	E	F	F	G	G	
1	472	C C	C C		C C	C	E			E	E	E	F		E	E	E	F					E	E	E	F	F	G	G G	
	562	С	С		C	С	E			E	E	E	F		E	E	E	F		-	\vdash		E	E	E	G	G	G	6	
6800	682	С	С		С	С	E			E	E	E			E	E	E	F					E	E	E	G	G	_		
8200	822	С	С		С	С	Е			Е	Е	Е			Е	Е	Е						Е	Е	Е	G	G			
	103	С	С		С	С	Е			Е	Е	Е			Е	Е	Е						Е	Е	F	G	G			
	153	С	С		Е	Е	Е			Е	Е	Е			F	F	F						Е	Е	F	G				
	183 223	С	С		E	E				E E	E	E F			F F	F	F			-	-		Е	E	G					<u> </u>
	273	С	С		E E	E				E	E E	F			F	F							E E	E	G					1
	333				E	E				E	E				F	F							E	E	G					
0.039	393									E	E				F	F		İ			t		E	E	G			İ		
	473									Е	Е				F	F							Е	Е	G					
	563									F	F				F	F							F	F						
	683									F	F				F	F					1		F	F		-		-		1
	823 104									F F	F							-	-	-	\vdash	-	F	F		-		-		-
	154									Г	Г							-	-		1	-	G	G		-		-		1
	224																						G	G						
	274																													
	334																													
	394																		<u> </u>	-	₩	_		_	₩	-		_		1
	474																				-				-			_		1
	564 684	-				\vdash										_		-	\vdash	-	\vdash	\vdash	\vdash	\vdash	\vdash			\vdash		\vdash
	824																		1	<u> </u>	 			 	 			 	\vdash	
	105																				1				1					
Voltage (V)		600	630	1000	600	630	1000	1500	2000	600	630	1000	1500	2000	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000
Case Size			0805				1206					1210						18	308							18	12			

Letter	Α	С	E	F	G	Х	7
Max.	0.813	1.448	1.8034	2.2098	2.794	0.940	3.30
Thickness	(0.032)	(0.057)	(0.071)	(0.087)	(0.110)	(0.037)	(0.130)



For 600V to 5000V Applications



X7R CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

Case Siz	e				18	325								2220)								222	5								3640)			
Soldering						w Only								low 0									flow (eflow (
	mm (in.)			(± 0.50 ± 0.02								70 ± 0. 24 ± 0.									70 ± 0 25 ± 0									.14 ± 0				
W) Width	mm				6.30 :	± 0.40							5.0	00 ± 0.	.40							6.	30 ± 0	.40							1	0.2 ± 0	.25			
780	(in.) mm			(± 0.01 40	6)						(0.19	7 ± 0. 3.40	.016)							(0.2	50 ± 0 3.40	.010)							(0.4	100 ± 0	.010)	_		
Thickness	(in.)				(0.1	134)							(0.134)								(0.100)))								(0.100))			
(t) Terminal	mm max			(: 0.75 : 0.030	± 0.35 ± 0.01	4)						0.8	35 ± 0. 33 ± 0.	.35 .014)							(0.0)	85 ± 0 33 ± 0	.35							0. 1.	76 (0.0 52 (0.0)30))60)			
Voltage (V		600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000	5000	600	630	1000	1500	2000	2500	3000	4000	5000	600	630	1000	1500	2000	2500	3000	4000	5000
Cap (pF) 100	101																																	ш	\sqcup	\vdash
120	121																																	ш	\sqcup	$\vdash \vdash$
150	151																																	\square	ш	$\vdash \vdash$
180	181																														_			\sqcup	ш	\vdash
220	221																																	\sqcup	ш	$\vdash \vdash$
270	271																														_		_	\sqcup	ш	$\vdash \vdash$
330	331																						_							_	_		_	\sqcup	ш	$\vdash \vdash$
390 470	391 471																						_											\square	\square	$\vdash \vdash$
								_									\Box						_							_				\square	\square	\vdash
560	561 681				-	\vdash	\vdash	\vdash	-		-		-	\vdash	-		\vdash	_		_	\vdash		1	-	-			_		-		-	-	\vdash	$\vdash \vdash$	$\vdash \vdash$
750	751				-	\vdash	\vdash	\vdash	-	_	<u> </u>		-	\vdash	\vdash	_	$\vdash \vdash \vdash$	_		<u> </u>	\vdash	-	-	-	\vdash	-	-	_	-	-	-	-	-	\vdash	\vdash	$\vdash\vdash\vdash$
820	821					-	-	-							-			_					-		-					-				\vdash	ш	\vdash
1000	102		F	-	-	F	F	F		_	-	F	-	-	-				_	_	-	F		-	_											
1200	122	F	F	F	F	F	F	F		F	F	F	F	F	F	G	\vdash		F	F	F	F	F	F	F			G G	G	G	G	G	G	G	G	G G
1500	152	F	F	F	F	F	F	F		F	F	F	F	F	F	_			F	F	F	F	F	F	F			G	-	G	G	G	_	G	G	
1800	182	F	F	F	F	F	F	F		F	F	F	F	F	F	G	\vdash		F	F	F	F	F	F	F			G	G	G	G	G	G	G G	G G	G
2200	222	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F			G	G	-	G	-	-	-	-	G G
2700	272	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F			G	G	G	G	G	G	G G	G G	G
3300	332	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F			G	G G	G	G	G	G	G	G	G
3900	392	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F			G	G	G	G	G	G	G	G	G
4700	472	F	F	F	F	F	F	F		F	F	F	F	F	F	G	\vdash	_	F	F	F	F	F	F	F			G	G	G	G	G	G	G	G	\vdash
5600	562	F	F	F	F	F	F	F		-	F	F	F	F	F	G	\vdash	_	F	F	F	F	F	F	-			G	G	G	G	G	G	G	G	\vdash
6800	682	F	F	F	G	G	G	G		F	F	F	F	F	G	G			F	F	F	F	F	G	G			G	G	G	G	G	G	G	G	\vdash
8200	822	F	F	F	G	G	G	G		F	F	F	G	G	G	G			F	F	F	F	F	G	G			G	G	G	G	G	G	G		-
Cap (µF) 0.010	103	F	F	F	G	G	G	G		F	F	F	G	G	G	G			F	F	F	F	F	G	G			G	G	G	G	G	G	G	\vdash	-
0.015	153	F	F	F	G	G	G	<u> </u>		F	F	F	G	G	G	_			F	F	F	G	G	G	G			G	G	G	G	G	G	G	\vdash	\Box
0.018	183	F	F	F	G	G				F	F	F	G	G	G				F	F	F	G	G	G				G	G	G	G	G	G	G	\Box	
0.022	223	F	F	F	G	G				F	F	F	G	G					F	F	F	G	G	G				G	G	G	G	G	G		М	
0.027	273	F	F	F	G					F	F	F	G	G					F	F	F	G	G					G	G	G	G	G		\vdash	М	\Box
0.033	333	F	F	F	G					F	F	F	G						F	F	F	G	G					G	G	G	G			\vdash	М	\Box
0.039	393	F	F	F	G					F	F	F	G						F	F	F	G						G	G	G	G				П	
0.047	473	F	F	F	Р					F	F	F	G						F	F	F	G						G	G	G	G				П	
0.056	563	F	F	F	G					F	F	F	G						F	F	F	G						G	G	G	G				\Box	
0.068	683	F	F	G						F	F	G							F	F	F	G						G	G	G	G				П	\Box
0.082	823	F	F	G						F	F	G							F	F	G							G	G						П	\Box
0.100	104	F	F	G						F	F	G							F	F	G							G	G						П	
0.150	154	F	F							F	F	G							F	F	G							G	G					\Box	\Box	\Box
0.220	224	F	F							F	F	G							F	F								G	G					\Box	П	
0.270	274	F	F							F	F								F	F								G	G		L					
0.330	334	F	F							F	F								F	F								G	G							
0.390	394	F	F							F	F								F	F								G	G							
0.470	474	F	F							F	F								F	F								G	G							
0.560	564	G	G							G	G								F	F								G	G							
0.680	684									G	G								G	G																
0.820	824																		G	G																
1.000	105																																			
Voltage (\		600	630	1000			2500	3000	4000	600	630	1000				3000	4000	5000	600	630	1000				3000	4000	5000	600	630	1000	1500			3000	4000	5000
Case Siz	е				18	325								2220	1								222	5								3640)			

Letter	Α	С	E	F	G	Χ	7
Max.	0.813	1.448	1.8034	2.2098	2.794	0.940	3.30
Thickness	(0.032)	(0.057)	(0.071)	(0.087)	(0.110)	(0.037)	(0.130)

Tin/Lead Termination "B" - 600V to 5000V Applications





NEW 630V RANGE

AVX Corporation will support those customers for commercial and military Multilayer Ceramic Capacitors with a termination consisting of 5% minimum lead. This termination is indicated by the use of a "B" in the 12th position of the AVX Catalog Part Number. This fulfills AVX's commitment to providing a full range of products to our customers. AVX has provided in the following pages, a full range of values that we are offering in this "B" termination.

Larger physical sizes than normally encountered chips are used to make high voltage MLC chip product. Special precautions must be taken in applying these chips in surface mount assemblies. The temperature gradient during heating or cooling cycles should not exceed 4°C per second.

The preheat temperature must be within 50°C of the peak temperature reached by the ceramic bodies through the soldering process. Chip sizes 1210 and larger should be reflow soldered only. Capacitors may require protective surface coating to prevent external arcing.

For 1825, 2225 and 3640 sizes, AVX offers leaded version in either thru-hole or SMT configurations (for details see section on high voltage leaded MLC chips).

HOW TO ORDER

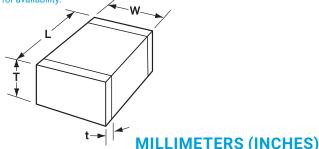
LD08	A	A T	<u>271</u>	<u>K</u>	<u>A</u>	<u>B</u>	1	A
AVX Style LD05 - 0805 LD06 - 1206 LD10 - 1210 LD08 - 1808 LD12 - 1812 LD13 - 1825 LD20 - 2220 LD14 - 2225 LD40 - 3640	1500V = S 2000V = G 2500V = W 3000V = H 4000V = J 5000V = K	Temperature Coefficient COG = A X7R = C	Capacitance Code (2 significant digits + no. of zeros) Examples: 10 pF = 100 100 pF = 101 1,000 pF = 102 22,000 pF = 223 220,000 pF = 224 1 µF =105	Capacitance Tolerance COG: J = ±5% K = ±10% M = ±20% X7R: K = ±10% M = ±20% Z = +80%, -2	Test Level A = Standard 4 = Automotive*	Termination* B = 5% Min Pb X = FLEXITERM® 5% min. Pb*	Packaging 2 = 7" Reel** 4 = 13" Reel	Special Code A = Standard

Notes: Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Contact plant for recommendations. Contact factory for availability of Termination and Tolerance options for Specific Part Numbers.

- * FLEXITERM is not available in the LD40 Style
- ** The LD40 Style is not available on 7" Reels.
- *** AVX offers nonstandard chip sizes. Contact factory for details...

* Not all values are supported in Automotive grade. Please contact factory for availability.

NOT RoHS Compliant



DIMENSIONS

SIZE	LD05 (0805)	LD06 (1206)	LD10* (1210)	LD08* (1808)	LD12* (1812)	LD13* (1825)	LD20* (2220)	LD14* (2225)	LD40* (3640)
(L) Length	2.10 ± 0.20	3.30 ± 0.30	3.30 ± 0.40	4.60 ± 0.50	4.60 ± 0.50	4.60 ± 0.50	5.70 ± 0.50	5.70 ± 0.50	9.14 ± 0.25
	(0.083 ± 0.008)	(0.130 ± 0.012)	(0.130 ± 0.016)	(0.181 ± 0.020)	(0.181 ± 0.020)	(0.181 ± 0.020)	(0.224 ± 0.020)	(0.224 ± 0.020)	(0.360 ± 0.010)
(W) Width	1.25 ± 0.20	1.60 ± 0.20	2.50 ± 0.30	2.00 ± 0.20	3.20 ± 0.30	6.30 ± 0.40	5.00 ± 0.40	6.30 ± 0.40	10.2 ± 0.25
	(0.049 ± 0.008)	(0.063 ± 0.008)	(0.098 ± 0.012)	(0.079 ± 0.008)	(0.126 ± 0.012)	(0.248 ± 0.016)	(0.197 ± 0.016)	(0.248 ± 0.016)	(0.400 ± 0.010)
(T) Thickness Max.	1.35	1.80	2.80	2.20	2.80	3.40	3.40	3.40	2.54
	(0.053)	(0.071)	(0.110)	(0.087)	(0.110)	(0.134)	(0.134)	(0.134)	(0.100)
(t) terminal min. max.	0.50 ± 0.20	0.60 ± 0.20	0.75 ± 0.35	0.75 ± 0.35	0.75 ± 0.35	0.75 ± 0.35	0.85 ± 0.35	0.85 ± 0.35	0.76 (0.030)
	(0.020 ± 0.008)	(0.024 ± 0.008)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.033 ± 0.014)	(0.033 ± 0.014)	1.52 (0.060)

^{*}Reflow Soldering Only

Performance of ceramic capacitors can be simulated by using the online SpiMLCC software program - http://spicat.avx.com/mlcc Custom values, ratings and configurations are also available.





Tin/Lead Termination "B" - 600V to 5000V Applications

NP0 (C0G) Dielectric

Performance Characteristics

Capacitance Range	10 pF to 0.047 μF
Capacitalice Ralige	(25°C, 1.0 ±0.2 Vrms at 1kHz, for ≤ 1000 pF use 1 MHz)
Capacitance Tolerances	±5%, ±10%, ±20%
Dissipation Factor	0.1% max. (+25°C, 1.0 \pm 0.2 Vrms, 1kHz, for \leq 1000 pF use 1 MHz)
Operating Temperature Range	-55°C to +125°C
Temperature Characteristic	0 ±30 ppm/°C (0 VDC)
Voltage Ratings	600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)
Insulation Resistance (+25°C, at 500 VDC)	100K MΩ min. or 1000 MΩ - μF min., whichever is less
Insulation Resistance (+125°C, at 500 VDC)	10K MΩ min. or 100 MΩ - μF min., whichever is less
Dielectric Strength	Minimum 120% rated voltage for 5 seconds at 50 mA max. current

HIGH VOLTAGE COG CAPACITANCE VALUES

VOLTA	IGE	LD05 (0805)	LD06 (1206)	LD10 (1210)	LD08 (1808)	LD12 (1812)	LD13 (1825)	LD20 (2220)	LD14 (2225)	LD40 (3640)
600/630	min.	10 pF	10 pF	100 pF	100 pF	100 pF	1000 pF	1000 pF	1000 pF	1000 pF
000/030	max.	330 pF	1200 pF	2700 pF	3300 pF	5600 pF	0.012 μF	0.012 pF	0.018 μF	0.047 μF
1000	min.	10 pF	10 pF	10 pF	100 pF	100 pF	100 pF	1000 pF	1000 pF	1000 pF
1000	max.	180 pF	560 pF	1500 pF	2200 pF	3300 pF	8200 pF	0.010 pF	0.010 μF	0.022 μF
1500	min.	_	10 pF	10 pF	10 pF	10 pF	100 pF	100 pF	100 pF	100 pF
1300	max.	_	270 pF	680 pF	820 pF	1800 pF	4700 pF	4700 pF	5600 pF	0.010 μF
2000	min.	_	10 pF	10 pF	10 pF	10 pF	100 pF	100 pF	100 pF	100 pF
2000	max.	_	120 pF	270 pF	330 pF	1000 pF	1800 pF	2200 pF	2700 pF	6800 pF
2500	min.	_	_	_	10 pF	10 pF	10 pF	100 pF	100 pF	100 pF
2300	max.	_	_	_	180 pF	470 pF	1200 pF	1500 pF	1800 pF	3900 pF
3000	min.	_	_	_	10 pF	10 pF	10 pF	10 pF	10 pF	100 pF
3000	max.	_	_	_	120 pF	330 pF	820 pF	1000 pF	1200 pF	2700 pF
4000	min.	_	_	_	10 pF	10 pF	10 pF	10 pF	10 pF	100 pF
4000	max.	_	_	_	47 pF	150 pF	330 pF	470 pF	560 pF	1200 pF
5000	min.	_	_	_	_	_	_	10 pF	10 pF	10 pF
3000	max.	_	-	_	_	_	_	220 pF	270 pF	820 pF

X7R Dielectric

Performance Characteristics

Capacitance Range	10 pF to 0.56 μF (25°C, 1.0 ±0.2 Vrms at 1kHz)
Capacitance Tolerances	±10%; ±20%; +80%, -20%
Dissipation Factor	2.5% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)
Operating Temperature Range	-55°C to +125°C
Temperature Characteristic	±15% (0 VDC)
Voltage Ratings	600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)
Insulation Resistance (+25°C, at 500 VDC)	100K MΩ min. or 1000 MΩ - μF min., whichever is less
Insulation Resistance (+125°C, at 500 VDC)	10K M Ω min. or 100 M Ω - μF min., whichever is less
Dielectric Strength	Minimum 120% rated voltage for 5 seconds at 50 mA max. current

HIGH VOLTAGE X7R MAXIMUM CAPACITANCE VALUES

VOLTA	IGE	0805	1206	1210	1808	1812	1825	2220	2225	3640
600/630	min.	100 pF	1000 pF	1000 pF	1000 pF	1000 pF	0.010 µF	0.010 μF	0.010 µF	0.010 μF
000/030	max.	6800 pF	0.022 μF	0.056 μF	0.068 µF	0.120 µF	0.390 µF	0.270 μF	0.330 μF	0.560 μF
1000	min.	100 pF	100 pF	1000 pF	1000 pF	1000 pF	1000 pF	1000 pF	1000 pF	0.010 μF
1000	max.	1500 pF	6800 pF	0.015 μF	0.018 µF	0.039 µF	0.100 µF	0.120 μF	0.150 μF	0.220 μF
1500	min.	_	100 pF	100 pF	100 pF	100 pF	1000 pF	1000 pF	1000 pF	1000 pF
1500	max.	-	2700 pF	5600 pF	6800 pF	0.015 µF	0.056 µF	0.056 µF	0.068 µF	0.100 µF
2000	min.	_	10 pF	100 pF	100 pF	100 pF	100 pF	1000 pF	1000 pF	1000 pF
2000	max.	_	1500 pF	3300 pF	3300 pF	8200 pF	0.022 µF	0.027 µF	0.033 µF	0.027 µF
2500	min.	_	_	_	10 pF	10 pF	100 pF	100 pF	100 pF	1000 pF
2500	max.	_	_	_	2200 pF	5600 pF	0.015 µF	0.018 μF	0.022 µF	0.022 μF
3000	min.	_	_	_	10 pF	10 pF	100 pF	100 pF	100 pF	1000 pF
3000	max.	_	_	_	1800 pF	3900 pF	0.010 µF	0.012 µF	0.015 µF	0.018 µF
4000	min.	_	_	_	_	-	_	_	-	100 pF
4000	max.	_	_	_	_	_	_	_	_	6800 pF
5000	min.	_	_	_	_	_	_	-	-	100 pF
3000	max.	_	_	_	_	_	_	_	_	3300 pF



FLEXITERM® - 600V to 5000V Applications





High value, low leakage and small size are difficult parameters to obtain in capacitors for high voltage systems. AVX special high voltage MLC chips capacitors meet these performance characteristics and are designed for applications such as snubbers in high frequency power converters, resonators in SMPS, and high voltage coupling/DC blocking. These high voltage chip designs exhibit low ESRs at high frequencies.

To make high voltage chips, larger physical sizes than are normally encountered are necessary. These larger sizes require that special precautions be taken in applying these chips in surface mount assemblies. In response to this, and to follow from the success of the FLEXITERM® range of low voltage parts, AVX is delighted to offer a FLEXITERM® high voltage range of capacitors, FLEXITERM®.

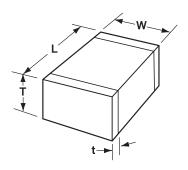
The FLEXITERM® layer is designed to enhance the mechanical flexure and temperature cycling performance of a standard ceramic capacitor, giving customers a solution where board flexure or temperature cycle damage are concerns.

HOW TO ORDER

1808	A 	<u>C</u>	<u>272</u>	<u>K</u>	<u>A</u>	z 	1	<u>A</u>
AVX Style 0805 1206 1210 1808 1812 1825 2220 2225	Voltage 600V/630V = C 1000V = A 1500V = S 2000V = G 2500V = W 3000V = H 4000V = J 5000V = K	Temperature Coefficient COG = A X7R = C	Capacitance Code (2 significant digits + no. of zeros) Examples: 10 pF = 100 100 pF = 101 1,000 pF = 102 22,000 pF = 223 220,000 pF = 224 1 µF =105	Capacitance Tolerance COG: J = ±5% K = ±10% M = ±20% X7R: K = ±10% M = ±20% Z = +80%, -20%	Test Level	Termination* Z=FLEXITERM® 100%Tin (RoHS Complian	Packaging 2 = 7" Reel 4 = 13" Reel nt)	Special Code A = Standard

Notes: Capacitors with X7R dielectrics are not intended for applications across AC supply mains or AC line filtering with polarity reversal. Contact plant for recommendations. Contact factory for availability of Termination and Tolerance options for Specific Part Numbers.

^{***} AVX offers nonstandard chip sizes. Contact factory for details.





DIMENSIONS

MILLIMETERS (INCHES)

SIZE	0805	1206	1210*	1808*	1812*	1825*	2220*	2225*
(L) Length	2.10 ± 0.20	3.30 ± 0.30	3.30 ± 0.40	4.60 ± 0.50	4.60 ± 0.50	4.60 ± 0.50	5.70 ± 0.50	5.70 ± 0.50
	(0.083 ± 0.008)	(0.130 ± 0.012)	(0.130 ± 0.016)	(0.181 ± 0.020)	(0.181 ± 0.020)	(0.181 ± 0.020)	(0.224 ± 0.020)	(0.224 ± 0.020)
(W) Width	1.25 ± 0.20	1.60 ^{+0.30}	2.50 ± 0.30	2.00 ± 0.20	3.20 ± 0.30	6.30 ± 0.40	5.00 ± 0.40	6.30 ± 0.40
	(0.049 ±0.008)	(0.063 ^{+0.012} _{-0.004})	(0.098 ± 0.012)	(0.079 ± 0.008)	(0.126 ± 0.012)	(0.248 ± 0.016)	(0.197 ± 0.016)	(0.248 ± 0.016)
(T) Thickness	1.35	1.80	2.80	2.20	2.80	3.40	3.40	3.40
Max.	(0.053)	(0.071)	(0.110)	(0.087)	(0.110)	(0.134)	(0.134)	(0.134)
(t) terminal min.	0.50 ± 0.20	0.60 ± 0.20	0.75 ± 0.35	0.75 ± 0.35	0.75 ± 0.35	0.75 ± 0.35	0.85 ± 0.35	0.85 ± 0.35
	(0.020 ± 0.008)	(0.024 ± 0.008)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.030 ± 0.014)	(0.033 ± 0.014)	(0.033 ± 0.014)

^{*}Reflow Soldering Only



Performance of SMPS capacitors can be simulated by downloading SpiCalci software program - http://www.avx.com/SpiApps/default.asp#spicalci
Custom values, ratings and configurations are also available.

FLEXITERM® - 600V to 5000V Applications



NP0 (COG) Dielectric Performance Characteristics

Capacitance Range	10 pF to 0.100 μF (+25°C, 1.0 ±0.2 Vrms, 1kHz)
Capacitance Tolerances	±5%, ±10%, ±20%
Dissipation Factor	0.1% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)
Operating Temperature Range	-55°C to +125°C
Temperature Characteristic	0 ±30 ppm/°C (0 VDC)
Voltage Ratings	600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)
Insulation Resistance (+25°C, at 500 VDC)	100K MΩ min. or 1000 MΩ - μF min., whichever is less
Insulation Resistance (+125°C, at 500 VDC)	10K MΩ min. or 100 MΩ - μF min., whichever is less
Dielectric Strength	Minimum 120% rated voltage for 5 seconds at 50 mA max. current

NP0 (C0G) CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

EFERRED	ગ	ZES	AI	15	<u>оп</u>	ΑD	LD																						
Case Size		0805				1206					1210						18	308							18	12			
Soldering		eflow/W				low/W					flow 0							w Only							Reflov				
(L) Length mm (in.)	(0	2.10 ± 0.: .083 ± 0.	20	ł		30 ± 0. 30 ± 0.				3.1 (0.1	30 ± 0. 30 ± 0.	40 016)					4.60	± 0.50 ± 0.020))					,	4.60 ± 0.181 ±)		
W) Width mm	T	1.25 ± 0.:	20		1.60	± 0.30/	/-0.10		ļ	2.	50 ± 0.	30					2.00	± 0.20							3.20 ±	₹ 0.30			
/ (In.)	(0	.049 ± 0. 1.35	008)	(0.063 :	± 0.012 1.80	/-0.004	1)	-	(0.0	98 ± 0. 2.80	012)				- (± 0.008	3)					(0.126 <u>±</u> 2.8)		
(in.)	ــــــ	(0.053))			(0.071)			<u> </u>		(0.110)						(0.0	087)							(0.1	10)			
(t) Terminal mm max	(0	0.50 ± 0. .020 ± 0.	20 008)		(0.0	.60 ± 0. 24 ± 0.	20 008)				75 ± 0. 30 ± 0.							± 0.35 ± 0.014	1)					(0.75 ± 0.030 ±	£ 0.014)		
Voltage (V)	600	630		600	630	1000	1500		600	630	1000	1500	2000	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000
Cap (pF) 1.5 1R 1.8 1R		A		X	X	X	X	X																					\vdash
2.2 2R		A		X	X	X	X	X																					
2.7 2R				X	X	Х	Х	X								С	С	С	С	С									
3.3 3R 3.9 3R				X	X	X	X	X	\vdash							C	C	C	C	C									\vdash
4.7 4R	7 A	Α		Х	Х	Х	Х	Х								С	C	С	С	С									
5.6 5R 6.8 6R				X	X	X	X	X	<u> </u>							С	С	С	С	С									\vdash
6.8 6R 8.2 8R				X	X	X	X	X	-							C	C	C	C	C									\vdash
10 10	0 A	Α	Α	Х	Х	Х	Χ	Х	С	С	D	D	D	С	С	С	С	С	С	С	С	C C C C C C							Е
12 12 15 15			A	X	X	X	X	X	C	C	D D	D D	D D	C C	C	C	C	C	C	C	C	C	C	C	C	C	C	C	E
18 18			A	X	X	X	X	X	C	С	D	D	D	C	С	C	C	C	C	С	С	С	C	C	С	С	C	C	E
22 22			Α	Х	Х	Х	Х	Х	С	С	D	D	D	С	С	С	С	С	С	С	С	С	С	С	С	С	С	С	Е
27 27 33 33			A	X	X	X	X D	X D	C	C	D D	D D	D D	C	C	C	C	C	C	C	C	C	C	C	C	C	C	C	E
39 39	0 A		A	X	X	Х	D	D	С	С	D	D	D	С	C	C	C	С	С	С	С	С	C	C	С	С	C	C	E
47 47			Α	Х	Х	М	D	D	С	С	D	D	D	С	С	С	С	С	С	С	С	С	С	С	С	С	С	С	Е
56 56 68 68			A	X	X	M	C	C	C	C	D D	C	C	C	C	C	C	C	C	C		C	C	C	C	C	C	C	F
82 82	0 X		X	X	X	C	С	C	C	С	D	C	C	С	O	C	C	C	C	C		С	C	C	С	C	C	C	F
100 10 120 12			Х	X	X	C	С	C	С	С	C	C	C	С	С	C	C	C	F	F		С	C	С	С	C	C	С	F
120 12 150 15			C	X	X	C	E	E	C	C	C	C E	C E	C	C	C	C F	C	F	F		C	C	C	C	C	C	C	G G
180 18	1 C	С	С	Х	Χ	Е	Е	E	С	С	E	Е	Е	С	С	С	F	F	F	F		С	С	С	С	С	F	F	
220 22 270 27	_			C	Х	E	E	E	С	С	E	E	E	С	С	C	F	F	F	F		С	C	С	С	С	F	F	\vdash
330 33		C		C	C	E	E	E	C	C	E	E	E	C	C	C F	F	F	F	F		C	C	C	C F	C F	F	F	\vdash
390 39	1 C	С		С	С	Е	Е	Е	С	С	Е	Е	Е	С	С	F	F	F	F	F		С	С	С	F	F	F	F	
470 47 560 56		C		C	C	E	Е	Е	C	С	E F	E	E	C	С	F	F	F	F	F		C	C	F	F	F	F	F	\vdash
680 68		C		C	C	E			C	C	E	F	F	C	C	F	F	F				C	C	F	F	F	G	G	
750 75		С		Е	Е	Е			С	С	Е	G	G	С	С	F	F	F				С	С	F	F	F	G	G	
820 82 1000 10		С		E F	E	E			C	C	E F	G	G	C	C	F	E	E				C	C	F	F	F	G	G	\vdash
1200 12				E	E				C	С	E			E	E	F	E	E				С	C	F	E	E			
1500 15				E	E				С	С	G			E	E	F						С	C	F	F	F			\vdash
1800 18 2200 22	2	+	 	F	E				C E	C E	G			E	E	F				\vdash		C	C	E	G	G			$\vdash\vdash$
2700 27	2			E	E				E	E				E	E							С	C	E	G	G			
3300 33 3900 39	2	_		Е	Е				E	E				E E	E F		_	-	-			С	C	F					$\vdash\vdash$
4700 47				 					E	E				E	E							C	C	G					\vdash
5600 56	2								Е	Е				E	E							С	C						
6800 68 8200 82		-		<u> </u>					-					F	F			-	-			C F	C F						\vdash
Cap (µF) 0.010 10		+																				E	E						H
0.012 12	_	1				t										t	l –	t				F	F				l –		Н
0.015 15	_	I																				G	G						
0.018 18	3																					G	G						
0.022 22	_																												
0.033 33	_																		_					_					Ш
0.047 47	_	\perp		<u> </u>	_	_			<u> </u>							_				_									Ш
0.056 56	_			-		-			-							-													Ш
0.068 68	_	+		<u> </u>	<u> </u>	-	_	_	<u> </u>		<u> </u>					-			-	<u> </u>				-	_	_		_	$\vdash\vdash\vdash$
0.100 10 Voltage (V)	600	0 630	1000	600	630	1000	1500	2000	600	630	1000	1500	2000	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000
Case Size	1 00	0805	,	550	, 550	1206		, 2000	550	, 550	1210	,	,2000	- 550		, . 500		308	, 2000	, 5500	,	550	, 550	,	18		, 2000	, 5500	,
	_																												

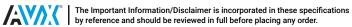




NPO (COG) CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

PERKED SIZ	_3		<u> </u>										000									000				
Case Size					825								2220									2225				
Soldering mm					ow Only 0 ± 0.50								eflow (eflow (
(L) Length (in.)					1 ± 0.02								224 ± 0					ł				.72 ± 0				
mm					± 0.40								.00 ± 0									.35 ± 0				
W) Width (in.)					3 ± 0.01								197 ± 0									250 ± 0				
(T) Thickness mm				` ;	3.40								3.40									3.40				
` ′ (in.)					.134)								(0.134									(0.13	4)			
(t) Terminal mm					5 ± 0.35								$.85 \pm 0$								0.	.85 ± 0	.35			
Voltage (V)	600	620	1000		2000 ± 0.01		2000	4000	600	620	1000)33 ± 0		2000	4000	5000	600	630	1000			2500	3000	4000	5000
Cap (pF) 1.5 1R5	000	030	1000	1300	2000	2300	3000	4000	000	030	1000	1300	2000	2300	3000	4000	3000	000	030	1000	1300	2000	2300	3000	4000	3000
1.8 1R8																										
2.2 2R2																										
2.7 2R7																										
3.3 3R3																										
3.9 3R9 4.7 4R7					-			<u> </u>	-							-		-					-			
5.6 5R6																										
6.8 6R8																										
8.2 8R2																		L			L					
10 100	Е	Ε	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F
12 120	Е	Е	Е	E	E	E	Е	E	Е	E	E	E	Е	E	E	E	E	E	Е	E	Е	E	Е	E	F	F
15 150 18 180	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F	F
22 220	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F	F
27 270	E	Ė	E	E	Ē	E	E	E	E	Ē	Ē	Ē	E	Ē	E	Ē	E	E	E	Ē	E	E	E	E	F	F
33 330	E	E	E	E	E	E	E	E	E	E	E	E	Е	E	E	E	E	E	Е	E	E	E	E	E	F	F
39 390	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F
47 470	Е	Е	Е	Е	Е	Е	Е	F	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	E	Е	Е	E	F	G
56 560	E	E	E	E	E	E	E	F	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F	G
68 680 82 820	E	E	E	E	E	E	E	F	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	F	G G
100 101	E	E	E	E	E	E	E	F	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	E	G	G
120 121	E	Ē	E	Ē	E	E	E	F	E	E	Ē	Ē	Ē	Ē	Ē	Ē	Ē	E	Ē	Ē	E	Ē	Ē	E	G	G
150 151	Е	Е	Е	Е	Е	Е	Е	F	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	G	G
180 181	Е	Е	Е	Е	Е	Е	Е	F	Е	Е	Е	Е	Е	Е	Е	F	F	Е	Е	Е	Е	Е	Е	Е	G	G
220 221	E	E	E	E	E	E	E	F	E	E	E	E	E	E	E	F	F	E	E	E	E	E	E	E	G	G
270 271 330 331	E	E	E	E	E	E	E	F	E	E	E	E	E	E	E			E	E	E	E	E	E	E	G	G
390 391	E	Ē	E	Ē	Ē	E	Ē	-	E	Ē	Ē	Ē	E	Ē	Ē			E	E	Ē	E	E	E	E	G	
470 471	E	Ē	E	Ē	Ē	Ē	Ē		Ē	Ē	Ē	Ē	E	Ē	E			E	Ē	Ē	Ē	Ē	Ē	Ē	G	
560 561	Е	Е	Е	Е	Е	Е	Е		Е	Е	Е	Е	Е	Е	Е			Е	Е	Е	Е	Е	Е	Е	G	
680 681	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	F	F			Е	Е	E	E	Е	Е	E		
750 751	E	E	E	E	E	F	F		E	E	E	E	E	F	F			E	E	E	E	E	E	E		
820 821 1000 102	E	E	E	E	E	F	F		E	E	E	E	E	F	F			E	E	E	E	E	F E	E		
1200 122	E	Ė	E	Ē	E	G	G		E	Ë	Ē	Ē	E	G	G			E	E	E	E	E	F	F		
1500 152	E	Ē	E	F	F	G	G		Ē	Ē	Ē	F	F	G	G			E	E	Ē	Ē	Ē	F	F		
1800 182	Е	Ε	Е	F	F	G	G		Е	Е	Е	F	F	G	G			Е	Е	Е	Е	Е	G	G		
2200 222	E	E	E	G	G				E	E	E	G	G					E	E	E	E	E				
2700 272 3300 332	E	E	E	G G	G		-	-	E	E	E	G	G G			<u> </u>		E	E	E	F	F		-		-
3300 332 3900 392	E	E	E	G	G			-	E	E	E	G G	G					E	E	E	G	G				
4700 472	E	Ė	E	G	G				E	E	E	G	G					F	F	F	G	G				
5600 562	F	F	F	G	G				F	F	F							F	F	F	G	G				
6800 682	F	F	F						F	F	F							F	F	F	G	G				
8200 822	G	G	Ğ		<u> </u>		_	ļ	G	G	G					ļ		_	G	G		_	<u> </u>			
Cap (μF) 0.010 103					-		-	-	\vdash	-	-				-	-		G	G	G		-	-	-		-
0.012 123 0.015 153	\vdash						\vdash	-	\vdash							 		G G	G G	G G			\vdash	1		
0.013 133																		G	G	G				<u> </u>		
0.022 223									t									G	G	G				1		
0.033 333																		G	G	G						
0.047 473																		G	G	G						
0.056 563								<u> </u>	<u> </u>							<u> </u>		G	G	G			<u> </u>	<u> </u>		
0.068 683 0.100 104					-		-	-								-	-	G	G	G			-	-	-	
Voltage (V)	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000	5000		G 630	1000	1500	2000	2500	3000	4000	5000
Case Size					825		,	,	1		,	,	2220		,	,	,	1		,	, . 500	222		, 2 300	,	,

Letter	A	C	E	F	G	X
Max.	0.813	1.448	1.803	2.210	2.794	0.940
Thickness	(0.032)	(0.057)	(0.071)	(0.087)	(0.110)	(0.037)



FLEXITERM® - 600V to 5000V Applications



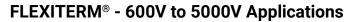
X7R Dielectric

Performance Characteristics

Capacitance Range	10 pF to 0.82 μF (25°C, 1.0 ±0.2 Vrms at 1kHz)
Capacitance Tolerances	±10%; ±20%; +80%, -20%
Dissipation Factor	2.5% max. (+25°C, 1.0 ±0.2 Vrms, 1kHz)
Operating Temperature Range	-55°C to +125°C
Temperature Characteristic	±15% (0 VDC)
Voltage Ratings	600, 630, 1000, 1500, 2000, 2500, 3000, 4000 & 5000 VDC (+125°C)
Insulation Resistance (+25°C, at 500 VDC)	100K MΩ min. or 1000 MΩ - μF min., whichever is less
Insulation Resistance (+125°C, at 500 VDC)	10K M Ω min. or 100 M Ω - μF min., whichever is less
Dielectric Strength	Minimum 120% rated voltage for 5 seconds at 50 mA max. current

X7R CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

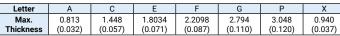
Case Size	. 1		0805				1206					1210 1808 1812 Reflow Only Reflow Only Reflow Only 3.30 0.40 4.60 ± 0.50 4.60 ± 0.50 (0.130 0.016) (0.181 ± 0.020) (0.181 ± 0.020)																		
										<u> </u>																				
Soldering	nm		low/W				low/V 30 ± 0.			-																				
(L) Length	ìn.)	(0.0	83 ± 0.	(800		(0.1	30 ± 0.	012)			(0.1	30 0.0	16)					(0.181	± 0.020)						(0.181 :	± 0.020)		
	nm in.)		.25 0.2 49 ± 0.		(1.60 - 0.063)	+0.30/ +0.012		1)			.50 0.3 098 0.0							0.20 ± 0.008	3)						: 3.20 : 0.126)	± 0.30 + 0.012)		
(T) Thickness 1	nm		1.35				1.80					2.80						2.	.20							2.	80	,		
	in.) nm	0.	(0.053) 50 ± 0.) 20		0.	(0.071 60 ± 0.	20		\vdash	0	(0.110) .75 0.3	5					0.75	087) ± 0.35							0.75	+ 0 35			
(t) Terrilliai r	max		50 ± 0. 20 ± 0.		600	(0.0	24 ± 0 .	(800	2000	600	(0.0)	30 ± 0.0	J14)	Inna	600			(0.030)	± 0.014	2500	10000	4000	600	1 600	11000	0.030	± 0.014	10500		14000
Voltage (V) Cap (pF) 100	101	X	630 X	C	C	C	F	1500 E	2000 E	E	630 E	1000 E	1500 E	2000 E	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000
120	121	X	X	С	С	С	E	E	E	E	E	E	E	E																
150	151	X	X	C	С	С	E	E	E	E	E	E	E	E																
180	181	Х	Х	С	С	С	E	E	E	E	Е	E	E	E																
220	221	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е																
270	271	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е									Е	Е	Е	Е	Е			
330	331	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F		E E E E E							
390	391	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F		E E E E E							
470	471	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F		E E E E E E							
560	561	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		E E E E E E							
680	681	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		E E E E F F							
750	751	Χ	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		E E E E E F F E E E E E F F							
820	821	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	F	F	
1000	102	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	F	F	
1200	122	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	F	F	
1500	152	Х	Х	С	С	С	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	Е	G	G	
1800	182	Х	Х		С	С	E	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	Е	F	F		Е	Е	Е	Е	E	G	G	
2200 2700	222 272	Х	Х		С	С	E	E	Е	E	E	E	F	E	E	E	E	F	F	F			E	E	E	E	E	G	G	
3300	332	X	X		С	С	E	Е		E	E	E	F	E	E	E	E	F	F		-		E	E	E	E	E	G	G	
3900	392	X	X		С	С	E			E	E	E	F	Е	E E	E	E	F	F				E E	E	E E	F	F	G	G	
4700	472	X	X		C	C	E			E	E	E	G		E	E	E	F		-	-		E	E E	E	F	F	G G	G G	
5600	562	X	X		С	С	E			E	E	E E	G G		E	E	E	F					E	E	E	G	G	G	G	
6800	682	X	X		С	С	E			E	E	E	0		E	E	E	F					E	E	E	G	G			
8200	822	X	X		С	С	E			E	E	E			E	E	E	<u> </u>		1			E	E	E	G	G			
Cap (µF) 0.010	103	C	C		C	С	E			E	E	E			E	E	E						E	E	F	G	G			
0.015	153	С	C		E	E	E			E	E	E			F	F	F						E	E	F	G				
0.018	183	С	С		E	E				E	Е	E			F	F	F						E	E	G					
0.022	223	С	С		Е	Е				Е	Е	Е			F	F							Е	Е	G					
0.027	273				Е	Е				Е	Е				F	F							Е	Е	G					
0.033	333				Е	Е				Е	Е				F	F							Е	Е	G					
0.039	393									Е	Е				F	F							Е	Е	G					
0.047	473									Е	Е				F	F							Е	Е	G					
0.056	563									F	F				F	F							F	F						
0.068	683									F	F				F	F							F	F						
0.082	823									F	F												F	F						
0.100	104									F	F								_		_		F	F						_
0.150	154		ļ		<u> </u>					<u> </u>										ļ	ļ		G	G						ļ
0.220	224		<u> </u>		<u> </u>					<u> </u>									_	<u> </u>	<u> </u>		G	G						_
0.270	274		ļ		<u> </u>	_		_		<u> </u>				_	<u> </u>		_	_	-	<u> </u>	ļ			_	-	_	_			<u> </u>
0.330	334		-		<u> </u>					├—				<u> </u>	<u> </u>			_	-	-	-		+ + + + + + + +							-
0.390	394		-		 					 				_	<u> </u>			_	-	-	-				-					-
0.470	474		-	-	\vdash		-			\vdash				<u> </u>	<u> </u>		-	-	-	-	-			-		-	-	-		-
0.560	564		-	-	\vdash		-			\vdash				<u> </u>	<u> </u>		-	-	-	-	-			-		-	-	-		-
0.680	684 824		-		\vdash	<u> </u>		<u> </u>		\vdash	-			\vdash	 	<u> </u>		-	1	-	-	-	+ + + + + + + +							\vdash
1.000	105									-																				
Voltage (V)	100	600	630	1000	600	630	1000	1500	2000	600	630	1000	1500	2000	600	630	1000	1500	2000	2500	3000	4000	0 600 630 1000 1500 2000 2500 3000 4							
Case Size	e 1		0805				1206					1210							308							18				
CUCC DIE	-				_		,																				_			





X7R CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

Case Siz	ze				18	25								2220									2225	5			
Solderin	g				Reflov	v Only							Ref	low 0	nly							Re	flow (Only			
(L) Length	mm (in.)				4.60 0.181	0.50)						5.	70 0.5 24 0.0	0							5.	72 ± 0 25 ± 0	.25			
W) Width	mm (in.)				6.30 0.248 ±	0.40							5.	00 0.4 97 0.0	0							6.3	35 ± 0 50 ± 0	.25			
(T)	mm				3.4	40								3.40 0.134									2.54				
Thickness	(in.) mm	┢			(0.1 0.75	0.35			_				0.	85 0.3	5				 				(0.100 85 ± 0				
(t) Terminal	max	400		(0	1.030 ±	0.01	4)			400			(0.03)	3 ± 0 .	014)											1000	
Voltage (Cap (pF) 100	V) 101	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000	5000	600	630	1000	1500	2000	2500	3000	4000	5000
120									_			_					\vdash		_								
150									_			_					\vdash		_								
180																											
220																											
270																											
330	331																										
390	391																										
470	471																										
560	561																										
680	681																										
750	751																										
820	821																										
1000	102	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
1200	122	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
1500	152	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
1800	182	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
2200		F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
2700	272	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
3300	332	F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
3900		F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
4700		F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
5600		F	F	F	F	F	F	F		F	F	F	F	F	F	G			F	F	F	F	F	F	F		
6800		F	F	F	G	G	G	G		F	F	F	F	F	G	G			F	F	F	F	F	G	G		
8200		F	F	F	G	G	G	G		F	F	F	G	G	G	G			F	F	F	F	F	G	G		
Cap (µF) 0.010		F	F	F	G	G	G	G		F	F	F	G	G	G	G			F	F	F	F	F	G	G		
0.015		F	F	F	G	G	G			F	F	F	G	G	G				F	F	F	G	G	G	G		
0.018		F	F	F	G	G				F	F	F	G	G	G				F	F	F	G	G	G			
0.022		F	F	F	G	G				F	F	F	G	G					F	F	F	G	G	G			
0.027	273	F	F	F	G					F	F	F	G	G					F	F	F	G	G				
0.033		F	F	F	G					F	F	F	G						F	F	F	G	G	_			
0.039		F	F	F	G			<u> </u>		F	F	F	G			_	_		F	F	F	G		-			
0.047	473	F	F	F	Р		\vdash			F	F	F	G	-		_		<u> </u>	F	F	F	G		-			_
0.056		F	F	F	G					F	F	F	G			-		\vdash	F	F	F	G		-			
0.068		F	F	G	\vdash		\vdash	<u> </u>		F	F	G				-		\vdash	F	F	F	G		-			
0.082		F	F F	G G	\vdash					F	F	G G							F	F	G G	-		-			
0.100		F	F				\vdash			F	F	G		-		-		\vdash	F	F	G			-			
0.150		F	F							F	F	G							F	F	G			1			
0270		F	F							F	F	G							F	F							
0.330		F	F							F	F	<u> </u>				 			F	F		-		<u> </u>			
0.390		F	F				\vdash			F	F	\vdash							F	F				\vdash			
0.470		F	F							F	F								F	F							
0.560		G	G							G	G								F	F							
0.680										G	G								G	G							
0.820		Г																	G	G				1			
1.000																											
Voltage ((V)	600	630	1000	1500	2000	2500	3000	4000	600	630	1000	1500	2000	2500	3000	4000	5000	600	630	1000	1500	2000	2500	3000	4000	5000
Case Siz					18	25								2220									2225				
																		. N	OTE	· Cor	toot	foot		far 5			CI



NOTE: Contact factory for non-specified capacitance values

High Voltage MLC Chip Capacitors









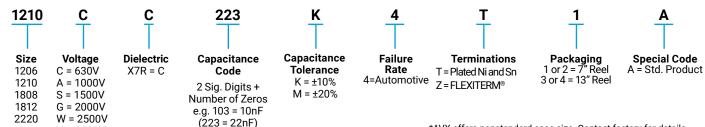
Modern automotive electronics could require components capable to work with high voltage (e.g. xenon lamp circuits or power converters in hybrid cards). AVX offers high voltage ceramic capacitors qualified according to AEC-Q200 standard.

High value, low leakage and small size are diffocult parameters to obtain in cpacitors for high voltage systems. AVX special hgih voltage MLC chip capacitors meet these performance characteristics and are designed for applications such as snubbers in high frequency power converters, resonators in SMPS, and high voltage coupling/dc blocking. These high voltage chip designs exhibit low ESRs at high frequencies.

Due to high voltage nature, larger physical dimensions are necessary. These larger sizes require special precautions to be taken in applying of MLC chips. The temperature gradient during heating or cooling cycles should not exceed 4°C per second. The preheat temperature must be within 50°C of the peak temperature reached by the ceramic bodies through the soldering process. Chip sizes 1210 and larger should be reflow soldered only. Capacitors may require protective surface coating to prevent external arcing.

To improve mechanical and thermal resistance, AVX recommend to use flexible terminations system - FLEXITERM®.

HOW TO ORDER



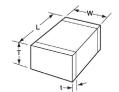
*AVX offers nonstandard case size. Contact factory for details.

Notes: Capacitors with X7R dielectrics are not indeded for applications across AC supply mains or AC line filtering with polarity reversal. Please contact AVX for recommendations

CHIP DIMENSIONS DESCRIPTION

H = 3000V

(See capacitance range chart on page 128)



L = Length W = Width T = Thickness t = Terminal

X7R DIELECTRIC PERFORMANCE CHARACTERISTICS

Parameter/Test	Specification Limits	Measuring Conditions
Operating Temperature Range	-55°C to +125°C	Temperature Cycle Chamber
Capacitance Dissipation Factor Capacitance Tolerance	within specified tolerance 2.5% max. ±5% (J), ±10% (K), ±20% (M)	Freq.: 1kHz ±10% Voltage: 1.0Vrm s ±0.2Vrms T = +25°C, V = 0Vdc
Temperature Characteristics	X7R = ±15%	Vdc = 0V, T = (-55°C to +125°C)
Insulation Resistance	100GΩ min. or 1000MΩ • μF min. (whichever is less) 10GΩ min. or 100MΩ • μF min. (whichever is less)	T = +25°C, V = 500Vdc T = +125°C, V = 500Vdc (t ≥ 120 sec, I ≤ 50mA)
Dielectric Strength	No breakdown or visual defect	120% of rated voltage t ≤ 5 sec, l ≤ 50mA

High Voltage MLC Chips FLEXITERM®



For 600V to 3000V Automotive Applications - AEC-Q200

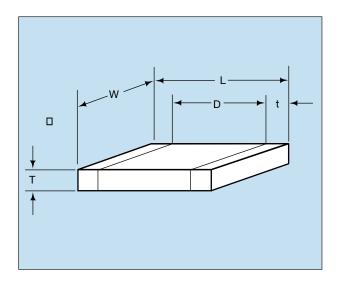
X7R CAPACITANCE RANGE PREFERRED SIZES ARE SHADED

Case	Size			1206				12	10				18	08						1812						2220		
Solde	ering		Ref	flow/V	/ave			Reflo	wOnly		ReflowOnly						Re	flowO	nly				Re	flow0	nly			
(L) Length	mm			.20 ± 0.					± 0.20				4.57 :				4.50 ± 0.30				5.70 ± 0.50							
(L) Length	(in.)			26 ± 0.				(0.126 :)		(± 0.010)					77 ± 0.						24 ± 0.		
W) Width	mm (in.)			.60 ± 0. 163 ± 0.				: 2.50 : 0.098)	± 0.20 + 0.008	6		(2.03 :	± 0.25 ± 0.010)					20 ± 0. 26 + 0.						00 ± 0. 97 ± 0.		
(T) This law as a	mm			1.52	,				70					03			(0.126 ± 0.008) 2.54				3.30							
(T) Thickness	(in.)			(0.060))				067)					080)						(0.100)			(0.130)				
(t) Terminal	mm			25 (0.0					0.010)				0.25 (25 (0.0				0.25 (0.010)				
	max			75 (0.0					0.030)				1.02 (2 (0.0						0.0		
Voltage		630	1000	1500	2000	2500	630	1000	1500	2000	630	1000	1500	2000	2500	3000	630	1000	1500	2000	2500	3000	4000	630	1000	1500	2000	3000
Cap (pF)	100 101																										igspace	\vdash
	120 121																										igwdot	\vdash
	150 151																										$\vdash \vdash \vdash$	\vdash
	180 181 220 221																			_							$\vdash \vdash$	\vdash
																					_	_					$\vdash \vdash \vdash$	\vdash
	270 271 330 331																										$\vdash \vdash$	\vdash
	330 331 390 391																	_	_	_							$\vdash \vdash$	\vdash
	470 471																										\vdash	-
	560 561																										\vdash	\vdash
	680 681																										\vdash	-
	820 821																										$\vdash \vdash$	-
	1000 102																											-
	1200 122																											-
	1500 152																											\vdash
	1800 182																											-
	2200 222																											
	2700 272																											
	3300 332																											-
	3900 392																											
	4700 472																											
	5600 562																					ĺ						
	6800 682																											
	8200 822																											
Cap (µF)	0.01 103																											
	0.012 123																											
	0.015 153																											
	0.018 183		ļ							$oxed{oxed}$																		oxdot
	0.022 223																											\vdash
	0.027 273			_																	_	_					igspace	\blacksquare
	0.033 333		<u> </u>																								igspace	$\vdash \vdash$
	0.039 393		ļ	_	_		_		<u> </u>						<u> </u>						<u> </u>	_					igspace	$\vdash \vdash$
	0.047 473		ļ						<u> </u>												<u> </u>	_					$\sqcup \sqcup$	$\vdash \vdash$
	0.056 563		-															-	_	_	_						$\vdash \vdash$	$\vdash \vdash \vdash$
	0.068 683	-	-	-	-		-	-	 			_			-		-	-			<u> </u>	-					\vdash	$\vdash \vdash$
	0.082 823	-	-	\vdash	-	-	\vdash		<u> </u>						-		\vdash	-	_	_	—	\vdash					$\vdash \vdash$	\vdash
	0.100 104 0.120 124		-	_		-	_		-			-	_		-		-	-	_	_	_	_					\vdash	\vdash
	0.120 124		\vdash	\vdash	<u> </u>	 	\vdash		 						-		\vdash	 	_		\vdash	\vdash					$\vdash \vdash$	\vdash
Voltage		630	1000	1500	2000	2500	630	1000	1500	2000	630	1000	1500	2000	2500	3000	630	1000	1500	2000	2500	3000	4000	630	1000	1500	2000	3000
	Size	000	1000	1206		2000	1000		10		000	7000		08	2000	3000	000	1000	1000	1812		, 5000	1000	000	.000	2220		3000

NOTE: Contact factory for non-specified capacitance values

Part Number Example CDR01 thru CDR06





MILITARY DESIGNATION PER MIL-PRF-55681

MIL Style
Voltage-temperature
Limits
Capacitance
Rated Voltage
Capacitance Tolerance

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

MIL Style: CDR01, CDR02, CDR03, CDR04, CDR05,

CDR06

Voltage Temperature Limits:

BP = 0 ± 30 ppm/°C without voltage; 0 ± 30 ppm/°C with rated voltage from -55°C to +125°C

BX = $\pm 15\%$ without voltage; $\pm 15 - 25\%$ with rated voltage from -55°C to ± 125 °C

Capacitance: Two digit figures followed by multiplier (number of zeros to be added) e.g., 101 = 100 pF

Rated Voltage: A = 50V, B = 100V

Capacitance Tolerance: J \pm 5%, K \pm 10%, M \pm 20%

Termination Finish:

M = Palladium silver

N = Silver-nickel-gold

S = Solder coated final with a minimum of 4 percent lead

T = Silver

Termination Finish
Failure Rate —

U = Base metallization-barrier metal-solder coated (tin/lead alloy, with a minimum of 4 percent lead)

W = Base metallization-barrier metal-tinned (tin or tin/lead alloy)

Y = Base metallization-barrier metal-tin (100 percent)

Z = Base metallization-barrier metal-tinned (tin/lead alloy, with a minimum of 4 percent lead)

*See MIL-PRF-55681 Specification for more details

Failure Rate Level: M = 1.0%, P = .1%, R = .01%,

S = .001%

Packaging: Bulk is standard packaging. Tape and reel per RS481 is available upon request.

*Not RoHS Compliant

CROSS REFERENCE: AVX/MIL-PRF-55681/CDR01 THRU CDR06*

Per	AVX	Length (L)	Width (W)	Thickr	ess (T)		D	Termination Band (t)		
MIL-PRF-55681	Style	Length (L)	wiath (w)	Min.	Max.	Min.	Max.	Min.	Max.	
CDR01	0805	.080 ± .015	.050 ± .015	.022	.055	.030	_	.010	-	
CDR02	1805	.180 ± .015	.050 ± .015	.022	.055	_	_	.010	.030	
CDR03	1808	.180 ± .015	.080 ± .018	.022	.080	_	_	.010	.030	
CDR04	1812	.180 ± .015	.125 ± .015	.022	.080	_	_	.010	.030	
CDR05	1825	.180 + .020 015	.250 + .020 015	.020	.080	_	_	.010	.030	
CDR06	2225	.225 ± .020	.250 ± .020	.020	.080	_	_	.010	.030	

^{*}For CDR11, 12, 13, and 14 see AVX Microwave Chip Capacitor Catalog

Military Part Number Identification CDR01 thru CDR06



CDR01 thru CDR06 to MIL-PRF-55681

Military Type Designation	Capacitance in pF	Capacitance tolerance	Rated temperature and voltage- temperature limits	WVDC
AVX Style 08	05/CDR01			
CDR01BP100B	10	J,K	BP	100
CDR01BP120B	12	J	BP	100
CDR01BP150B	15	J,K	BP	100
CDR01BP180B	18	J	BP	100
CDR01BP220B	22	J,K	BP	100
CDR01BP270B CDR01BP330B	27 33	J	BP BP	100 100
CDR01BP330B	33	J,K J	BP BP	100
CDR01BP470B	47	J,K	BP BP	100
CDR01BP560B	56	J	BP	100
CDR01BP680B	68	J,K	BP	100
CDR01BP820B	82	J	BP	100
CDR01BP101B	100	J,K	BP	100
CDR01B121B	120	J,K	BP,BX	100
CDR01B151B	150	J,K	BP,BX	100
CDR01B181B	180	J,K	BP,BX	100
CDR01BX221B	220	K,M	BX	100
CDR01BX271B	270	K	BX	100
CDR01BX331B CDR01BX391B	330 390	K,M K	BX BX	100 100
CDR01BX391B	470	K,M	BX	
CDR01BX471B	560	K,IVI	BX	100 100
CDR01BX681B	680	K,M	BX	100
CDR01BX821B	820	K	BX	100
CDR01BX102B	1000	K,M	BX	100
CDR01BX122B	1200	К	BX	100
CDR01BX152B	1500	K,M	BX	100
CDR01BX182B	1800	K	BX	100
CDR01BX222B	2200	K,M	BX	100
CDR01BX272B	2700	K	BX	100
CDR01BX332B	3300	K,M	BX	100
CDR01BX392A CDR01BX472A	3900 4700	K K,M	BX BX	50 50
			DA	30
AVX Style 18				ı
CDR02BP221B	220	J,K	BP	100
CDR02BP271B	270	J	BP BV	100 100
CDR02BX392B CDR02BX472B	3900 4700	K K,M	BX BX	100
CDR02BX472B	5600	K,IVI	BX	100
CDR02BX682B	6800	K.M	BX	100
CDR02BX822B	8200	K	BX	100
CDR02BX103B	10,000	K,M	BX	100
CDR02BX123A	12,000	ĸ	BX	50
CDR02BX153A	15,000	K,M	BX	50
CDR02BX183A	18,000	K	BX	50
CDR02BX223A	22,000	K,M	BX	50
	– Add appropriato	e termination fi	nish	
	 Capacitance To 	lerance		

Military Type	Capacitance	Capacitance	Rated temperature and voltage-	WVDC						
Designation/	in pF	tolerance	temperature limits							
AVX Style 18	08/CDR03									
CDR03BP331B	330	J,K	BP	100						
CDR03BP391B CDR03BP471B	390 470	J J,K	BP BP	100 100						
CDR03BP561B	560	J	BP BP	100						
CDR03BP681B	680	J,K	BP	100						
CDR03BP821B	820	J	BP	100						
CDR03BP102B	1000	J,K	BP	100						
CDR03BX123B CDR03BX153B	12,000 15.000	K K,M	BX BX	100 100						
CDR03BX183B	18.000	K	BX	100						
CDR03BX223B	22,000	K,M	BX	100						
CDR03BX273B	27.000	ĸ	BX	100						
CDR03BX333B	33.000	K,M	BX	100						
CDR03BX393A CDR03BX473A	39.000 47.000	K K,M	BX BX	50 50						
CDR03BX563A	56.000	K	BX	50						
CDR03BX5683A	68.000	K,M	BX	50						
AVX Style 18	AVX Style 1812/CDR04									
CDR04BP122B	1200	J	ВР	100						
CDR04BP152B	1500	J,K	BP	100						
CDR04BP182B CDR04BP222B	1800 2200	J	BP BP	100 100						
CDR04BP272B	2700	J,K J	BP	100						
CDR04BP332B	3300	J,K	BP	100						
CDR04BX393B	39.000	K	BX	100						
CDR04BX473B	47.000	K,M	BX	100						
CDR04BX563B CDR04BX823A	56.000 82.000	K K	BX BX	100 50						
CDR04BX104A	100,000	K,M	BX	50						
CDR04BX104A CDR04BX124A	120,000	K	BX	50						
CDR04BX154A	150.000	K,M	BX	50						
CDR04BX184A	180.000	К	BX	50						
AVX Style 18	25/CDR05									
CDR05BP392B	3900	J,K	BP	100						
CDR05BP472B CDR05BP562B	4700 5600	J,K	BP BP	100 100						
CDR05BF302B	68,000	J,K K,M	BX	100						
CDR05BX823B	82,000	K	BX	100						
CDR05BX104B	100,000	K,M	BX	100						
CDR05BX124B	120,000	K	BX	100						
CDR05BX154B	150.000	K,M	BX	100						
CDR05BX224A CDR05BX274A	220.000 270,000	K,M K	BX BX	50 50						
CDR05BX334A	330,000	K,M	BX	50						
AVX Style 22	25/CDR06									
CDR06BP682B	6800	J,K	ВР	100						
CDR06BP822B	8200	J,K	BP	100						
CDR06BP103B	10,000	J,K	BP BV	100						
CDR06BX394A CDR06BX474A	390.000 470.000	K K,M	BX BX	50 50						

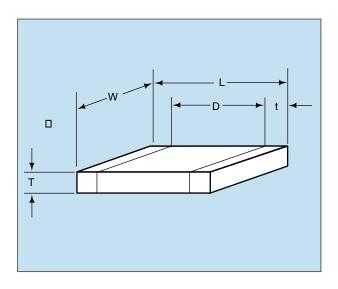
- Add appropriate failure rate

Add appropriate termination finish

- Capacitance Tolerance

Part Number Example CDR31 thru CDR35





MILITARY DESIGNATION PER MIL-PRF-55681

Part Number Example (example) CDR31 BP 101 MIL Style Voltage-temperature Limits Capacitance Rated Voltage Capacitance Tolerance **Termination Finish** Failure Rate

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

MIL Style: CDR31, CDR32, CDR33, CDR34, CDR35

Voltage Temperature Limits:

BP = 0 ± 30 ppm/°C without voltage; 0 ± 30 ppm/°C with rated voltage from -55°C to +125°C

BX = $\pm 15\%$ without voltage; $\pm 15 - 25\%$ with rated voltage from -55°C to +125°C

Capacitance: Two digit figures followed by multiplier (number of zeros to be added) e.g., 101 = 100 pF

Rated Voltage: A = 50V, B = 100V

Capacitance Tolerance: B ± .10 pF, C ± .25 pF, D ± .5

pF, F ± 1%, J ± 5%, K ± 10%,

M ± 20%

Termination Finish:

M = Palladium silver

N = Silver-nickel-gold

S = Solder coated final with a minimum of 4 percent lead

U = Base metallization-barrier metal-solder coated (tin/lead alloy, with a minimum of 4 percent lead)

W = Base metallization-barrier metal-tinned (tin or tin/lead alloy)

Y = Base metallization-barrier metal-tin (100 percent)

Z = Base metallization-barrier metal-tinned (tin/lead alloy, with a minimum of 4 percent lead)

*See MIL-PRF-55681 Specification for more details

Failure Rate Level: M = 1.0%, P = .1%, R = .01%,

S = .001%

Packaging: Bulk is standard packaging. Tape and reel per RS481 is available upon request.

*Not RoHS Compliant

CROSS REFERENCE: AVX/MIL-PRF-55681/CDR31 THRU CDR35

Per AVX Style		Length (L)	Width (W)	Thickness (T)	D	Termination Band (t)		
MIL-PRF-55681	AVX Style	(mm)	(mm)	Max. (mm)	Max. (mm)	Min. (mm)	Max.	
CDR31	0805	2.00	1.25	1.3	.50	.70	.30	
CDR32	1206	3.20	1.60	1.3	_	.70	.30	
CDR33	1210	3.20	2.50	1.5	_	.70	.30	
CDR34	1812	4.50	3.20	1.5	_	.70	.30	
CDR35	1825	4.50	6.40	1.5	_	.70	.30	

Military Part Number Identification CDR32



CDR31 to MIL-PRF-55681/7

AVX Style 08	205/CDD31		temperature limits					
AVX Style 0805/CDR31 (BP)								
CDR31BP1R0B CDR31BP1R2B CDR31BP1R3B CDR31BP1R5B CDR31BP1R6B CDR31BP1R8B CDR31BP2R2B CDR31BP2R4B	1.0 1.1 1.2 1.3 1.5 1.6 1.8 2.0 2.2 2.4	B,C B,C B,C B,C B,C B,C B,C B,C B,C B,C	BP BP BP BP BP BP BP BP BP	100 100 100 100 100 100 100 100 100				
CDR31BP2R7B CDR31BP3R0B CDR31BP3R6B CDR31BP3R9B CDR31BP4R3B CDR31BP4R7B CDR31BP5R1B CDR31BP5R6B CDR31BP5R6B	2.7 3.0 3.3 3.6 3.9 4.3 4.7 5.1 5.6 6.2	B,C,D B,C,D B,C,D B,C,D B,C,D B,C,D B,C,D B,C,D B,C,D B,C,D	BP BP BP BP BP BP BP BP	100 100 100 100 100 100 100 100 100				
CDR31BP6R8B CDR31BP7R5B CDR31BP8R2B CDR31BP9R1B CDR31BP100B	6.8 7.5 8.2 9.1 10	B,C,D B,C,D B,C,D B,C,D FJ,K	BP BP BP BP	100 100 100 100 100				
CDR31BP110B CDR31BP120B CDR31BP130B CDR31BP150B CDR31BP160B	11 12 13 15 16	FJ,K FJ,K FJ,K FJ,K FJ,K	BP BP BP BP	100 100 100 100 100				
CDR31BP180B CDR31BP200B CDR31BP220B CDR31BP240B CDR31BP270B CDR31BP300B CDR31BP330B	18 20 22 24 27 30 33	FJ,K F,J,K FJ,K FJ,K FJ,K FJ,K	BP BP BP BP BP BP	100 100 100 100 100 100				
CDR31BP360B CDR31BP390B CDR31BP430B CDR31BP470B	36 39 43 47	FJ,K F,J,K FJ,K FJ,K	BP BP BP BP	100 100 100 100				
CDR31BP510B CDR31BP560B CDR31BP620B CDR31BP680B CDR31BP750B CDR31BP820B	51 56 62 68 75 82	FJ,K FJ,K FJ,K FJ,K FJ,K FJ,K	BP BP BP BP BP BP	100 100 100 100 100 100				

Add appropriate failure rate
— Add appropriate termination finish
— Capacitance Tolerance

Military Type Designation 1/	Capacitance in pF	Capacitance tolerance	Rated temperature and voltage- temperature limits	WVDC			
AVX Style 08	05/CDR31	1 (BP) cont'd					
CDR31BP101B— CDR31BP111B— CDR31BP121B— CDR31BP131B— CDR31BP151B— CDR31BP161B— CDR31BP161B— CDR31BP201B— CDR31BP221B— CDR31BP241B— CDR31BP271B— CDR31BP231B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B— CDR31BP301B—	100 110 120 130 150 160 180 200 220 240 270 300 330 360 390 430 470	F.J.K F.J.K	BP BP BP BP BP BP BP BP BP BP BP	100 100 100 100 100 100 100 100 100 100			
CDR31BP511A CDR31BP561A CDR31BP621A CDR31BP681A AVX Style 08	510 560 620 680	F,J,K F,J,K F,J,K F,J,K	BP BP BP BP	50 50 50 50 50			
CDR31BX471B— CDR31BX561B— CDR31BX681B— CDR31BX821B— CDR31BX102B— CDR31BX122B— CDR31BX152B— CDR31BX182B— CDR31BX222B— CDR31BX222B— CDR31BX272B— CDR31BX332B—	470 560 680 820 1,000 1,200 1,500 1,800 2,200 2,700 3,300	K,M K,M K,M K,M K,M K,M K,M K,M K,M	BX BX BX BX BX BX BX BX BX BX BX	100 100 100 100 100 100 100 100 100			
CDR31BX392B CDR31BX472B CDR31BX562A CDR31BX822A CDR31BX103A CDR31BX123A CDR31BX153A CDR31BX153A CDR31BX183A	3,900 4,700 5,600 6,800 8,200 10,000 12,000 15,000 18,000	K,M K,M K,M K,M K,M K,M K,M K,M	BX BX BX BX BX BX BX BX BX	100 100 50 50 50 50 50 50 50			

Capacitance Tolerance

^{1/} The complete part number will include additional symbols to indicate capacitance tolerance, termination and failure rate level.

Military Part Number Identification CDR32



CDR32 to MIL-PRF-55681/8

Military Type Designation 1/	Capacitance in pF	Capacitance tolerance	Rated temperature and voltage- temperature limits	WVDC
AVX Style 12	06/CDR32	(BP)		
CDR32BP1R0B	1.0	B,C	BP	100
CDR32BP1R1B	1.1	B,C	BP	100
CDR32BP1R2B	1.2	B,C	BP	100
CDR32BP1R3B	1.3	B,C	BP	100
CDR32BP1R5B	1.5	B,C	BP	100
CDR32BP1R6B	1.6	B,C	BP	100
CDR32BP1R8B	1.8	B,C	BP	100
CDR32BP2R0B	2.0	B,C	BP	100
CDR32BP2R2B	2.2	B,C	BP	100
CDR32BP2R4B	2.4	B,C	BP	100
CDR32BP2R7B	2.7	B,C,D	BP	100
CDR32BP3R0B	3.0	B,C,D	BP	100
CDR32BP3R3B	3.3	B,C,D	BP	100
CDR32BP3R6B	3.6	B,C,D	BP	100
CDR32BP3R9B	3.9	B,C,D	BP	100
CDR32BP4R3B CDR32BP4R7B	4.3 4.7 5.1	B,C,D B,C,D	BP BP	100 100 100
CDR32BP5R1B	5.1	B,C,D	BP	100
CDR32BP5R6B	5.6	B,C,D	BP	100
CDR32BP6R2B	6.2	B,C,D	BP	100
CDR32BP6R8B	6.8	B,C,D	BP	100
CDR32BP7R5B	7.5	B,C,D	BP	100
CDR32BP8R2B	8.2	B,C,D	BP	100
CDR32BP9R1B	9.1	B,C,D	BP	100
CDR32BP100B	10	FJ,K	BP	100
CDR32BP110B	11	F.J.K	BP	100
CDR32BP120B	12	FJ,K	BP	100
CDR32BP130B	13	FJ,K	BP	100
CDR32BP150B	15	FJ,K	BP	100
CDR32BP160B	16	FJ,K	BP	100
CDR32BP180B	18	FJ,K	BP	100
CDR32BP200B	20	F,J,K	BP	100
CDR32BP220B	22	FJ,K	BP	100
CDR32BP240B	24	F,J,K	BP	100
CDR32BP270B	27	FJ,K	BP	100
CDR32BP300B	30	FJ,K	BP	100
CDR32BP330B	33	F,J,K	BP	100
CDR32BP360B	36	FJ,K	BP	100
CDR32BP390B	39	F,J,K	BP	100
CDR32BP430B	43	FJ,K	BP	100
CDR32BP470B	47	FJ,K	BP	100
CDR32BP510B	51	F,J,K	BP	100
CDR32BP560B	56	FJ,K	BP	100
CDR32BP620B CDR32BP680B CDR32BP750B	62 68 75	F,J,K FJ,K	BP BP BP	100 100 100
CDR32BP750B	75	FJ,K	Bb	100
CDR32BP820B	82	F,J,K	Bb	100
CDR32BP910B	91	FJ,K	Bb	100

Add appropriate failure rate
— Add appropriate termination finish
Capacitance Tolerance

Military Type Designation 1/	Capacitance in pF	Capacitance tolerance	Rated temperature and voltage- temperature limits	WVDC
AVX Style 08	05/CDR31	(BP) con	t'd	
CDR32BP101B	100	FJ,K	BP	100
CDR32BP111B	110	FJ,K	BP	100
CDR32BP121B	120	FJ,K	BP	100
CDR32BP131B	130	FJ,K	BP	100
CDR32BP151B	150	FJ,K	BP	100
CDR32BP161B	160	FJ,K	BP	100
CDR32BP181B	180	F,J,K	BP	100
CDR32BP201B	200	FJ,K	BP	100
CDR32BP221B	220	F,J,K	BP	100
CDR32BP241B	240	FJ,K	BP	100
CDR32BP271B	270	FJ.K	BP	100
CDR32BP301B	300	F,J,K	BP BP	100
CDR32BP331B	330	FJ,K	BP BP	100
CDR32BP361B	360	F,J,K	BP	100
CDR32BP391B	390	FJ,K	BP BP	100
CDR32BP431B	430	FJ,K	BP	100
CDR32BP471B	470	F,J,K	BP	100
CDR32BP511B	510	FJ,K	BP	100
CDR32BP561B	560	F,J,K	BP	100
CDR32BP621B	620	FJ,K	BP	100
CDR32BP681B	680	FJ,K	BP	100
CDR32BP751B	750	F,J,K	BP	100
CDR32BP821B	820	FJ,K	BP	100
CDR32BP911B	910	F,J,K	BP	100
CDR32BP102B	1,000	FJ,K	BP	100
CDR32BP112A	1,100	FJ,K	BP	50
CDR32BP122A	1,200	F,J,K	BP	50
CDR32BP132A	1,300	FJ,K	BP	50
CDR32BP152A	1,500	F,J,K	BP	50
CDR32BP162A	1,600	FJ,K	BP	50
CDR32BP182A	1.800	FJ.K	BP	50
CDR32BP202A	2,000	F,J,K	BP	50
CDR32BP222A	2,200	FJ,K	BP	50
AVX Style 12	06/CDR32	(BX)		
CDR32BX472B	4,700	K,M	BX	100
CDR32BX562B	5,600	K,M	BX	100
CDR32BX682B	6,800	K,M	BX	100
CDR32BX822B	8,200	K,M	BX	100
CDR32BX103B	10,000	K,M	BX	100
CDR32BX123B	12,000	K,M	BX	100
CDR32BX153B	15.000	K,M	BX	100
CDR32BX183A	18.000	K,M	BX	50
CDR32BX223A	22,000	K,M	BX	50
CDR32BX273A	27,000	K,M	BX	50
CDR32BX333A	33.000	K.M	BX	50
CDR32BX393A	39.000	K,M	BX	50

1/ The complete part number will include additional symbols to indicate capacitance tolerance, termination and failure rate level.

Add appropriate termination finish

Capacitance Tolerance





CDR33/34/35 to MIL-PRF-55681/9/10/11

Military Type Designation 1 /	Capacitance in pF	Capacitance tolerance	Rated temperature and voltage- temperature limits	WVDC					
AVX Style 12	210/CDR33	(BP)	. •						
CDR33BP102B	1,000	FJ,K	BP	100					
CDR33BP112B	1,100	FJ,K	BP	100					
CDR33BP122B	1,200	FJ,K	BP	100					
CDR33BP132B	1,300	FJ,K	BP	100					
CDR33BP152B	1,500	FJ,K	BP	100					
CDR33BP162B	1,600	FJ,K	BP	100					
CDR33BP182B	1,800	F,J,K	BP	100					
CDR33BP202B	2,000	FJ,K	BP	100					
CDR33BP222B	2,200	F,J,K	BP	100					
CDR33BP242A	2,400	FJ,K	BP	50					
CDR33BP272A	2,700	FJ,K	BP	50					
CDR33BP302A	3,000	F,J,K	BP	50					
CDR33BP332A	3,300	FJ,K	BP	50					
AVX Style 12	AVX Style 1210/CDR33 (BX)								
CDR33BX153B	15.000	K,M	BX	100					
CDR33BX183B	18.000	K,M	BX	100					
CDR33BX223B	22,000	K,M	BX	100					
CDR33BX273B	27.000	K,M	BX	100					
CDR33BX393A	39.000	K,M	BX	50					
CDR33BX473A	47.000	K,M	BX	50					
CDR33BX563A	56.000	K,M	BX	50					
CDR33BX683A	68.000	K,M	BX	50					
CDR33BX823A	82,000	K,M	BX	50					
CDR33BX104A	100,000	K,M	BX	50					
AVX Style 18	312/CDR34	(BP)							
CDR34BP222B	2,200	FJ,K	BP	100					
CDR34BP242B	2,400	F,J,K	BP	100					
CDR34BP272B	2,700	FJ,K	BP	100					
CDR34BP302B	3,000	F,J,K	BP	100					
CDR34BP332B	3,300	FJ,K	BP	100					
CDR34BP362B	3,600	FJ,K	BP	100					
CDR34BP392B	3,900	F,J,K	BP	100					
CDR34BP432B	4,300	FJ,K	BP	100					
CDR34BP472B	4,700	F,J,K	BP	100					
CDR34BP512A	5,100	FJ,K	BP	50					
CDR34BP562A	5,600	FJ,K	BP	50					
CDR34BP622A	6,200	F,J,K	BP	50					
CDR34BP682A	6,800	FJ,K	BP	50					
CDR34BP752A	7,500	F,J,K	BP	50					
CDR34BP822A	8,200	FJ,K	BP	50					
CDR34BP912A	9,100	FJ,K	BP	50					
CDR34BP103A	10,000	F,J,K	BP	50					
	– Add appropriate	e failure rate							

Add appropriate termination finish

Capacitance Tolerance

Military Type Designation 1/	Capacitance in pF	Capacitance tolerance	Rated temperature and voltage- temperature limits	WVDC
AVX Style 18	12/CDR34	(BX)		
CDR34BX273B CDR34BX333B CDR34BX393B CDR34BX473B CDR34BX563B CDR34BX104A CDR34BX124A CDR34BX154A CDR34BX184A	27.000 33.000 39.000 47.000 56.000 100,000 120,000 150.000 180.000	K,M K,M K,M K,M K,M K,M K,M	BX BX BX BX BX BX BX BX BX	100 100 100 100 100 50 50 50
AVX Style 18	25/CDR35	(BP)		
CDR35BP472B— CDR35BP562B— CDR35BP62B— CDR35BP622B— CDR35BP682B— CDR35BP822B— CDR35BP912B— CDR35BP912B— CDR35BP133A— CDR35BP13A— CDR35BP133A— CDR35BP153A— CDR35BP163A— CDR35BP163A— CDR35BP183A— CDR35BP183A— CDR35BP183A— CDR35BP183A— CDR35BP183A— CDR35BP183A— CDR35BP203A— CDR35BP203A—	4,700 5,100 5,600 6,200 6,800 7,500 8,200 9,100 11,000 12,000 13,000 15,000 16,000 18,000 20,000 22,000	£1,K £1,K £1,K £1,K £1,K £1,K £1,K £1,K	BP BP BP BP BP BP BP BP BP BP BP BP BP B	100 100 100 100 100 100 100 100 50 50 50 50 50 50 50
AVX Style 18	325/CDR35	(BX)		
CDR35BX563B— CDR35BX683B— CDR35BX823B— CDR35BX104B— CDR35BX124B— CDR35BX154B— CDR35BX184A— CDR35BX224A— CDR35BX274A— CDR35BX334A— CDR35BX394A— CDR35BX394A— CDR35BX474A—	56.000 68.000 82,000 100,000 120,000 150.000 220,000 270.000 390.000 470.000	K,M K,M K,M K,M K,M K,M K,M K,M K,M	BX BX BX BX BX BX BX BX BX BX	100 100 100 100 100 100 50 50 50 50

- Add appropriate failure rate

- Add appropriate termination finish

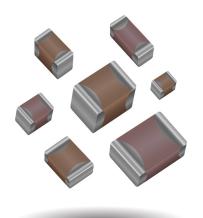
- Capacitance Tolerance

^{1/} The complete part number will include additional symbols to indicate capacitance tolerance, termination and failure rate level.

MLCC Medical Applications – MM Series

General Specifications





The AVX MM series is a multi-layer ceramic capacitor designed for use in medical applications other than implantable/life support. These components have the design & change control expected for medical devices and also offer enhanced LAT including reliability testing and 100% inspection.

APPLICATIONS

Implantable, Non-Life Supporting Medical Devices

· e.g. implanted temporary cardiac monitor, insulin pumps

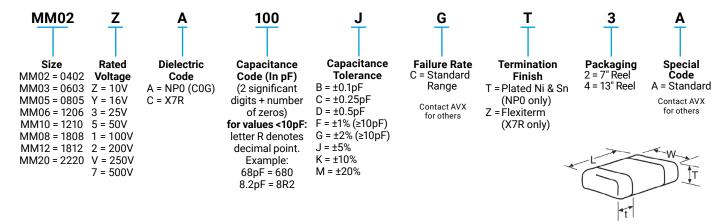
External, Life Supporting Medical Devices

· e.g. heart pump external controller

External Devices

· e.g. patient monitoring, diagnostic equipment

HOW TO ORDER



COMMERCIAL VS MM SERIES PROCESS COMPARISON

	Commercial	MM Series				
Administrative	Standard part numbers; no restriction on who purchases these parts	Specific series part number, used to control supply of product				
Design	Minimum ceramic thickness of 0.020" on all X7R product	Minimum ceramic thickness of 0.029" (0.74mm)				
Dicing	Side & end margins = 0.003" min	Side & end margins = 0.004" min Cover layers = 0.003" min				
Lot Qualification Destructive Physical Analysis (DPA)	As per EIA RS469	Increased sample plan – stricter criteria				
Visual/Cosmetic Quality	Standard process and inspection	100% inspection				
Application Robustness	Standard sampling for accelerated wave solder on X7R dielectrics	Increased sampling for accelerated wave solder o X7R and NP0 followed by lot by lot reliability testin				
Design/Change Control	Required to inform customer of changes in:	AVX will qualify and notify customers before making any change to the following materials or processes: • Dielectric formulation, type, or supplier • Metal formulation, type, or supplier • Termination material formulation, type, or supplier • Manufacturing equipment type • Quality testing regime including sample size and accept/ reject criteria				





Parame	ter/Test	NP0 Specification Limits	Measuring Conditions				
	perature Range	-55°C to +125°C	Temperature C				
•	itance Q	Within specified tolerance <30 pF: Q≥ 400+20 x Cap Value ≥30 pF: Q≥ 1000	Freq.: 1.0 MHz ± 10% 1.0 kHz ± 10% fo Voltage: 1.0'	r cap > 1000 pF			
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with 60 ± 5 secs @ rooi	rated voltage for n temp/humidity			
Dielectric	: Strength	No breakdown or visual defects	Charge device with 300 1-5 seconds, w/charge limited to 50 Note: Charge device voltage for 50	and discharge current mA (max) with 150% of rated			
	Appearance	No defects	Deflectio	n: 2mm			
Resistance to	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 3				
Flexure Stresses	Q	Meets Initial Values (As Above)	l				
	Insulation Resistance	≥ Initial Value x 0.3	90 r				
Solde	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 \pm 5 for 5.0 \pm 0.5 seconds				
	Appearance	No defects, <25% leaching of either end terminal					
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater					
Resistance to Solder Heat	Q	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room	temperature for 24 ± 2			
	Insulation Resistance	Meets Initial Values (As Above)	hours before measuring	j electrical properties.			
	Dielectric Strength	Meets Initial Values (As Above)					
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes			
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp	≤ 3 minutes			
Thermal Shock	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes			
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes			
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 hours at roor	and measure after n temperature			
	Appearance	No visual defects					
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twic chamber set at				
Load Life	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	for 1000 hou Remove from test cha	rs (+48, -0).			
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	room temperatu before me	re for 24 hours			
	Dielectric Strength	Meets Initial Values (As Above)	before measuring.				
	Appearance	No visual defects					
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater	Store in a test chamber set at 85°C ± 2°C/				
Load Humidity	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied. Remove from chamber and stabilize a				
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)					
Dielectric Strength		Meets Initial Values (As Above)		J			





PREFERRED SIZES ARE SHADED

SIZE			06	03				0805				1206	
	WVDC	16	25	50	100	16	25	50	100	16	25	50	100
Cap 0.5	0R5			- 00	100				100			- 00	.00
(pF) 1.0	1R0												
1.2	1R2												
1.5	1R5												
1.8	1R8												
2.2	2R2												
2.7	2R7												
3.3	3R3												
3.9	3R9												
4.7	4R7												
5.6	5R6												
6.8	6R8												
8.2	8R2												
10	100												
12	120												
15	150												
18	180												
22	220												
27	270												
33	330												
39	390												
47	470												
56	560												
68	680												
82	820												
100	101												
120	121												
150	151												
180 220	181 221				-								
270	271				-								
330	331				 								
390	391												
470	471				-								
560	561												
680	681												
820	821												
1000	102												
1200	122												
1500	152												
WVD		16	16 25 50 100			16	25	50	16	25	50	100	
SIZE				03				0805	100		1206		





Parame	ter/Test	X7R Specification Limits	Measuring (Conditions	
Operating Tem	perature Range	-55°C to +125°C	Temperature C	ycle Chamber	
Capac	itance	Within specified tolerance			
(Ď	≤ 10% for ≥ 50V DC rating ≤ 12.5% for 25V DC rating ≤ 12.5% for 25V and 16V DC rating ≤ 12.5% for ≤ 10V DC rating	Freq.: 1.0 k Voltage: 1.0'		
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with rate secs @ room to		
Dielectric	: Strength	No breakdown or visual defects	Charge device with 300 1-5 seconds, w/charge limited to 50 Note: Charge device voltage for 50	and discharge current mA (max) with 150% of rated	
	Appearance	No defects	Deflectio		
Resistance to	Capacitance Variation	≤ ±12%	Test Time: 3		
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)	V		
	Insulation Resistance	≥ Initial Value x 0.3	90 r	nm —	
Solde	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic for 5.0 ± 0.5		
	Appearance	No defects, <25% leaching of either end terminal			
	Capacitance Variation	≤ ±7.5%			
Resistance to Solder Heat	Dissipation Factor	Meets Initial Values (As Above)	Dip device in eutectic solder at 260°C for seconds. Store at room temperature for 2		
Joidel Heat	Insulation Resistance	Meets Initial Values (As Above)	hours before measuring electrical p		
	Dielectric Strength	Meets Initial Values (As Above)			
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes	
	Capacitance Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes	
Thermal Shock	Dissipation Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes	
Gildon	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes	
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles 24 ± 2 hours at ro		
	Appearance	No visual defects			
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 r test chamber set		
Load Life	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	for 1000 hou	,	
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test cha room temperature for	24 ± 2 hours before	
	Dielectric Strength	Meets Initial Values (As Above)	meası	g.	
	Appearance	No visual defects			
	Capacitance Variation	≤ ±12.5%	Store in a test chamber : ± 5% relative humid		
Load	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated	I voltage applied.	
Humidity	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature an	d humidity for	
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours bef	ore measuring.	





PREFERRED SIZES ARE SHADED

	SIZE	•	(040	12			0	603	}				(080	5						12	06							12	10				18	808	3		18	12			222	0
		WVDC	16	25	50	10	16	25	50	10	0 200	10	16	25	50	100	200	250	10	16	25	50	100	200	250	500	10	16	25	50	100	200 2	250 5	00	50	100	200	50	100	200	250	25	50	100
Сар	220	221							i i							İ									П						П	\neg		T			\Box		П	П		Г	-	
(pF)	270	271			T		\top	T	1	1	1	1	T	т		T									П						П	十	\neg	寸	\neg		П		т	г	г	\Box	\vdash	Г
u /	330	331			T		\top	T	1	1	1	1	т												П						П	一十	\neg	寸	\neg		П		т	г	г	\Box	\vdash	Г
\neg	390	391							1	+	+	1	1								<u> </u>		t								Ħ	一		\neg	\neg		П		т	г	т	\Box	-	
\neg	470	471							1	+	+	1											t						t		Ħ	一		\neg	\neg		H		т	г		П	-	
	560	561						T	1	+	+	1	1												П						\Box	\neg	\neg	7	\neg		Н		т	г	т	П	\vdash	
	680	681						1	1	+	_	1	1												H						\Box	\neg	\neg	_			\vdash		т	г	т	-	\vdash	
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\dashv	6800	682			+	┢	+	╁	+	+			+	-	-		\vdash		\vdash	\vdash	-		+		\vdash		-	-	\vdash	\vdash	\vdash	\rightarrow	+	-	\dashv	\dashv	\vdash		\vdash	⊢	₩	\vdash	₩	\vdash
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Packaging of Chip Components

Automatic Insertion Packaging

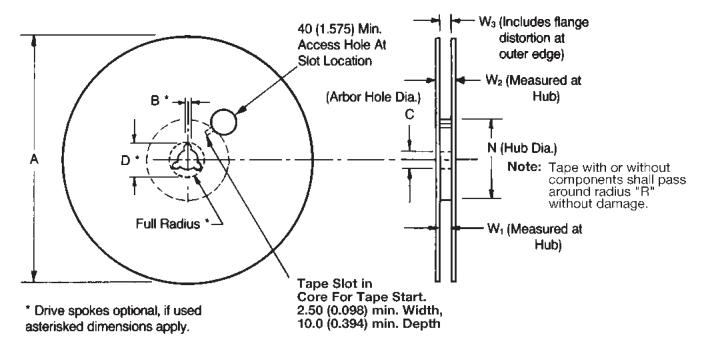


TAPE & REEL QUANTITIES

All tape and reel specifications are in compliance with RS481.

	4mm	8mm	12mm	
Paper or Embossed Carrier		0612, 0508, 0805, 1206, 1210		
Embossed Only	0101		1808	1812, 1825 2220, 2225
Paper Only		0101, 0201, 0306, 0402, 0603		
Qty. per Reel/7" Reel	4,000	1,000, 2,000, 3,000 or 4,000, 10,000, 15,000, 20,000 Contact factory for exact quantity	3,000	500, 1,000 Contact factory for exact quantity
Qty. per Reel/13" Reel		5,000, 10,000, 50,000 Contact factory for exact quantity	10,000	4,000

REEL DIMENSIONS



Tape Size ⁽¹⁾	A Max.	B* Min.	С	D* Min.	N Min.	W ₁	W ₂ Max.	W ₃
4mm	1.80 (7.087)	1.5 (0.059)	13.0±0.5 (0.522±0.020)	20.2 (0.795)	60.0 (2.362)	4.35±0.3 (0.171±0.011)	7.95 (0.312)	
8mm	330	1.5	13.0 ^{+0.50}	20.2	50.0	8.40 ^{+1.5} (0.331 ^{+0.059})	14.4 (0.567)	7.90 Min. (0.311) 10.9 Max. (0.429)
12mm	(12.992)	(0.059)	(0.512 +0.020)	(0.795)	(1.969)	12.4 ^{+2.0} _{-0.0} (0.488 ^{+0.079})	18.4 (0.724)	11.9 Min. (0.469) 15.4 Max. (0.607)

Metric dimensions will govern.

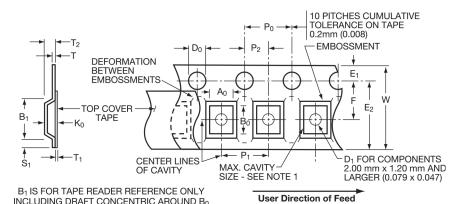
English measurements rounded and for reference only.

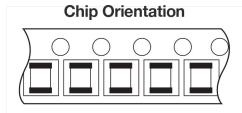
(1) For tape sizes 16mm and 24mm (used with chip size 3640) consult EIA RS-481 latest revision.

Embossed Carrier Configuration

4, 8 & 12mm Tape Only







4, 8 & 12mm Embossed Tape Metric Dimensions Will Govern

CONSTANT DIMENSIONS

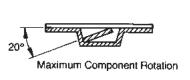
Tape Size	D _o	E ₁	P ₀	P ₂	S ₁ Min.	T Max.	T₁ Max.
1mm	0.80±0.04	0.90±0.05	2.0±0.04	1.00±0.02	1.075	0.26	0.06
4mm	(0.031±0.001)	(0.035±0.001)	(0.078±0.001)	(0.039±0.0007)	(0.042)	(0.010)	(0.002)
8mm	1.50 +0.10	1.75 ± 0.10	4.0 ± 0.10	2.0 ± 0.05	0.60	0.60	0.10
& 12mm	$(0.059^{+0.004}_{-0.0})$	(0.069 ± 0.004)	(0.157 ± 0.004)	(0.079 ± 0.002)	(0.024)	(0.024)	(0.004)

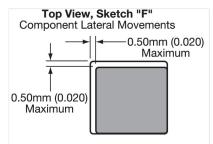
VARIABLE DIMENSIONS

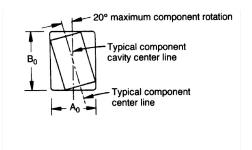
Tape Size	B ₁ Max.	D ₁ Min.	E ₂ Min.	F	P ₁ See Note 5	R Min. See Note 2	T ₂	W Max.	A ₀ B ₀ K ₀
8mm	4.35 (0.171)	1.00 (0.039)	6.25 (0.246)	3.50 ± 0.05 (0.138 ± 0.002)	4.00 ± 0.10 (0.157 ± 0.004)	25.0 (0.984)	2.50 Max. (0.098)	8.30 (0.327)	See Note 1
12mm	8.20 (0.323)	1.50 (0.059)	10.25 (0.404)	5.50 ± 0.05 (0.217 ± 0.002)	4.00 ± 0.10 (0.157 ± 0.004)	30.0 (1.181)	6.50 Max. (0.256)	12.3 (0.484)	See Note 1
8mm 1/2 Pitch	4.35 (0.171)	1.00 (0.039)	6.25 (0.246)	3.50 ± 0.05 (0.138 ± 0.002)	2.00 ± 0.10 (0.079 ± 0.004)	25.0 (0.984)	2.50 Max. (0.098)	8.30 (0.327)	See Note 1
12mm Double Pitch	8.20 (0.323)	1.50 (0.059)	10.25 (0.404)	5.50 ± 0.05 (0.217 ± 0.002)	8.00 ± 0.10 (0.315 ± 0.004)	30.0 (1.181)	6.50 Max. (0.256)	12.3 (0.484)	See Note 1

NOTES:

- The cavity defined by A0, B0, and K0 shall be configured to provide the following: Surround the component with sufficient clearance such that:
- b) the component does not protrude beyond the sealing plane of the cover tape.
- c) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the cover tape has been removed.
- d) rotation of the component is limited to 20° maximum (see Sketches D & E).
- e) lateral movement of the component is restricted to 0.5mm maximum (see Sketch F).
- 2. Tape with or without components shall pass around radius "R" without damage.
- Bar code labeling (if required) shall be on the side of the reel opposite the round sprocket holes. Refer to EIA-556.
- 4. B₁ dimension is a reference dimension for tape feeder clearance only.
- 5. If P_1 = 2.0mm, the tape may not properly index in all tape feeders.





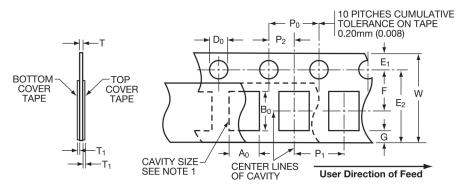




Paper Carrier Configuration

8 & 12mm Tape Only





4, 8 & 12mm Embossed Tape Metric Dimensions Will Govern

CONSTANT DIMENSIONS

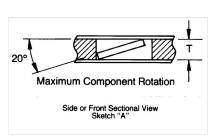
Tape Size	D _o	E	P ₀	P ₂	T ₁	G. Min.	R Min.
8mm	1.50 +0.10	1.75 ± 0.10	4.00 ± 0.10	2.00 ± 0.05	0.10	0.75	25.0 (0.984)
and	$(0.059^{+0.004}_{-0.0})$	(0.069 ± 0.004)	(0.157 ± 0.004)	(0.079 ± 0.002)	` ′	(0.030)	See Note 2
12mm					Max.	Min.	Min.

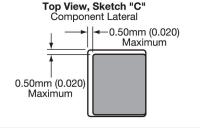
VARIABLE DIMENSIONS

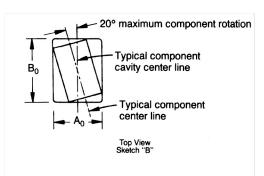
Tape Size	P ₁ See Note 4	E ₂ Min.	F	w	$A_0 B_0$	Т
8mm	4.00 ± 0.10 (0.157 ± 0.004)	6.25 (0.246)	3.50 ± 0.05 (0.138 ± 0.002)	8.00 ±0.30 (0.315±0.012)	See Note 1	1.10mm (0.043) Max.
12mm	4.00 ± 0.10 (0.157 ± 0.004)	10.25 (0.404)	5.50 ± 0.05 (0.217 ± 0.002)	12.0 ± 0.30 (0.472 ± 0.012)		for Paper Base Tape and
8mm 1/2 Pitch	2.00 ± 0.05 (0.079 ± 0.002)	6.25 (0.246)	3.50 ± 0.05 (0.138 ± 0.002)	8.00 ^{+0.30} _{-0.10} (0.315 ^{+0.012} _{-0.004})		1.60mm (0.063) Max. for Non-
12mm Double Pitch	8.00 ± 0.10 (0.315 ± 0.004)	10.25 (0.404)	5.50 ± 0.05 (0.217 ± 0.002)	12.0 ± 0.30 (0.472 ± 0.012)		Paper Base Compositions

NOTES

- 1. The cavity defined by A0, B0, and T shall be configured to provide sufficient clearance surrounding the component so that:
 - a) the component does not protrude beyond either surface of the carrier tape;
 - b)) the component can be removed from the cavity in a vertical direction without mechanical restriction after the top cover tape has been removed;
- c) rotation of the component is limited to 20° maximum (see Sketches A $\&\,B);$
- d) lateral movement of the component is restricted to 0.5mm maximum (see Sketch C).
- 2. Tape with or without components shall pass around radius "R" without damage.
- Bar code labeling (if required) shall be on the side of the reel opposite the sprocket holes. Refer to EIA-556.
- 4. If P_1 = 2.0mm, the tape may not properly index in all tape feeders.







Bar Code Labeling Standard

AVX bar code labeling is available and follows latest version of EIA-556





I. Capacitance (farads)

English:
$$C = \frac{.224 \text{ K A}}{T_D}$$

Metric: $C = \frac{.0884 \text{ K A}}{T_D}$

II. Energy stored in capacitors (Joules, watt - sec)

$$E = \frac{1}{2} CV^2$$

III. Linear charge of a capacitor (Amperes)

$$I=C \ \frac{dV}{dt}$$

IV. Total Impedance of a capacitor (ohms)

$$Z = \sqrt{R_S^2 + (X_C - X_L)^2}$$

V. Capacitive Reactance (ohms)

$$x_C = \frac{1}{2 \pi fC}$$

VI. Inductive Reactance (ohms)

$$x_L = 2 \pi fL$$

VII. Phase Angles:

Ideal Capacitors: Current leads voltage 90° Ideal Inductors: Current lags voltage 90° Ideal Resistors: Current in phase with voltage

VIII. Dissipation Factor (%)

D.F.=
$$\tan \delta$$
 (loss angle) = $\frac{\text{E.S.R.}}{\text{X}_{\text{C}}}$ = (2 π fC) (E.S.R.)

IX. Power Factor (%)

P.F. = Sine (loss angle) = $\cos \varphi$ (phase angle)

P.F. = (when less than 10%) = DF

X. Quality Factor (dimensionless)

Q = Cotan
$$\delta$$
 (loss angle) = $\frac{1}{D}$ F

XI. Equivalent Series Resistance (ohms)

E.S.R. = (D.F.) (Xc) = (D.F.) / (2
$$\pi$$
 fC)

XII. Power Loss (watts)

Power Loss = $(2 \pi fCV^2)$ (D.F.)

XIII. KVA (Kilowatts)

$$KVA = 2 \pi fCV^2 \times 10^{-3}$$

XIV. Temperature Characteristic (ppm/°C)

T.C. =
$$\frac{Ct - C_{25}}{C_{25} (T_t - 25)} \times 10^6$$

XV. Cap Drift (%)

C.D. =
$$\frac{C_1 - C_2}{C_1}$$
 x 100

XVI. Reliability of Ceramic Capacitors

$$\begin{array}{c} L_{o} = \left(\frac{V_{t}}{V_{o}}\right)^{X} & \left(\frac{T_{t}}{T_{o}}\right)^{-Y_{t}} \end{array}$$

XVII. Capacitors in Series (current the same)

Any Number:
$$\frac{1}{C_T} = \frac{1}{C_1} + \frac{1}{C_2} - \frac{1}{C_N}$$
 Two: $C_T = \frac{C_1 C_2}{C_1 + C_2}$

XVIII. Capacitors in Parallel (voltage the same)

$$C_T = C_1 + C_2 --- + C_N$$

XIX. Aging Rate

A.R. = $\%\Delta$ C/decade of time

XX. Decibels

$$db = 20 \log \frac{V_1}{V_2}$$

METRIC PREFIXES

Pico	X 10 ⁻¹²
Nano	X 10 ⁻⁹
Micro	X 10 ⁻⁶
Milli	X 10 ⁻³
Deci	X 10 ⁻¹
Deca	X 10 ⁺¹
Kilo	X 10 ⁺³
Mega	X 10 ⁺⁶
Giga	X 10 ⁺⁹
Tera	X 10 ⁺¹²

SYMBOLS

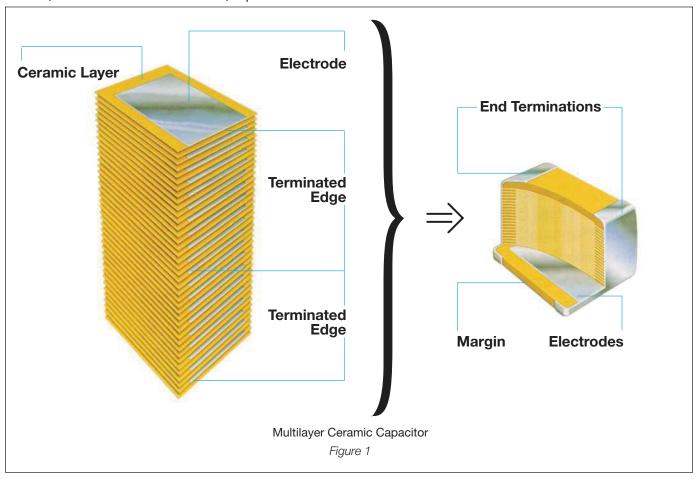
К	= Dielectric Constant	f	= frequency	Lŧ	= Test life
А	= Area	L	= Inductance	V_{t}	= Test voltage
T _D	= Dielectric thickness	δ	= Loss angle	V _o	= Operating voltage
V	= Voltage	φ	= Phase angle	T _t	= Test temperature
t	= time	X & Y	= exponent effect of voltage and temp.	T _o	= Operating temperature
R _s	= Series Resistance	L _o	= Operating life		





Basic Construction – A multilayer ceramic (MLC) capacitor is a monolithic block of ceramic containing two sets of offset, interleaved planar electrodes that extend to two opposite surfaces of the ceramic dielectric. This simple structure requires a considerable amount of sophistication, both in material and manufacture, to produce it in the

quality and quantities needed in today's electronic equipment.



Formulations – Multilayer ceramic capacitors are available in both Class 1 and Class 2 formulations. Temperature compensating formulation are Class 1 and temperature stable and general application formulations are classified as Class 2.

Class 1 – Class 1 capacitors or temperature compensating capacitors are usually made from mixtures of titanates where barium titanate is normally not a major part of the mix. They have predictable temperature coefficients and in general, do not have an aging characteristic. Thus they are the most stable capacitor available. The most popular Class 1 multilayer ceramic capacitors are COG (NPO) temperature compensating capacitors (negative-positive 0 ppm/°C).

Class 2 – EIA Class 2 capacitors typically are based on the chemistry of barium titanate and provide a wide range of capacitance values and temperature stability. The most commonly used Class 2 dielectrics are X7R and Y5V. The X7R provides intermediate capacitance values which vary only ±15% over the temperature range of -55°C to 125°C. It finds applications where stability over a wide temperature range is required.

The Y5V provides the highest capacitance values and is used in applications where limited temperature changes are expected. The capacitance value for Y5V can vary from 22% to -82% over the -30°C to 85°C temperature range.

All Class 2 capacitors vary in capacitance value under the influence of temperature, operating voltage (both AC and DC), and frequency. For additional information on performance changes with operating conditions, consult AVX's software, SpiCap.





Table 1: EIA and MIL Temperature Stable and General Application Codes

EIA CODE Percent Capacity Change Over Temperature Range			
RS198	Temperature Range		
X7 X6 X5 Y5 Z5	-55°C to +125°C -55°C to +105°C -55°C to +85°C -30°C to +85°C +10°C to +85°C		
Code	Percent Capacity Change		
D E F P R S T U V	±3.3% ±4.7% ±7.5% ±10% ±15% ±22% +22%, -33% +22%, - 56% +22%, -82%		

EXAMPLE – A capacitor is desired with the capacitance value at 25°C to increase no more than 7.5% or decrease no more than 7.5% from -30°C to +85°C. EIA Code will be Y5F.

MIL CODE				
Symbol	mbol Temperature Range			
A B C	-55°C to +85°C -55°C to +125°C -55°C to +150°C			
Symbol	Cap. Change Zero Volts	Cap. Change Rated Volts		
R S W X Y Z	+15%, -15% +22%, -22% +22%, -56% +15%, -15% +30%, -70% +20%, -20%	+15%, -40% +22%, -56% +22%, -66% +15%, -25% +30%, -80% +20%, -30%		

Temperature characteristic is specified by combining range and change symbols, for example BR or AW. Specification slash sheets indicate the characteristic applicable to a given style of capacitor.

In specifying capacitance change with temperature for Class 2 materials, EIA expresses the capacitance change over an operating temperature range by a 3 symbol code. The first symbol represents the cold temperature end of the temperature range, the second represents the upper limit of the operating temperature range and the third symbol represents the capacitance change allowed over the operating temperature range. Table 1 provides a detailed explanation of the EIA system.

Effects of Voltage – Variations in voltage have little effect on Class 1 dielectric but does affect the capacitance and dissipation factor of Class 2 dielectrics. The application of DC voltage reduces both the capacitance and dissipation factor while the application of an AC voltage within a reasonable range tends to increase both capacitance and dissipation factor readings. If a high enough AC voltage is applied, eventually it will reduce capacitance just as a DC voltage will. Figure 2 shows the effects of AC voltage.

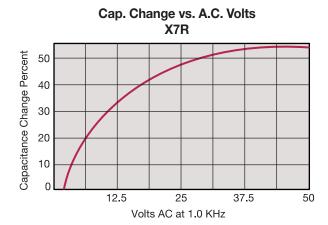


Figure 2

Capacitor specifications specify the AC voltage at which to measure (normally 0.5 or 1 VAC) and application of the wrong voltage can cause spurious readings. Figure 3 gives the voltage coefficient of dissipation factor for various AC voltages at 1 kilohertz. Applications of different frequencies will affect the percentage changes versus voltages.

D.F. vs. A.C. Measurement Volts X7R

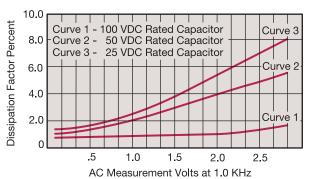
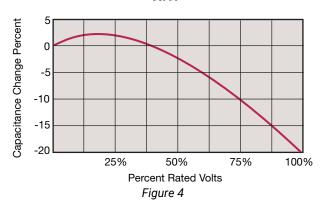


Figure 3

Typical effect of the application of DC voltage is shown in Figure 4. The voltage coefficient is more pronounced for higher K dielectrics. These figures are shown for room temperature conditions. The combination characteristic known as voltage temperature limits which shows the effects of rated voltage over the operating temperature range is shown in Figure 5 for the military BX characteristic.



Typical Cap. Change vs. D.C. Volts X7R



Typical Cap. Change vs. Temperature X7R

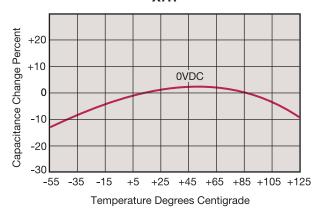


Figure 5

Effects of Time – Class 2 ceramic capacitors change capacitance and dissipation factor with time as well as temperature, voltage and frequency. This change with time is known as aging. Aging is caused by a gradual re-alignment of the crystalline structure of the ceramic and produces an exponential loss in capacitance and decrease in dissipation factor versus time. A typical curve of aging rate for semistable ceramics is shown in Figure 6.

If a Class 2 ceramic capacitor that has been sitting on the shelf for a period of time, is heated above its curie point, (125°C for 4 hours or 150°C for 1/2 hour will suffice) the part will de-age and return to its initial capacitance and dissi-pation factor readings. Because the capacitance changes rapidly, immediately after deaging, the basic capacitance measurements are normally referred to a time period sometime after the de-aging process. Various manufacturers use different time bases but the most popular one is one day or twenty-four hours after "last heat." Change in the aging curve can be caused by the application of voltage and other stresses. The possible changes in capacitance due to de-aging by heating the unit explain why capacitance changes are allowed after test, such as temperature cycling, moisture resistance, etc., in MIL specs. The application of high voltages such as dielectric withstanding voltages also tends to de-age capacitors and is why re-reading of capacitance after 12 or 24 hours is allowed in military specifications after dielectric strength tests have been performed.

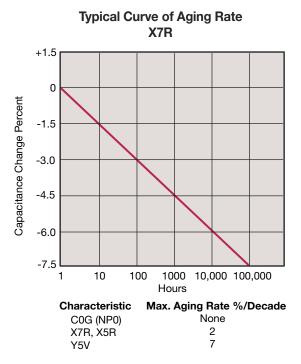


Figure 6

Effects of Frequency – Frequency affects capacitance and impedance characteristics of capacitors. This effect is much more pronounced in high dielectric constant ceramic formulation than in low K formulations. AVX's SpiCap software generates impedance, ESR, series inductance, series resonant frequency and capacitance all as functions of frequency, temperature and DC bias for standard chip sizes and styles. It is available free from AVX and can be downloaded for free from AVX website: www.avx.



General Description



Effects of Mechanical Stress - High "K" dielectric ceramic capacitors exhibit some low level piezoelectric reactions under mechanical stress. As a general statement, the piezoelectric output is higher, the higher the dielectric constant of the ceramic. It is desirable to investigate this effect before using high "K" dielectrics as coupling capacitors in extremely low level applications.

Reliability - Historically ceramic capacitors have been one of the most reliable types of capacitors in use today. The approximate formula for the reliability of a ceramic capacitor is:

$$\frac{L_o}{L_t} = \left(\frac{V_t}{V_o}\right) X \left(\frac{T_t}{T_o}\right) Y$$

where

 L_0 = operating life T_t = test temperature and L, = test life T_0 = operating temperature in °C V, = test voltage

X,Y = see text V_0 = operating voltage

Historically for ceramic capacitors exponent X has been considered as 3. The exponent Y for temperature effects typically tends to run about 8.

A capacitor is a component which is capable of storing electrical energy. It consists of two conductive plates (electrodes) separated by insulating material which is called the dielectric. A typical formula for determining capacitance is:

$$C = \frac{.224 \text{ KA}}{t}$$

C = capacitance (picofarads)

K = dielectric constant (Vacuum = 1)

A = area in square inches

t = separation between the plates in inches (thickness of dielectric)

.224 = conversion constant (.0884 for metric system in cm)

Capacitance - The standard unit of capacitance is the farad. A capacitor has a capacitance of 1 farad when 1 coulomb charges it to 1 volt. One farad is a very large unit and most capacitors have values in the micro (10⁻⁶), nano (10⁻⁹) or pico (10⁻¹²) farad level.

Dielectric Constant - In the formula for capacitance given above the dielectric constant of a vacuum is arbitrarily chosen as the number 1. Dielectric constants of other materials are then compared to the dielectric constant of a vacuum.

Dielectric Thickness - Capacitance is indirectly proportional to the separation between electrodes. Lower voltage requirements mean thinner dielectrics and greater capacitance per volume.

Area - Capacitance is directly proportional to the area of the electrodes. Since the other variables in the equation are usually set by the performance desired, area is the easiest parameter to modify to obtain a specific capacitance within a material group.

Energy Stored - The energy which can be stored in a capacitor is given by the formula:

$$E = \frac{1}{2}CV^2$$

E = energy in joules (watts-sec)

V = applied voltage

C = capacitance in farads

Potential Change - A capacitor is a reactive component which reacts against a change in potential across it. This is shown by the equation for the linear charge of a capacitor:

$$I_{ideal} = C \frac{dV}{dt}$$

where

I = Current

C = Capacitance

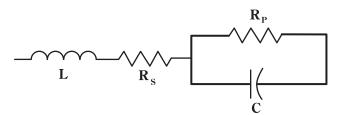
dV/dt = Slope of voltage transition across capacitor

Thus an infinite current would be required to instantly change the potential across a capacitor. The amount of current a capacitor can "sink" is determined by the above equation.

Equivalent Circuit - A capacitor, as a practical device, exhibits not only capacitance but also resistance and inductance. A simplified schematic for the equivalent circuit is:

C = Capacitance L = Inductance

R_s = Series Resistance R_D = Parallel Resistance



Reactance - Since the insulation resistance (Rp) is normally very high, the total impedance of a capacitor is:

$$Z = \sqrt{R_S^2 + (X_C - X_L)^2}$$

where

Z = Total Impedance

R_s = Series Resistance

 $= \frac{1}{2 \pi \text{ fC}}$ $= 2 \pi \text{ fL}$ **X**_c = Capacitive Reactance

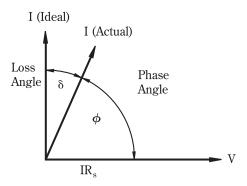
 X_1 = Inductive Reactance

The variation of a capacitor's impedance with frequency determines its effectiveness in many applications.

Phase Angle - Power Factor and Dissipation Factor are often confused since they are both measures of the loss in a capacitor under AC application and are often almost identical in value. In a "perfect" capacitor the current in the capacitor will lead the voltage by 90°.

General Description





In practice the current leads the voltage by some other phase angle due to the series resistance RS. The complement of this angle is called the loss angle and:

Power Factor (P.F.) = Cos ϕ or Sine δ Dissipation Factor (D.F.) = $\tan \delta$

for small values of the tan and sine are essentially equal which has led to the common interchangeability of the two terms in the industry.

Equivalent Series Resistance – The term E.S.R. or Equivalent Series Resistance combines all losses both series and parallel in a capacitor at a given frequency so that the equivalent circuit is reduced to a simple R-C series connection.

Dissipation Factor – The DF/PF of a capacitor tells what percent of the apparent power input will turn to heat in the capacitor.

Dissipation Factor =
$$\frac{\text{E.S.R.}}{\text{X}_{\odot}}$$
 = (2 π fC) (E.S.R.)

The watts loss are:

Watts loss = $(2 \pi fCV^2)$ (D.F.)

Very low values of dissipation factor are expressed as their reciprocal for convenience. These are called the "Q" or Quality factor of capacitors.

Parasitic Inductance – The parasitic inductance of capacitors is becoming more and more important in the decoupling of today's high speed digital systems. The relationship between the inductance and the ripple voltage induced on the DC voltage line can be seen from the simple inductance equation:

$$V = L \frac{di}{dt}$$

The $\frac{cli}{clt}$ seen in current microprocessors can be as high as 0.3 A/ns, and up to 10A/ns. At 0.3 A/ns, 100pH of parasitic inductance can cause a voltage spike of 30mV. While this does not sound very drastic, with the Vcc for microprocessors decreasing at the current rate, this can be a fairly large percentage.

Another important, often overlooked, reason for knowing the parasitic inductance is the calculation of the resonant frequency. This can be important for high frequency, bypass capacitors, as the resonant point will give the most signal attenuation. The resonant frequency is calculated from the simple equation:

$$f_{\text{res}} = \frac{1}{2\pi\sqrt{\text{LC}}}$$

Insulation Resistance – Insulation Resistance is the resistance measured across the terminals of a capacitor and consists principally of the parallel resistance RP shown in the equivalent circuit. As capacitance values and hence the area of dielectric increases, the I.R. decreases and hence the product (C x IR or RC) is often specified in ohm farads or more commonly megohmmicrofarads. Leakage current is determined by dividing the rated voltage by IR (Ohm's Law).

Dielectric Strength – Dielectric Strength is an expression of the ability of a material to withstand an electrical stress. Although dielectric strength is ordinarily expressed in volts, it is actually dependent on the thickness of the dielectric and thus is also more generically a function of volts/mil.

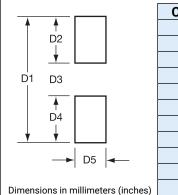
Dielectric Absorption – A capacitor does not discharge instantaneously upon application of a short circuit, but drains gradually after the capacitance proper has been discharged. It is common practice to measure the dielectric absorption by determining the "reappearing voltage" which appears across a capacitor at some point in time after it has been fully discharged under short circuit conditions.

Corona – Corona is the ionization of air or other vapors which causes them to conduct current. It is especially prevalent in high voltage units but can occur with low voltages as well where high voltage gradients occur. The energy discharged degrades the performance of the capacitor and can in time cause catastrophic failures.

MLC Chip Capacitors



REFLOW SOLDERING



Case Size	D1	D2	D3	D4	D5
0201	0.85 (0.033)	0.30 (0.012)	0.25 (0.010)	0.30 (0.012)	0.35 (0.014)
0402	1.70 (0.067)	0.60 (0.024)	0.50 (0.020)	0.60 (0.024)	0.50 (0.020)
0603	2.30 (0.091)	0.80 (0.031)	0.70 (0.028)	0.80 (0.031)	0.75 (0.030)
0805	3.00 (0.118)	1.00 (0.039)	1.00 (0.039)	1.00 (0.039)	1.25 (0.049)
1206	4.00 (0.157)	1.00 (0.039)	2.00 (0.079)	1.00 (0.039)	1.60 (0.063)
1210	4.00 (0.157)	1.00 (0.039)	2.00 (0.079)	1.00 (0.039)	2.50 (0.098)
1808	5.60 (0.220)	1.00 (0.039)	3.60 (0.142)	1.00 (0.039)	2.00 (0.079)
1812	5.60 (0.220)	1.00 (0.039)	3.60 (0.142)	1.00 (0.039)	3.00 (0.118)
1825	5.60 (0.220)	1.00 (0.039)	3.60 (0.142)	1.00 (0.039)	6.35 (0.250)
2220	6.60 (0.260)	1.00 (0.039)	4.60 (0.181)	1.00 (0.039)	5.00 (0.197)
2225	6.60 (0.260)	1.00 (0.039)	4.60 (0.181)	1.00 (0.039)	6.35 (0.250)

Component Pad Design

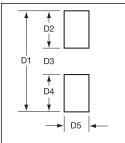
Component pads should be designed to achieve good solder filets and minimize component movement during reflow soldering. Pad designs are given below for the most common sizes of multilayer ceramic capacitors for both wave and reflow soldering. The basis of these designs is:

· Pad width equal to component width. It is permissible to

decrease this to as low as 85% of component width but it is not advisable to go below this.

- · Pad overlap 0.5mm beneath component.
- Pad extension 0.5mm beyond components for reflow and 1.0mm for wave soldering.

WAVE SOLDERING

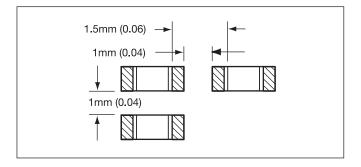


Case Size	D1	D2	D3	D4	D5
0603	3.10 (0.12)	1.20 (0.05)	0.70 (0.03)	1.20 (0.05)	0.75 (0.03)
0805	4.00 (0.15)	1.50 (0.06)	1.00 (0.04)	1.50 (0.06)	1.25 (0.05)
1206	5.00 (0.19)	1.50 (0.06)	2.00 (0.09)	1.50 (0.06)	1.60 (0.06)

Dimensions in millimeters (inches)

Component Spacing

For wave soldering components, must be spaced sufficiently far apart to avoid bridging or shadowing (inability of solder to penetrate properly into small spaces). This is less important for reflow soldering but sufficient space must be allowed to enable rework should it be required.



Preheat & Soldering

The rate of preheat should not exceed 4°C/second to prevent thermal shock. A better maximum figure is about 2°C/second.

For capacitors size 1206 and below, with a maximum thickness of 1.25mm, it is generally permissible to allow a temperature differential from preheat to soldering of 150°C. In all other cases this differential should not exceed 100°C.

For further specific application or process advice, please consult AVX.

Cleaning

Care should be taken to ensure that the capacitors are thoroughly cleaned of flux residues especially the space beneath the capacitor. Such residues may otherwise become conductive and effectively offer a low resistance bypass to the capacitor.

Ultrasonic cleaning is permissible, the recommended conditions being 8 Watts/litre at 20-45 kHz, with a process cycle of 2 minutes vapor rinse, 2 minutes immersion in the ultrasonic solvent bath and finally 2 minutes vapor rinse.

Recommended Soldering Profiles



REFLOW SOLDER PROFILES

AVX RoHS compliant products utilize termination finishes (e.g.Sn or SnAg) that are compatible with all Pb-Free soldering systems and are fully reverse compatible with SnPb soldering systems. A recommended SnPb profile is shown for comparison; for Pb-Free soldering, IPC/JEDECJ- STD-020C may be referenced. The upper line in the chart shows the maximum envelope to which products are qualified (typically 3x reflow cycles at 260°C max). The center line gives the recommended profile for optimum wettability and soldering in Pb-Free Systems.

Preheat:

The pre-heat stabilizes the part and reduces the temperature differential prior to reflow. The initial ramp to 125°C may be rapid, but from that point (2-3)°C/sec is recommended to allow ceramic parts to heat uniformly and plastic encapsulated parts to stabilize through the glass transition temperature of the body (~ 180°C).

Reflow:

In the reflow phase, the maximum recommended time > 230°C is 40secs. Time at peak reflow is 10secs max.; optimum reflow is achieved at 250°C, (see wetting balance chart opposite) but products are qualified to 260°C max. Please reference individual product datasheets for maximum limits

Cool Down:

Cool down should not be forced and 6°C/sec is recommended. A slow cool down will result in a finer grain structure of the reflow solder in the solder fillet.

WAVE SOLDER PROFILES

For wave solder, there is no change in the recommended wave profile; all standard Pb-Free (SnCu/SnCuAg) systems operate at the same 260°C max recommended for SnPb systems.

Preheat:

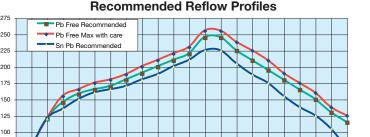
This is more important for wave solder; a higher temperature preheat will reduce the thermal shock to SMD parts that are immersed (please consult individual product data sheets for SMD parts that are suited to wave solder). SMD parts should ideally be heated from the bottom-Side prior to wave. PTH (Pin through hole) parts on the topside should not be separately heated.

Wave:

250°C - 260°C recommended for optimum solderability.

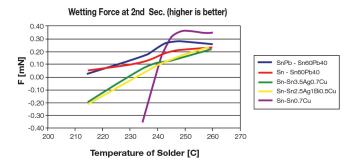
Cool Down:

As with reflow solder, cool down should not be forced and 6°C/sec is recommended. Any air knives at the end of the 2nd wave should be heated.

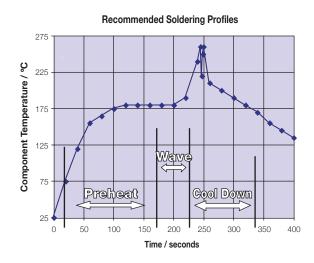


Time / secs

100 120 140 160 180 200 220 240 260 280 300 320 340 360 380 400 420



IMPORTANT NOTE: Typical Pb-Free reflow solders have a more dull and grainy appearance compared to traditional SnPb. Elevating the reflow temperature will not change this, but extending the cool down can help improve the visual appearance of the joint.



250

225

200

175

50

Component Temperature /

MLC Chip Capacitors



APPLICATION NOTES

Storage

The components should be stored in their "as received packaging" where possible. If the components are removed from their original packaging then they should be stored in an airtight container (e.g. a heat sealed plastic bag) with desiccant (e.g. silica gel). Storage area temperature should be kept between +5 degrees C and +30 degrees C with humidity < 70% RH. Storage atmosphere must be free of gas containing sulfur and chlorine. Avoid exposing the product to saline moisture or to temperature changes that might result in the formation of condensation. To assure good solderability performance we recommend that the product be used within 6 months from our shipping date, but can be used for up to 12 months. Chip capacitors may crack if exposed to hydrogen (H2) gas while sealed or if coated with silicon, which generates hydrogen gas.

Solderability

Terminations to be well soldered after immersion in a 60/40 tin/lead solder bath at 245°C +/- 5°C for 5 +0/-0.5 seconds.

Leaching

Terminations will resist leaching for at least the immersion times and conditions shown below.

Termination Type	Solder Tin/ Lead/Silver	Solder Temp °C	Immersion Time Seconds	
Nickel Barrier	60/40/0	260 ± 5	30 ± 1	

Lead-Free Wave Soldering

The recommended peak temperature for lead-free wave soldering is 250°C-260°C for 3-5 seconds. The other parameters of the profile remains the same as above.

The following should be noted by customers changing from lead based systems to the new lead free pastes.

- A. The visual standards used for evaluation of solder joints will need to be modified as lead free joints are not as bright as with tin-lead pastes and the fillet may not be as large.
- B. Lead-free solder pastes do not allow the same self alignment as lead containing systems. Standard mounting pads are acceptable, but machine set up may need to be modified.

General

Surface mounting chip multilayer ceramic capacitors are designed for soldering to printed circuit boards or other substrates. The construction of the components is such that they will withstand the time/temperature profiles used in both wave and reflow soldering methods.

Handling

Chip multilayer ceramic capacitors should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of tweezers or vacuum pick ups is strongly recommended for individual components. Bulk handling should ensure that abrasion and mechanical shock are minimized. Taped and reeled components provides the ideal medium for direct presentation to the placement machine. Any mechanical shock should be minimized during handling chip multilayer ceramic capacitors.

Preheat

It is important to avoid the possibility of thermal shock during soldering and carefully controlled preheat is therefore required. The rate of preheat should not exceed 4°C/second and a target figure 2°C/second is recommended. Although an 80°C to 120°C temperature differential is preferred, recent developments allow a temperature differential between the component surface and the soldering temperature of 150°C (Maximum) for capacitors of 1210 size and below with a maximum thickness of 1.25mm. The user is cautioned that the risk of thermal shock increases as chip size or temperature differential increases.

Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder to give a good joint should be used. Excessive solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. AVX terminations are suitable for all wave and reflow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Cooling

Natural cooling in air is preferred, as this minimizes stresses within the soldered joint. When forced air cooling is used, cooling rate should not exceed 4°C/second. Quenching is not recommended but if used, maximum temperature differentials should be observed according to the preheat conditions above.

Cleaning

Flux residues may be hygroscopic or acidic and must be removed. AVX MLC capacitors are acceptable for use with all of the solvents described in the specifications MIL-STD-202 and EIA-RS-198. Alcohol based solvents are acceptable and properly controlled water cleaning systems are also acceptable. Many other solvents have been proven successful, and most solvents that are acceptable to other components on circuit assemblies are equally acceptable for use with ceramic capacitors.

Prevention of Metallic Migration

Note that when components with Sn plating on the end terminations are to be used in applications that are likely to experience conditions of high humidity under bias voltage, we strongly recommend that the circuit boards be conformally coated to protect the Sn from moisture that might lead to migration and eventual current leakage.

When using Capacitor Arrays we recommend that there is no differential in applied voltage between adjacent elements.

MLC Chip Capacitors



POST SOLDER HANDLING

Once SMP components are soldered to the board, any bending or flexure of the PCB applies stresses to the soldered joints of the components. For leaded devices, the stresses are absorbed by the compliancy of the metal leads and generally don't result in problems unless the stress is large enough to fracture the soldered connection.

Ceramic capacitors are more susceptible to such stress because they don't have compliant leads and are brittle in nature. The most frequent failure mode is low DC resistance or short circuit. The second failure mode is significant loss of capacitance due to severing of contact between sets of the internal electrodes.

Cracks caused by mechanical flexure are very easily identified and generally take one of the following two general forms:

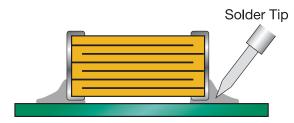
Mechanical cracks are often hidden underneath the termination and are difficult to see externally. However, if one end termination falls off during the removal process from PCB, this is one indication that the cause of failure was excessive mechanical stress due to board warping.

COMMON CAUSES OF MECHANICAL CRACKING

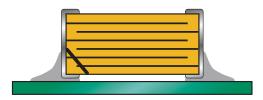
The most common source for mechanical stress is board depanelization equipment, such as manual breakapart, v-cutters and shear presses. Improperly aligned or dull cutters may cause torqueing of the PCB resulting in flex stresses being transmitted to components near the board edge. Another common source of flexural stress is contact during parametric testing when test points are probed. If the PCB is allowed to flex during the test cycle, nearby ceramic capacitors may be broken.

A third common source is board to board connections at vertical connectors where cables or other PCBs are connected to the PCB. If the board is not supported during the plug/unplug cycle, it may flex and cause damage to nearby components.

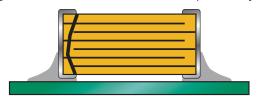
Special care should also be taken when handling large (>6" on a side) PCBs since they more easily flex or warp than smaller boards.



Preferred Method - No Direct Part Contact



Type A: Angled crack between bottom of device to top of solder joint.

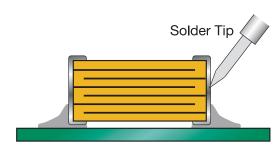


Type B: Fracture from top of device to bottom of device.

REWORKING OF MLCS

Thermal shock is common in MLCs that are manually attached or reworked with a soldering iron. AVX strongly recommends that any reworking of MLCs be done with hot air reflow rather than soldering irons. It is practically impossible to cause any thermal shock in ceramic capacitors when using hot air reflow.

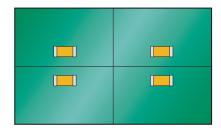
However direct contact by the soldering iron tip often causes thermal cracks that may fail at a later date. If rework by soldering iron is absolutely necessary, it is recommended that the wattage of the iron be less than 30 watts and the tip temperature be <300°C. Rework should be performed by applying the solder iron tip to the pad and not directly contacting any part of the ceramic capacitor.



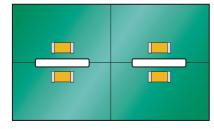
Poor Method - Direct Contact with Part

PCB BOARD DESIGN

To avoid many of the handling problems, AVX recommends that MLCs be located at least .2" away from nearest edge of board. However when this is not possible, AVX recommends that the panel be routed along the cut line, adjacent to where the MLC is located.



No Stress Relief for MLCs



Routed Cut Line Relieves Stress on MLC



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