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### **MMPQ2222**

### **NPN Multi-Chip General Purpose Amplifier**

- This device is for use as a medium power amplifier and switch requiring collector currents up to 500mA.
- Sourced from process 19.



### Absolute Maximum Ratings \* T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
I <sub>C</sub>	Collector Current - Continuous	500	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	- 55 ~ +155	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired

- These ratings are based on a maximum junction temperature of 150 degrees C.
   These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

### Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Charact	eristics	•		•	•
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage *	$I_C = 10 \text{mA}, I_B = 0$	30		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10\mu A, I_E = 0$	60		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_C = 10\mu A, I_C = 0$	5.0		V
I <sub>CBO</sub>	Collector Cutoff Current	$V_{CB} = 50V, I_{E} = 0$		50	nA
I <sub>EBO</sub>	Emitter Cutoff Current	$V_{EB} = 3.0 \text{V}, I_{C} = 0$		50	nA
On Charact	eristics *	•		•	•
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 10mA, V <sub>CE</sub> = 10V I <sub>C</sub> = 150mA, V <sub>CE</sub> = 1.0V * I <sub>C</sub> = 150mA, V <sub>CE</sub> = 1.0V *	75 100 50		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage *	$I_C = 150 \text{mA}, I_B = 15 \text{mA}$ $I_C = 500 \text{mA}, I_B = 50 \text{mA}$		0.4 1.6	V V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage *	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA I <sub>C</sub> = 500mA, I <sub>B</sub> = 50mA		1.3 2.6	V V
Small Signa	Il Characteristics				
f <sub>T</sub>	Current GAin Bandwidth Product	$I_C = 20$ mA, $V_{CE} = 20$ V, $f = 100$ MHz		300	MHz
C <sub>obo</sub>	Output Capacitance	$V_{CB} = 10V, I_E = 0, f = 100kHz$		4.0	pF
C <sub>ibo</sub>	Input Capacitance	$V_{EB} = 0.5V$ , $I_E = 0$ , $f = 100kHz$		20	pF
NF	Noise Figure	$I_C = 100\mu A$ , $V_{CE} = 10V$ , $R_S = 1.0k\Omega$ , $f = 1.0kHz$		2.0	dB

<sup>\*</sup> Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2.0%

Symbol	Parameter	Max.	Units
$P_{D}$	Total Device Dissipation	1000	mW
	Derate above 25°C	8.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		
	Effective 4 Die	125	°C/W
	Each Die	240	°C/W

### **Typical Characteristics**

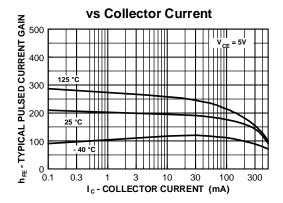


Figure 1. Typical Pulsed Current Gain vs Collector Current

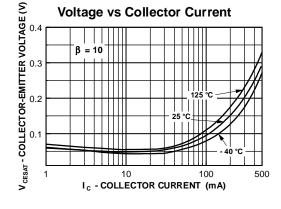


Figure 2. Collector-Emitter Saturation Voltage vs Collector Current

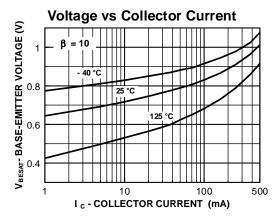


Figure 3. Base-Emitter Saturation Voltage vs Collector Current

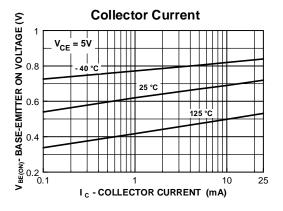


Figure 4. Base-Emitter On Voltage vs Collector Current

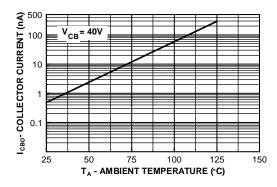


Figure 5. Collector Cutoff Current vs Ambient Temperature

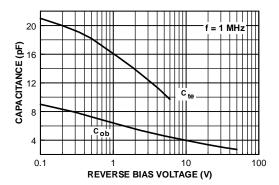


Figure 6. Emitter Transition and Output Capacitance vs Reverse Bias Voltage

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### Typical Characteristics (Continued)

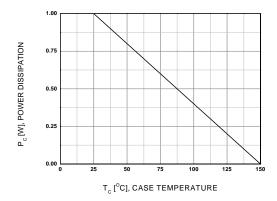
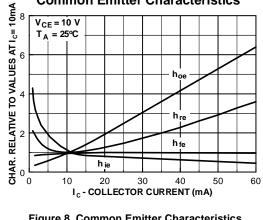


Figure 7. Power Dissipation vs **Ambient Temperature** 



**Common Emitter Characteristics** 

Figure 8. Common Emitter Characteristics

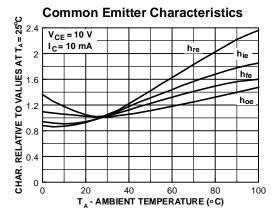


Figure 9. Common Emitter Characteristics

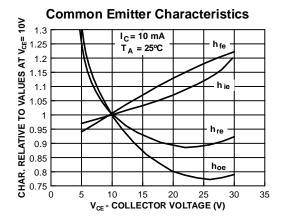


Figure 10. Common Emitter Characteristics

### **Test Circuit**

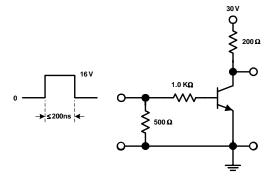


Figure 1. Saturated Turn-On Switching Time

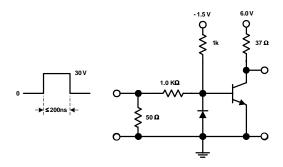
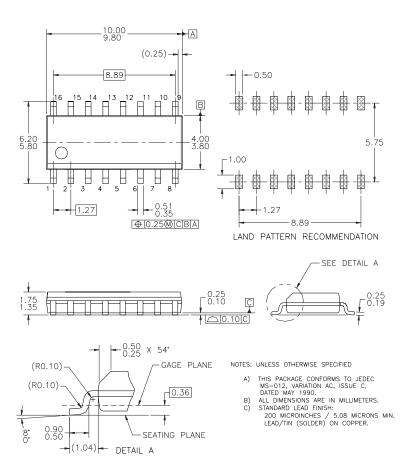


Figure 2. Saturated Turn-Off Switching Time

### **Package Dimensions**

### SOIC-16



Dimensions in Millimeters

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