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### FDP8030L/FDB8030L

### N-Channel Logic Level PowerTrench<sup>®</sup> MOSFET

#### **General Description**

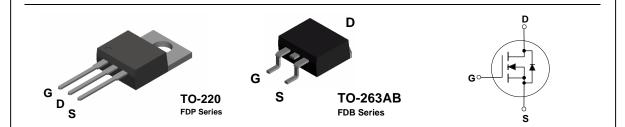
This N-Channel Logic level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

These MOSFETS feature faster switching and lower gate charge than other MOSFETS with comparable  $R_{\text{DS}(\text{on})}$  specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

#### Features

- 80 A, 30 V.  $R_{DS(ON)} = 0.0035 \ \Omega \ @ \ V_{GS} = 10 \ V$  $R_{DS(ON)} = 0.0045 \ \Omega \ @ \ V_{GS} = 4.5 \ V$
- Critical DC electrical parameters specified at elevated temperature
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor
- High performance trench technology for extremely low R<sub>DS(ON)</sub>
- 175°C maximum junction temperature rating



### Absolute Maximum Ratings TA=25°C unless otherwise noted

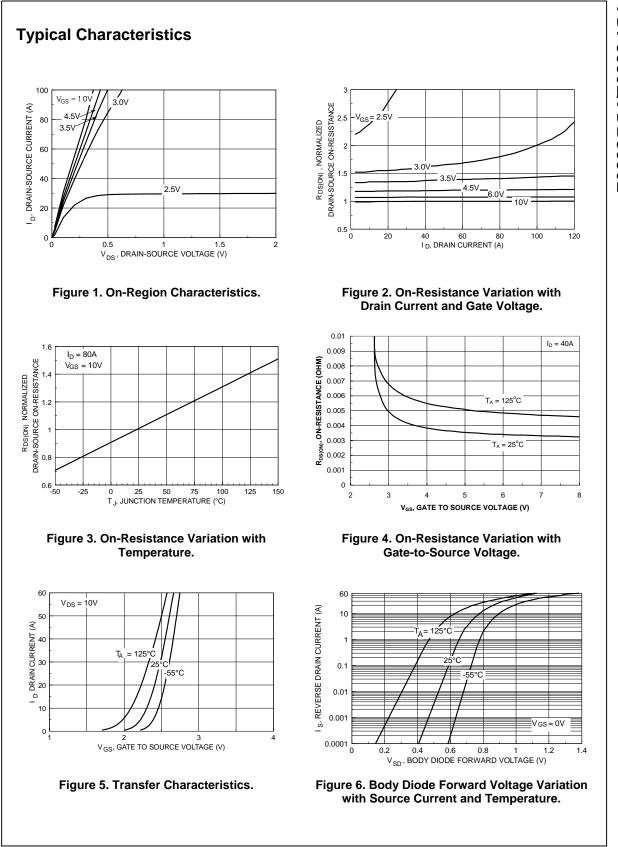
Symbol	Parameter	Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage	30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (Note	1) 80	A
	– Pulsed (Note	1) 300	
PD	Total Power Dissipation @ $T_c = 25^{\circ}C$	187	W
	Derate above 25°	C 1.25	W°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Rang	e -65 to +175	°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	275	°C
Therma	I Characteristics		
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.8	°C/W
R <sub>eJA</sub>	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

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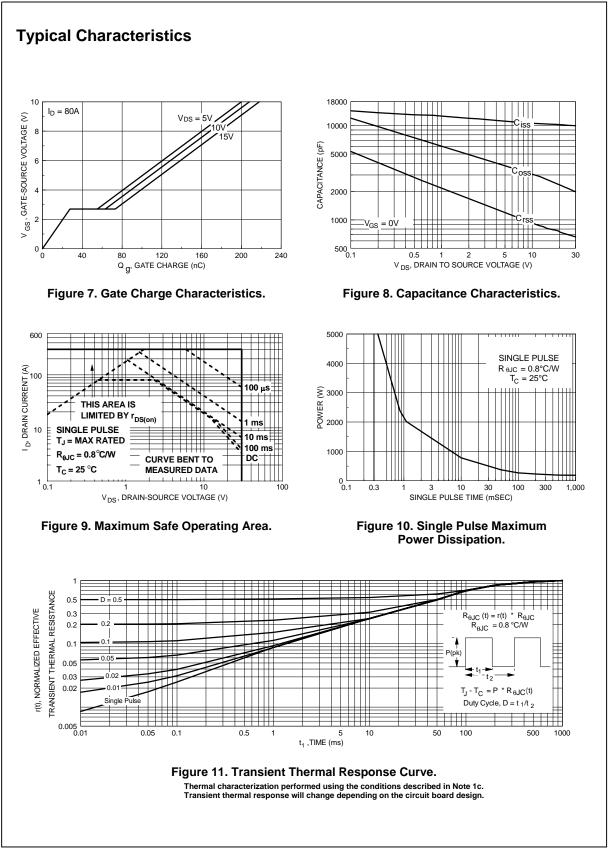
	Deveneter	Test Conditions	N/1:10	<b>T</b>	Max	11
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-So	purce Avalanche Ratings (Note					
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 20 V, I_D = 80 A$			1500	mJ
AR	Maximum Drain-Source Avalanche Current				80	A
Off Char	acteristics					
BV <sub>DSS</sub>	Drain–Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	30			V
<u>ΔBV<sub>DSS</sub></u> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to 25°C		23		mV/°C
IDSS	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 24 \text{ V}, \qquad V_{\text{GS}} = 0 \text{ V}$			10	μA
IGSSF	Gate–Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
	Gate-Body Leakage, Reverse	$V_{GS} = -20 V$ $V_{DS} = 0 V$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1	1.5	2	V
<u>ΔVgs(th)</u> ΔTJ	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$		-5		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance			3.1 4.0	3.5 5.6	mΩ
		$V_{GS} = 4.5 \text{ V}, \qquad I_D = 70 \text{ A}$		3.6	4.5	
I <sub>D(on)</sub>	On–State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	60			Α
<b>g</b> fs	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_D = 80 \text{ A}$		170		S
Dynamic	c Characteristics					
Ciss	Input Capacitance	$V_{DS} = 15 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		10500		pF
Coss	Output Capacitance	f = 1.0 MHz		2700		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			1650		pF
Switchin	g Characteristics (Note 2)					
t <sub>D(on)</sub>	Turn–On Delay Time	$V_{DD} = 15 V$ , $I_D = 50 A$ ,		20	35	ns
t <sub>r</sub>	Turn–On Rise Time	$V_{GS} = 4.5 \text{ V}, \qquad R_{GEN} = 10 \Omega$		185	225	ns
t <sub>D (off)</sub>	Turn–Off Delay Time	$R_{GS} = 10 \Omega$		160	200	ns
t <sub>f</sub>	Turn–Off Fall Time	1		200	240	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 15 V,		120	170	nC
Q <sub>gs</sub>	Gate–Source Charge	$I_{D} = 80 \text{ A}, V_{GS} = 5 \text{ V}$		27		nC
Q <sub>gd</sub>	Gate–Drain Charge	1		48		nC
	ource Diode Characteristics	and Maximum Ratings	1			
	Maximum Continuous Drain–Source				80	А
Is I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode	х ў			300	A
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	, ,	1	1	1.3	V

1. Pulse Test: Pulse Width <  $300\mu$ s, Duty Cycle < 2.0%



FDP8030L/FDB8030L

FDP8030L/FDB8030L Rev C2(W)



FDP8030L/FDB8030L

FDP8030L/FDB8030L Rev C2(W)



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