SWITCHMODE **Power Rectifiers**

These state-of-the-art devices have the following features:

- Low Power Loss / High Efficiency
- New Package Provides Capability of Inspection and Probe After Board Mounting
- Guardring for Stress Protection
- Low Forward Voltage
- 175°C Operating Junction Temperature
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free and Halide-Free Devices
- **Mechanical Characteristics:**
- Case: Epoxy, Molded
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting SurfaceTemperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements

MAXIMUM RATINGS

Rating	Symbol	ol Value U	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
Average Rectified Forward Current (Rated V_R , T_C = 165°C)	I _{F(AV)}	5	A
Peak Repetitive Forward Current, (Rated V _R , Square Wave, 20 kHz, T _C = 165°C)	I _{FRM}	10	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	150	A
Storage Temperature Range	T _{stg}	-65 to +175	°C
Operating Junction Temperature	Т _Ј	-40 to +175	°C
Unclamped Inductive Switching Energy (10 mH Inductor, Non-repetitive)	E _{AS}	40	mJ
ESD Rating (Human Body Model)		3B	
ESD Rating (Machine Model)		M4	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

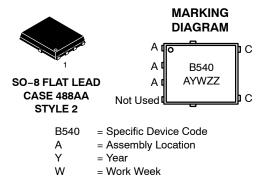


ON Semiconductor[®]

http://onsemi.com

SCHOTTKY BARRIER RECTIFIERS **5 AMPERES** 40 VOLTS





= Work Week

77

= Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping†
MBR540MFST1G	SO–8 FL (Pb–Free)	1500 / Tape & Reel
MBR540MFST3G	SO–8 FL (Pb–Free)	5000 / Tape & Reel
NRVB540MFST1G	SO–8 FL (Pb–Free)	1500 / Tape & Reel
NRVB540MFST3G	SO–8 FL (Pb–Free)	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

THERMAL CHARACTERISTICS

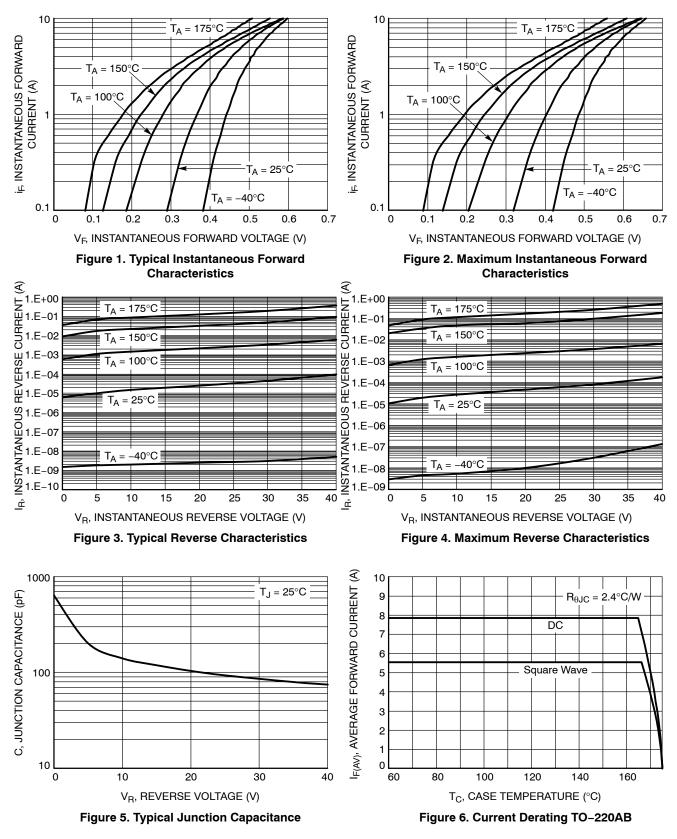
Characteristic	Symbol	Тур	Мах	Unit
Thermal Resistance, Junction-to-Case, Steady State (Assumes 600 mm ² 1 oz. copper bond pad, on a FR4 board)	R _{θJC}	-	4.0	°C/W
	!	<u>.</u>		

ELECTRICAL CHARACTERISTICS

Instantaneous Forward Voltage (Note 1) ($i_F = 5 \text{ Amps}, T_J = 100^{\circ}\text{C}$) ($i_F = 5 \text{ Amps}, T_J = 25^{\circ}\text{C}$)	٧F	0.44 0.50	0.54 0.58	V
Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_J = 100^{\circ}$ C) (Rated dc Voltage, $T_J = 25^{\circ}$ C)	i _R	6 0.100	20 2	mA

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

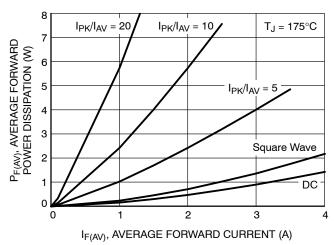


Figure 7. Forward Power Dissipation

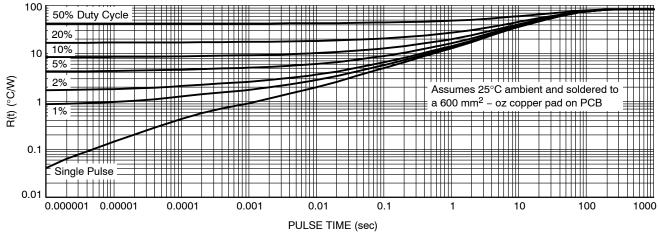
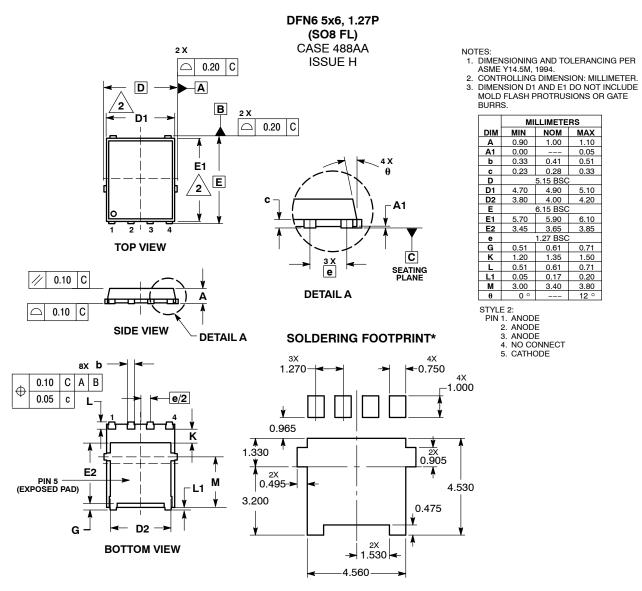


Figure 8. Thermal Characteristics

PACKAGE DIMENSIONS



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and **W** are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemic.om/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights of others. SCILLC products are not designed, intended, or authorized for use as components intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly, any claim of personal injury or death massociated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and ex

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada ON Semiconductor Website: www.onsemi.com Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local

Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: MBR540MFST3G NRVB540MFST3G NRVB540MFST1G MBR540MFST1G