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April 2014

# 2N3906 / MMBT3906 / PZT3906 PNP General-Purpose Amplifier

### **Description**

This device is designed for general-purpose amplifier and switching applications at collector currents of 10 mA to 100 mA.



### **Ordering Information**

Part Number	Marking	Package	Packing Method	Pack Quantity
2N3906BU	2N3906	TO-92 3L	Bulk	10000
2N3906TA	2N3906	TO-92 3L	Ammo	2000
2N3906TAR	2N3906	TO-92 3L	Ammo	2000
2N3906TF	2N3906	TO-92 3L	Tape and Reel 2000	
2N3906TFR	2N3906	TO-92 3L	Tape and Reel 2000	
MMBT3906	2A	SOT-23 3L	Tape and Reel	3000
PZT3906	3906	SOT-223 4L	Tape and Reel	2500

### Absolute Maximum Ratings(1)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>CBO</sub>	Collector-Base Voltage	-40	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5.0	V
I <sub>C</sub>	Collector Current - Continuous	-200	mA
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

#### Note:

1. These ratings are based on a maximum junction temperature of 150°C.

These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

### **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Maximum			Unit
		2N3906 <sup>(3)</sup>	MMBT3906 <sup>(2)</sup>	PZT3906 <sup>(3)</sup>	Oilit
В	Total Device Dissipation	625	350	1,000	mW
$P_{D}$	Derate Above 25°C	5.0	2.8	8.0	mW/°C
$R_{ heta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

### Notes:

- 2. Device is mounted on FR-4 PCB 1.6 inch X 1.6 inch X 0.06 inch.
- 3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

### **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
OFF CHAR	ACTERISTICS	l		I.	1
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(4)</sup>	I <sub>C</sub> = -1.0 mA, I <sub>B</sub> = 0	-40		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -10 \mu\text{A}, \ I_E = 0$	-40		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -10 \mu\text{A},  I_C = 0$	-5.0		V
I <sub>BL</sub>	Base Cut-Off Current	$V_{CE} = -30 \text{ V}, V_{BE} = 3.0 \text{ V}$		-50	nA
I <sub>CEX</sub>	Collector Cut-Off Current	$V_{CE} = -30 \text{ V}, V_{BE} = 3.0 \text{ V}$		-50	nA
ON CHARA	CTERISTICS		•	•	•
		$I_C = -0.1 \text{ mA}, V_{CE} = -1.0 \text{ V}$	60		
		$I_C = -1.0 \text{ mA}, V_{CE} = -1.0 \text{ V}$	80		
h <sub>FE</sub>	DC Current Gain <sup>(4)</sup>	$I_C = -10 \text{ mA}, V_{CE} = -1.0 \text{ V}$	100	300	
		$I_C = -50 \text{ mA}, V_{CE} = -1.0 \text{ V}$	60		
		$I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{V}$	30		
)/ (aat)	Collector-Emitter Saturation Voltage	$I_C = -10 \text{ mA}, I_B = -1.0 \text{ mA}$		-0.25	V
V <sub>CE</sub> (sat)		$I_C = -50 \text{ mA}, I_B = -5.0 \text{ mA}$		-0.40	
\/ (aat)	Base-Emitter Saturation Voltage	$I_C = -10 \text{ mA}, I_B = -1.0 \text{ mA}$	-0.65	-0.85	.,
V <sub>BE</sub> (sat)		$I_C = -50 \text{ mA}, I_B = -5.0 \text{ mA}$		-0.95	- V
SMALL SIG	NAL CHARACTERISTICS			•	
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_C = -10 \text{ mA}, V_{CE} = -20 \text{ V},$ f = 100 MHz	250		MHz
C <sub>obo</sub>	Output Capacitance	$V_{CB} = -5.0 \text{ V}, I_{E} = 0,$ f = 100 kHz		4.5	pF
C <sub>ibo</sub>	Input Capacitance	$V_{EB} = -0.5 \text{ V}, I_{C} = 0,$ f = 100 kHz		10.0	pF
NF	Noise Figure	$I_C$ = -100 μA, $V_{CE}$ = -5.0 V, $R_S$ = 1.0 kΩ, $f$ = 10 Hz to 15.7 kHz		4.0	dB
SWITCHING	CHARACTERISTICS	l			
t <sub>d</sub>	Delay Time	$V_{CC} = -3.0 \text{ V}, V_{BF} = -0.5 \text{ V}$		35	ns
t <sub>r</sub>	Rise Time	I <sub>C</sub> = -10 mA, I <sub>B1</sub> = -1.0 mA		35	ns
t <sub>s</sub>	Storage Time	$V_{CC} = -3.0 \text{ V}, I_{C} = -10 \text{ mA},$		225	ns
t <sub>f</sub>	Fall Time	$I_{B1} = I_{B2} = -1.0 \text{ mA}$		75	ns

#### Note:

4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

### **Typical Performance Characteristics**

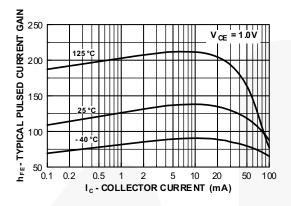


Figure 1. Typical Pulsed Current Gain vs. Collector Current

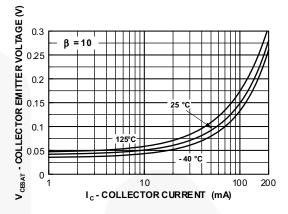


Figure 2. Collector-Emitter Saturation Voltage vs.
Collector Current

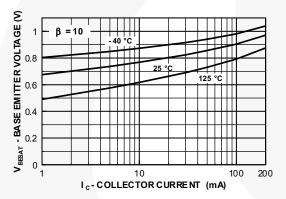


Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

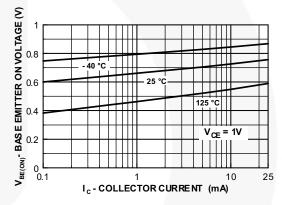


Figure 4. Base-Emitter On Voltage vs. Collector Current

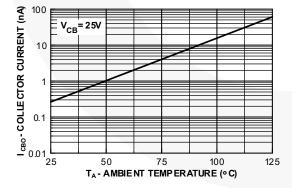


Figure 5. Collector Cut-Off Current vs.
Ambient Temperature

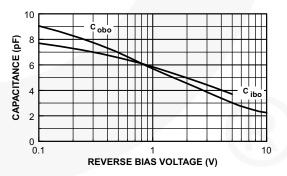


Figure 6. Common-Base Open Circuit Input and Output Capacitance vs. Reverse Bias Voltage

### **Typical Performance Characteristics** (Continued)

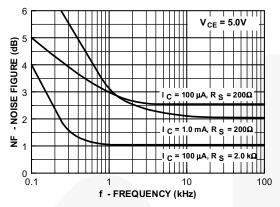


Figure 7. Noise Figure vs. Frequency

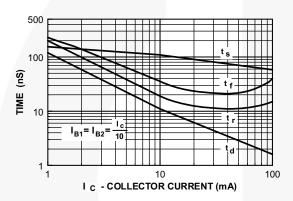


Figure 9. Switching Times vs. Collector Current

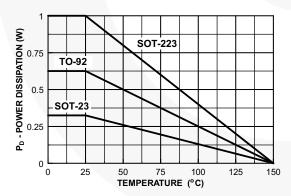


Figure 11. Power Dissipation vs. Ambient Temperature

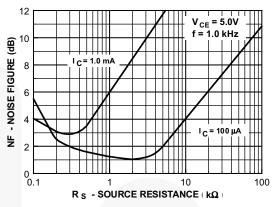


Figure 8. Noise Figure vs. Source Resistance

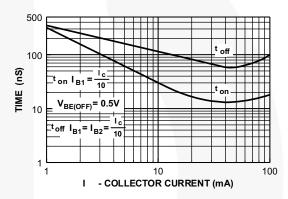


Figure 10. Turn-On and Turn-Off Times vs. Collector Current

### **Typical Performance Characteristics** (Continued)

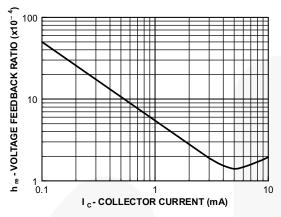


Figure 12. Voltage Feedback Ratio

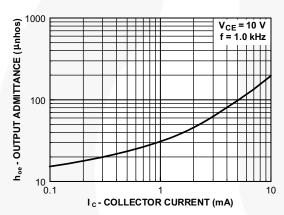


Figure 14. Output Admittance

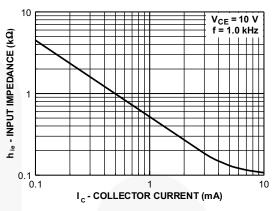


Figure 13. Input Impedance

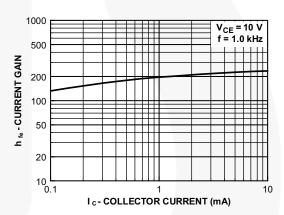


Figure 15. Current Gain

### **Physical Dimensions**

### **TO-92 (Bulk)**

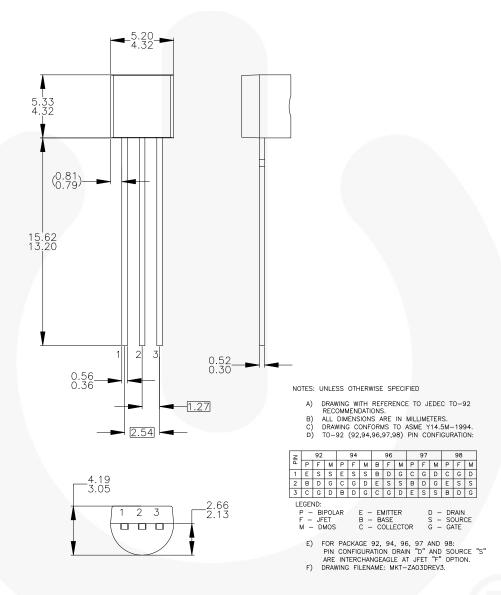


Figure 16. 3-LEAD, TO92, JEDEC TO-92 COMPLIANT STRAIGHT LEAD CONFIGURATION (OLD TO92AM3)

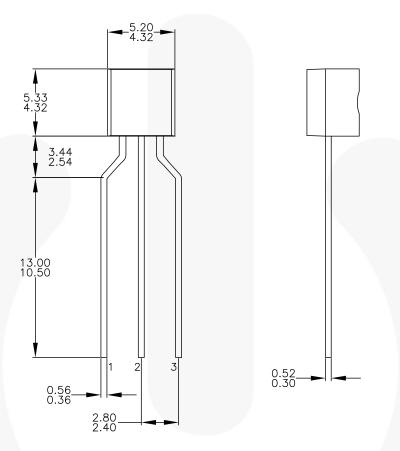
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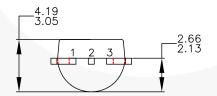
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### Physical Dimensions (Continued)

### TO-92 (Ammo, Tape and Reel)





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC.
  ALL DIMENSIONS ARE IN MILLIMETERS.
  DRAWING CONFORMS TO ASME Y14.5M-2009.
  DRAWING FILENAME: MKT-ZAO3FREV3.
  FAIRCHILD SEMICONDUCTOR.

Figure 17. 3-LEAD, TO92, MOLDED 0.200 IN LINE SPACING LEAD FORM (J61Z OPTION)

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### Physical Dimensions (Continued)

### 0.95 2.92±0.20 3 1.40 1.30 +0.20 2.20 -0.15 2 0.60 0.37 (0.29)0.95 ⊕ 0.20M A B 1.00 1.90 1.90 LAND PATTERN RECOMMENDATION SEE DETAIL A 1.20 MAX (0.93)0.10 0.00 0.10M C 2.40±0.30 **NOTES: UNLESS OTHERWISE SPECIFIED GAGE PLANE** A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H. 0.23 B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONS ARE INCLUSIVE OF BURRS, 0.25 MOLD FLASH AND TIE BAR EXTRUSIONS. D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M - 1994. 0.20 MIN E) DRAWING FILE NAME: MA03DREV10 SEATING **PLANE** (0.55)

SOT-23

Figure 18. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE

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### Physical Dimensions (Continued)

### SOT-223 4L

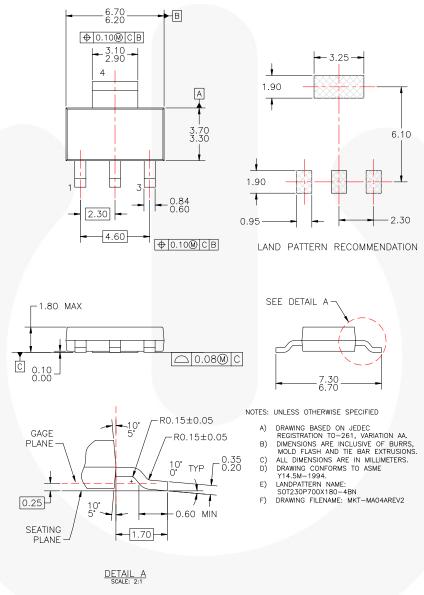


Figure 19. MOLDED PACKAGE, SOT-223, 4-LEAD

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Definition of Terms				
Datasheet Identification		Definition		
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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<u>2N3906\_J25Z</u> <u>2N3906\_D26Z</u> <u>2N3906\_D28Z</u> <u>2N3906\_D74Z</u> <u>2N3906\_D75Z</u> <u>2N3906RA</u> <u>2N3906TAM</u> <u>2N3906TAM</u> <u>2N3906TAR</u> <u>2N3906\_D81Z</u> <u>2N3906TFR</u> <u>2N3906TA</u> <u>2N3906TF</u> <u>2N3906BU</u> <u>2N3906\_J05Z</u> <u>2N3906\_J61Z</u> <u>2N3906\_J18Z</u> 2N3906\_D11Z 2N3906\_D27ZS00Z 2N3906TF\_Q 2N3906TFR\_Q 2N3906\_J05Z\_Q