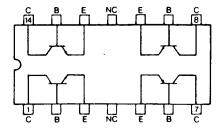


DESCRIPTION

The CENTRAL SEMICONDUCTOR MPQ6501, MPQ6502, types are comprised of four independent silicon transistors mounted in a 14 PIN DIP, designed for general purpose amplifier and switching applications. The MPQ6501 contains two 2N2221 (NPN) chips and two 2N2906 (PNP) chips, and the MPQ6502 contains two 2N2222 (NPN) chips and two 2N2907 (PNP) chips, both acting as dual complementary pairs.

MAXIMUM RATINGS $(T_A=25^{\circ}C \text{ unless otherwise noted})$								
		SYM						UNIT
Collector-Base Voltage		V _{CBO}		60			V	
Collector-Emitter Voltage		V _{CEO}		30			V	
Emitter-Base Voltage		V _{EBO}		5.0			V	
Collector Current		I _C		500			mA	
Power Dissipation (Each Transistor)		PD			650			mW
Power Dissipation (Total Package)		PD			2000			mW
Operating and Storage		-	-	1-		_		0
Junction Temperature			TSTG	-65 to +150)		°C
ELECTRICAL CHARACTERISTICS $(T_A=25^{\circ}C \text{ unless otherwise noted})$			MPQ6501 MPQ		502			
SYMBOL	TEST CONDITIONS			MIN	MAX	MIN	MAX	UNIT
I CBO	V _{CB} =50V				30		30	nA
IEB0	V _{EB} =3.0V				30		30	nA
BVCBO	Ι _C =10μΑ			60		60		V
BVCEO	I _C =10mA			30		30		V
^{ΒV} ΕΒΟ	¢ε=μA			5.0		5.0		V
V _{CE} (SAT)	I _C =150mA, I _B =15mA				0.4		0.4	V
VCE(SAT)	I _C =300mA, I _B =30mA				1.4		1.4	V
V _{BE} (SAT)	$I_{C}=150$ mA, $I_{B}=15$ mA				1.3		1.3	V
VBE (SAT)	$I_{C}=300$ mA, $I_{B}=30$ mA			0.5	2.0	50	2.0	V
hFE	$V_{CE}=10V$, $I_{C}=1.0mA$			25		50		
h _{FE}	$V_{CE}=10V$, $I_{C}=10mA$			35 40		75		
hFE	V _{CE} =10V, I _C =150mA V _{CE} =10V, I _C =300mA			20		100 30		
hFE fT	$V_{CE}=20V$, $I_{C}=50mA$, f=100MHz			200		200		MHz
C _{ob}	$V_{CB}=10V$, $I_{E}=0$, f=100kHz			200	8.0	200	8.0	pF
Cip	$V_{EB}=2.0V$, $I_{C}=0$, f=100kHz				30		30	pF
ton	$V_{CC}=30V$, $V_{BF}=0.5V$, $I_{C}=150mA$, $I_{B1}=15mA$			30TYP		30	30TYP ns	
t_{OFF} V _{CC} =30V, I _C =150mA, I _{B1} =I _{B2} =15mA			225TYP			225TYP ns		
V 11		-		-		-		

CONNECTION DIAGRAM



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Central Semiconductor: MPQ6502