

1. Global joint venture starts operations as WeEn Semiconductors

Dear customer,

As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

In this document where the previous NXP references remain, please use the new links as shown below.

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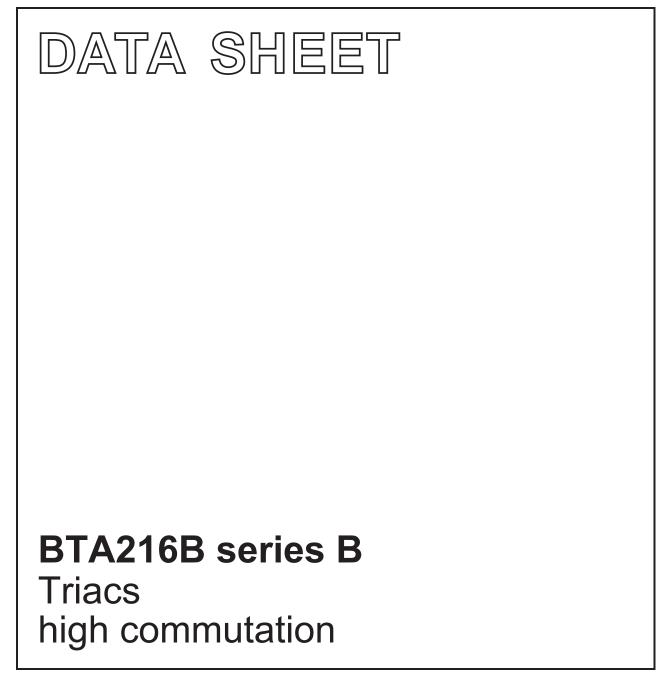
If you have any questions related to this document, please contact our nearest sales office via email or phone (details via <u>salesaddresses@ween-semi.com</u>).

Thank you for your cooperation and understanding,

WeEn Semiconductors



DISCRETE SEMICONDUCTORS



Product specification

October 1997



MAX.

800B

800

16

140

UNIT

V

A

А

Triacs high commutation

BTA216B series B

GENERAL DESCRIPTION

Glass passivated high commutation triacs in a plastic envelope suitable for surface mounting, intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. These devices will commutate the full rated rms current at the maximum rated junction temperature, without the aid of a snubber.

PINNING - SOT404

PINDESCRIPTION1main terminal 12main terminal 23gatembmain terminal 2

-0-

2

1

3

QUICK REFERENCE DATA

voltages

current

PARAMETER

Repetitive peak off-state

Non-repetitive peak on-state

RMS on-state current

SYMBOL

VDRM

T(RMS)

I_{TSM}

SYMBOL

MAX.

500B

500

16

140

BTA216B-

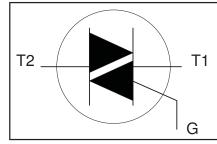
MAX.

600B

600

16

140



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
V _{DRM}	Repetitive peak off-state voltages		-	-500 500 ¹	-600 600 ¹	-800 800	v
I _{T(RMS)}	RMS on-state current	full sine wave;	-		16		A
I _{TSM}	Non-repetitive peak on-state current	$T_{mb} \le 99 \ ^{\circ}C$ full sine wave; $T_j = 25 \ ^{\circ}C$ prior to surge					
		t = 20 ms t = 16.7 ms	-		140 150		A A
l²t dl _⊤ /dt	I ² t for fusing Repetitive rate of rise of on-state current after triggering		-		98 100		A ² s A/μs
$\begin{matrix} I_{GM} \\ V_{GM} \\ P_{GM} \\ P_{G(AV)} \end{matrix}$	Peak gate current Peak gate voltage Peak gate power Average gate power	over any 20 ms	- - -		2 5 5 0.5		A V W W
T _{stg} T _j	Storage temperature Operating junction temperature	period	-40 -		150 125		°C °C

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/ μ s.

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-mb}	Thermal resistance junction to mounting base	full cycle half cycle	-	-	1.2	K/W K/W
R _{th j-a}		minimum footprint, FR4 board	-	55	-	K/W

STATIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current ²	$V_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$					
a			T2+ G+	2	18	50	mA
			T2+ G-	2	21	50	mA
			T2- G-	2	34	50	mA
IL.	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$					
	_		T2+ G+	-	31	60	mA
			T2+ G-	-	34	90	mA
			T2- G-	-	30	60	mA
I _H	Holding current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$		-	31	60	mA
ι _н V _T	On-state voltage	$I_{T} = 20 \text{ A}$		-	1.2	1.5	
V _{GT}	Gate trigger voltage	$\dot{V}_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$		-	0.7	1.5	V
		$V_{D} = 400 \text{ V}; I_{T} = 0.1 \text{ A}; T_{L} = 12$	25 °C	0.25	0.4	-	V
I _D	Off-state leakage current	$V_D = V_{DRM(max)}; T_j = 125 \degree C$		-	0.1	0.5	mA

DYNAMIC CHARACTERISTICS

 $T_j = 25$ °C unless otherwise stated

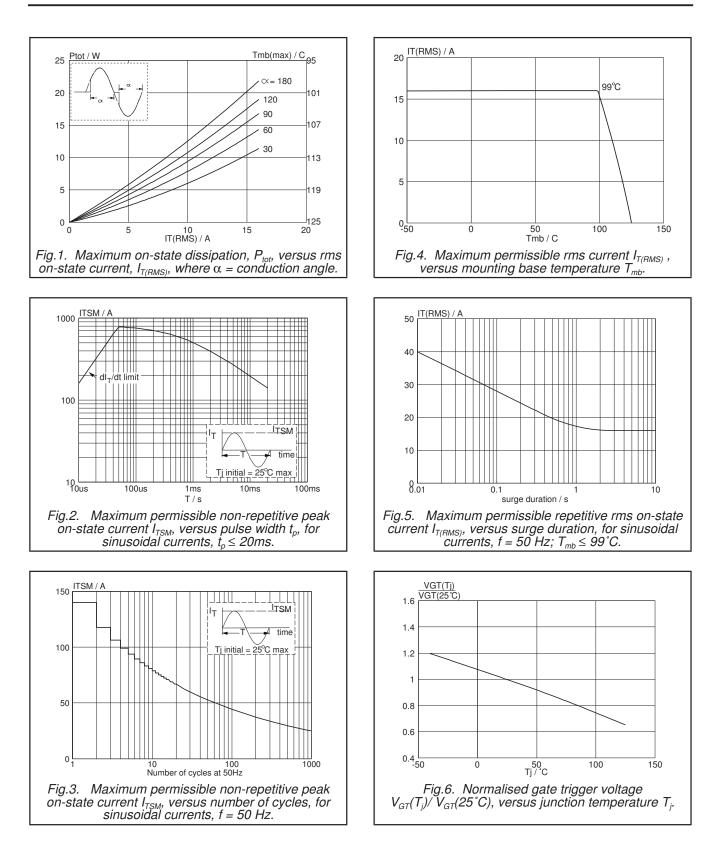
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$ exponential waveform; gate open circuit	1000	4000	-	V/µs
dI _{com} /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; \text{ T}_{\text{j}} = 125 \text{ °C}; \text{ I}_{\text{T}(RMS)} = 16 \text{ A};$ without snubber; gate open circuit	-	28	-	A/ms
t _{gt}		$I_{TM} = 20 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	-	μs

BTA216B series B

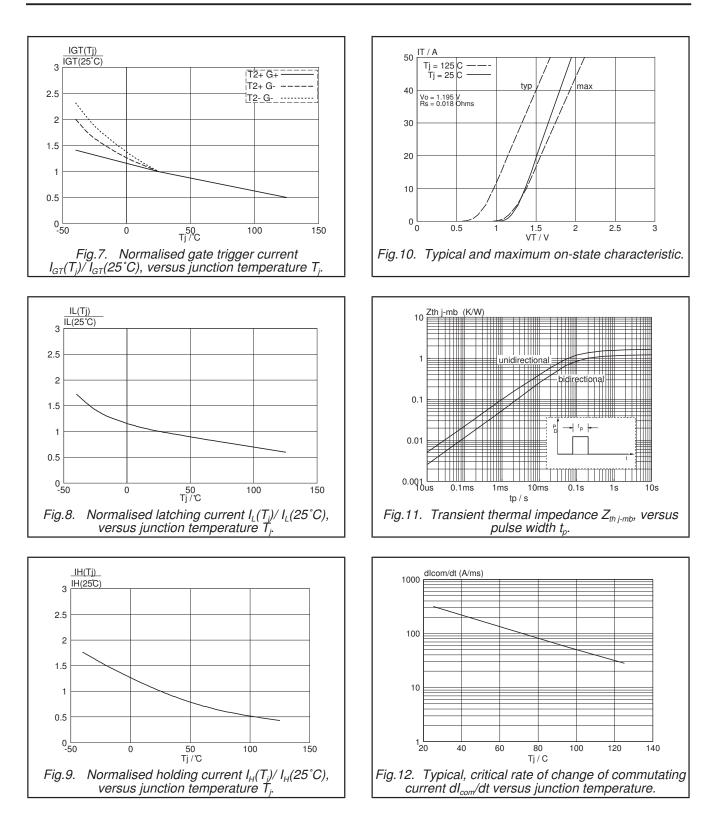
2 Device does not trigger in the T2-, G+ quadrant.

October 1997

BTA216B series B

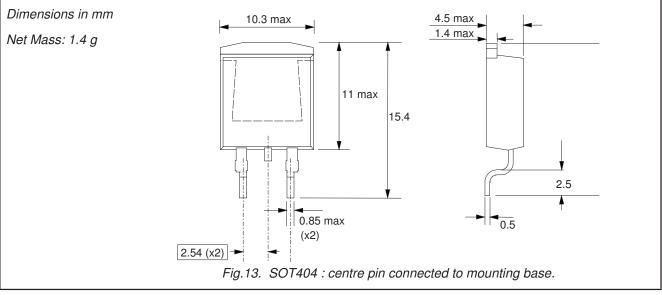


BTA216B series B



BTA216B series B

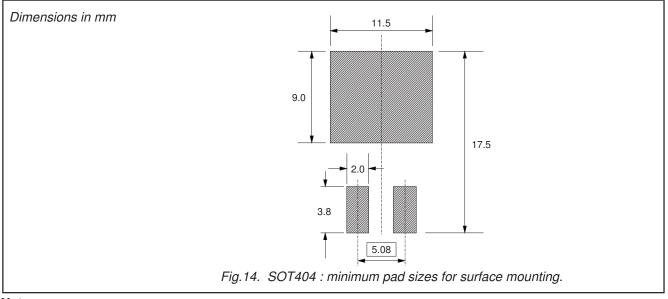
MECHANICAL DATA



Notes

1. Epoxy meets UL94 V0 at 1/8".

MOUNTING INSTRUCTIONS



Notes

1. Plastic meets UL94 V0 at 1/8".

Legal information

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Contact information

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