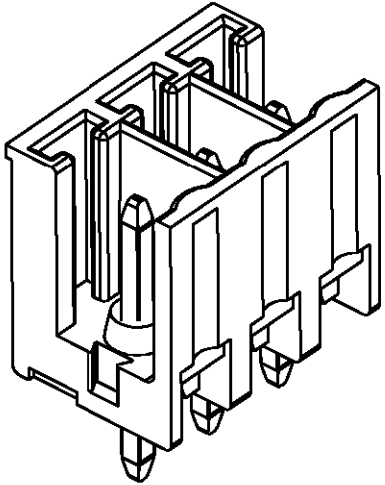
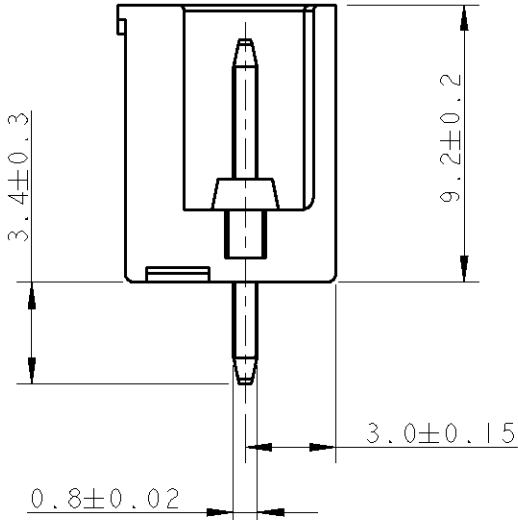
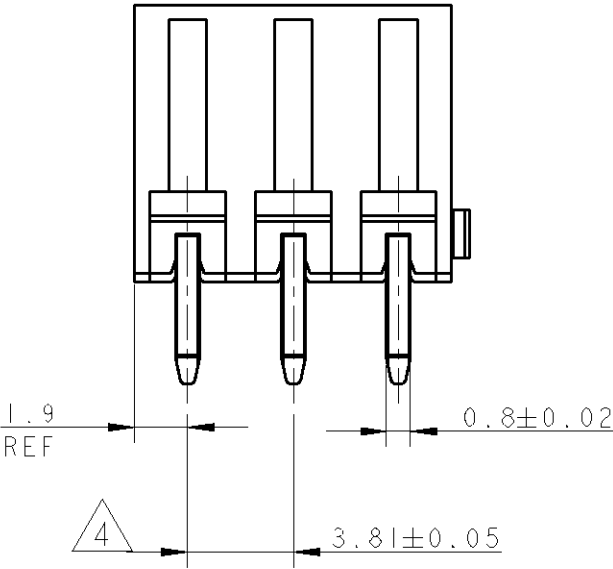
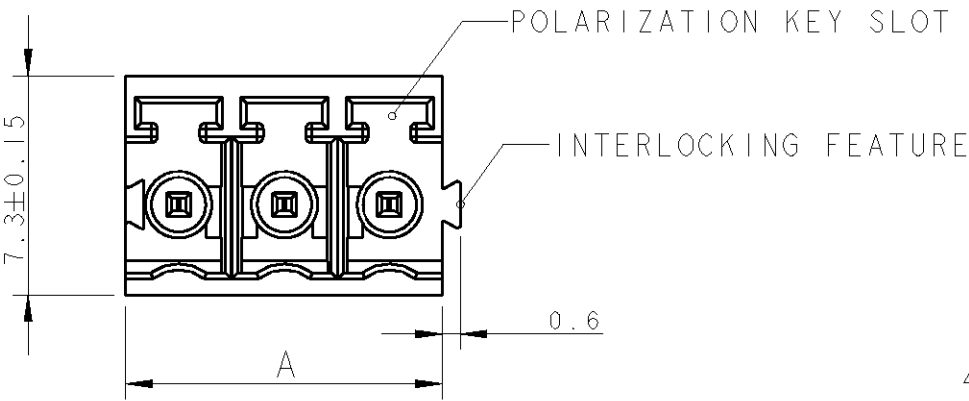


2		1					
LOC	DIST	REVISIONS					
FT	0	P	LTR	DESCRIPTION	DATE	DWN	APVD
			B	REVISED PER 0G3B-0119-01	06FEB2002	EZ	DB



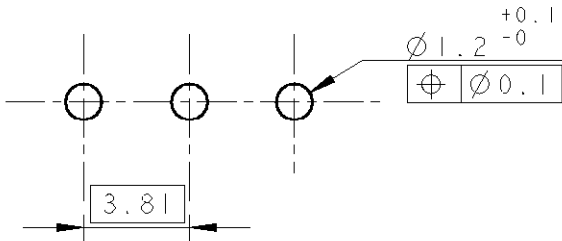
284518-3 AS SHOWN

95.25	25	2-284518-5
91.44	24	2-284518-4
87.63	23	2-284518-3
83.82	22	2-284518-2
80.01	21	2-284518-1
76.20	20	2-284518-0
72.39	19	1-284518-9
68.58	18	1-284518-8
64.77	17	1-284518-7
60.96	16	1-284518-6
57.15	15	1-284518-5
53.34	14	1-284518-4
49.53	13	1-284518-3
45.72	12	1-284518-2
41.91	11	1-284518-1
38.10	10	1-284518-0
34.29	9	284518-9
30.48	8	284518-8
26.67	7	284518-7
22.86	6	284518-6
19.05	5	284518-5
15.24	4	284518-4
11.43	3	284518-3
7.62	2	284518-2
DIM A	NO OF POS	PART NUMBER



- 1 MATERIALS AND FINISH
HOUSING : PA 6.6, UL 94-V0, COLOR GREEN
TERMINAL : PHOSPHOR BRONZE, TIN PLATED
- 2 END-TO-END STACKABLE W/INTERLOCK WITHOUT
LOSS OF CENTERLINE SPACING
- 3 SUITABLE FOR 1.6-2.4 mm. PC BOARD THICKNESS
- 4 NOT CUMULATIVE TOLERANCE

RECOMMENDED PC BOARD LAYOUT



THIS DRAWING IS A CONTROLLED DOCUMENT FOR TYCO ELECTRONICS CORPORATION IT IS SUBJECT TO CHANGE AND THE CONTROLLING ENGINEERING ORGANIZATION SHOULD BE CONTACTED FOR THE LATEST REVISION.		DWN 06FEB2002 E. ZANOLINI	BUCHANAN Tyco Electronics Harrisburg, PA 17105-3608			
DIMENSIONS: mm		CHK 06FEB2002 E. ZANOLINI				
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD 06FEB2002 D. BIEVENOUR	NAME TERMINAL BLOCK, HEADER ASSEMBLY STRAIGHT, OPEN ENDS STACKING W/INTERLOCK, 3.81mm PITCH			
0 PLC ±.3 1 PLC ±.3 2 PLC ±.25 3 PLC ±. 4 PLC ±. ANGLES ±2		PRODUCT SPEC 108-20166				
MATERIAL		APPLICATION SPEC 114-20079	SIZE A3	CAGE CODE 00779	DRAWING NO C-284518	RESTRICTED TO -
FINISH		WEIGHT -	CUSTOMER DRAWING			
SCALE 4:1		SHEET 1 OF 1				

Mouser Electronics

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