CEL

5 V AGC AMPLIFIER + UPC3217GV VIDEO AMPLIFIER UPC3218GV

FEATURES

- ON-CHIP LOW DISTORTION AMPLIFIER: IIP3 = -4 dBm at minimuim gain
- WIDE AGC DYNAMIC RANGE: GCR = 53 dB TYP
- ON-CHIP VIDEO AMPLIFIER: VOUT = 1.25 VP-P at single-ended output
- SUPPLY VOLTAGE: Vcc = 5 V
- PACKAGED IN 8 PIN SSOP SUITABLE FOR SURFACE MOUNTING

DESCRIPTION

NEC's UPC3217GV and UPC3218GV are Silicon Monolithic ICs designed for use as AGC amplifiers for digital CATV, cable modems and IP telephony systems. These ICs consist of a two stage gain control amplifier and a fixed video gain amplifier. The devices provide a differential input and differential output for noise performance, which eliminates shielding requirements.

The package is 8-pin SSOP (Shrink Small Outline Package) suitable for surface mount.

These ICs are manufactured using NEC's 10 GHz fT NESAT[™]II AL silicon bipolar process. This process uses silicon nitride passivation film. This material can protect chip surface from external pollution and prevent corrosion/migration. Thus, these ICs have excellent performance, uniformity and reliability.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

ELECTRICAL CHARACTERISTICS

(TA = 25°C, Vcc = 5 V, Zs = 1 K Ω , ZL = 240 Ω , fin = 45 MHz, Unless otherwise specified)

PART NUMBER PACKAGE OUTLINE		UPC3217GV S08			UPC3218GV S08			
SYMBOLS PARAMETERS AND CONDITIONS		UNITS	MIN	ТҮР	МАХ	MIN	ТҮР	МАХ
DC Characteristic	CS							
Icc	Circuit Current (no input signal)	mA	15	23	34	15	23	34
RF Characterisit	tics							
BW	Frequency Bandwidth, VAGC* = 4.5 V ¹	MHz		100			100	
Gмах	Maximum Gain , VAGC* = 4.5 V	dB	50	53	56	60	63	66
GMIN	Minimum Gain, VAGC* = 0.5 V	dB	-4.5	0	3.5	4.5	10	13.5
GCR	Gain ConTrol Range, VAGC* = 0.5 to 4.5 V	dB	46.5	53		46.5	53	
NFAGC	Noise Figure, VAGC* = 4.5 V at MAX Gain	dB		6.5	8.0		3.5	4.5
Vout	Output Voltage, Single Ended Output	VP-P		1.25			1.25	
IМз	Third Order Intermodulation Distortion,	dBc		55			55	
	fin1 = 44 MHz, fin2 = 45 MHz,							
	VIN = 30 dBmV per tone ²							

Note:

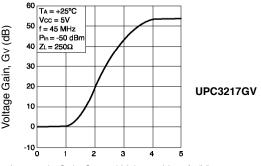
1. -3dB with respect to 10 MHz gain

2. VAGC is adjusted to establish VOUT = 1.25 VP-P per tone

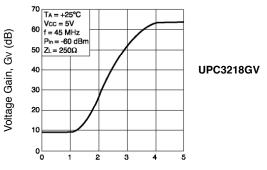
_California Eastern Laboratories

* VAGC shown as applied in the evaluation cicuit (see page 5) through a resistive bridge (voltage divider). Actual voltage range on the pin of the IC is 0 to 3 V.

VOLTAGE GAIN vs. AUTOMATIC GAIN CONTROL VOLTAGE*



Automatic Gain Control Voltage, VAGC* (V)



Automatic Gain Control Voltage, VAGC* (V)

APPLICATIONS

- Digital CATV
- · Cable modem receivers
- IP Telephony Receivers

UPC3217GV, UPC3218GV

ABSOLUTE MAXIMUM RATINGS¹

(TA = 25°C, unless otherwise specified)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	6.0
PD	Power Dissipation ² , TA = 85°C	mW	250
TOP1	Operating Ambient Temp.	°C	-40 to +85
TSTG	Storage Temperature	°C	-50 to +150

Notes:

1. Operation in excess of any one of these parameters may result

in permanent damage.

5

4

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2

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0

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30

60

90

Frequency, f (MHz)

2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB, with copper patterning on both sides.

RECOMMENDED **OPERATING CONDITIONS**

SYMBOL	PARAMETER	UNITS	MIN	ТҮР	МАХ
Vcc	Supply Voltage	V	4.5	5.0	5.5
Та	Operating Ambient Temp.1	°C	-40	+25	+85
VAGC ²	Gain Control Voltage Range	V	0	-	3.0
Vin	Video Input Signal Range	dBmV	8		30

Note:

1. Vcc = 4.5 to 5.5 V

2. AGC range at pin 4 of the IC

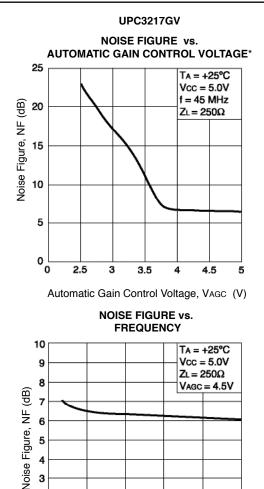
ORDERING INFORMATION

PART NUMBER	QUANTITY
UPC3217GV-E1-A	1 kp/Reel
UPC3218GV-E1-A	1 kp/Reel

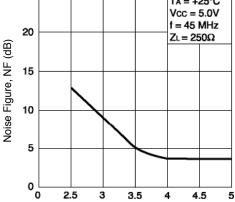
Note:

Embossed tape 8 mm wide. Pin 1 indicates pull-out direction of tape.

TYPICAL PERFORMANCE CURVES (TA = 25°C, unless otherwise specified)

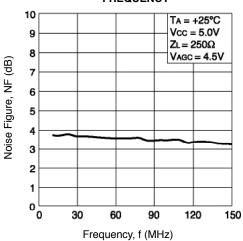


UPC3218GV NOISE FIGURE vs. **AUTOMATIC GAIN CONTROL VOLTAGE*** 25 TA = +25°C



Automatic Gain Control Voltage, VAGC (V)

NOISE FIGURE vs. FREQUENCY



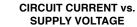
* VAGC shown as applied in the evaluation cicuit (see page 5) through a resistive bridge (voltage divider). Actual voltage range on the pin of the IC is 0 to 3 V.

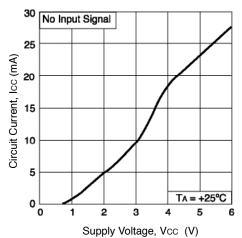
150

120

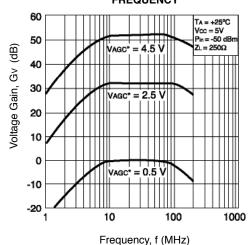
TYPICAL PERFORMANCE CURVES (TA = 25°C, unless otherwise specified)

UPC3217GV

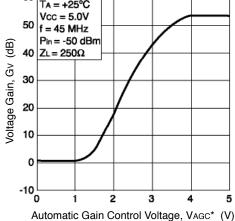








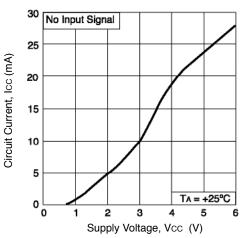
VOLTAGE GAIN vs. AUTOMATIC GAIN CONTROL VOLTAGE*



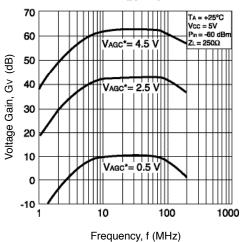
* VAGC shown as applied in the evaluation cicuit (see page 5) through a resistive bridge (voltage divider). Actual voltage range on the pin of the IC is 0 to 3 V.

UPC3218GV

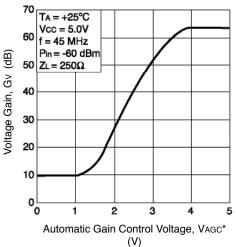
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



VOLTAGE GAIN vs. FREQUENCY

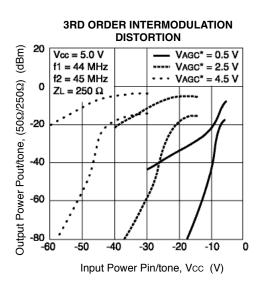


VOLTAGE GAIN vs. AUTOMATIC GAIN CONTROL VOLTAGE*



TYPICAL PERFORMANCE CURVES, cont. (TA = 25°C, unless otherwise specified)

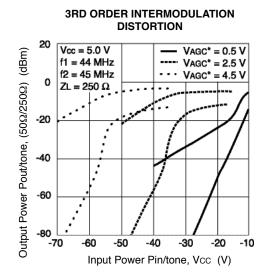
UPC3217GV

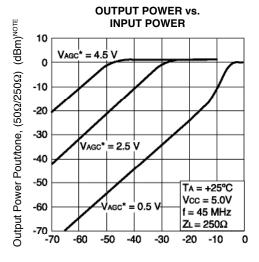


OUTPUT POWER vs. **INPUT POWER** Output Power Pout/tone, (500/2500) (dBm)^{NOTE} 10 4.5 VAGO 0 -10 -20 -30 VAGC* = 2.5 V -40 -50 TA = +25°C Vcc = 5.0V oc* = 0.5 V -60 f = 45 MHz $ZL = 250\Omega$ -70 -60 -50 -40 -30 -20 -10 0 10 Input Power Pin/tone, Vcc (V)

NOTE: Measurement value with spectrum analyzer.

UPC3218GV

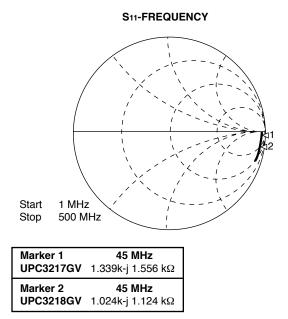




Input Power Pin/tone, Vcc (V)

* VAGC shown as applied in the evaluation cicuit (see page 5) through a resistive bridge (voltage divider). Actual voltage range on the pin of the IC is 0 to 3 V.

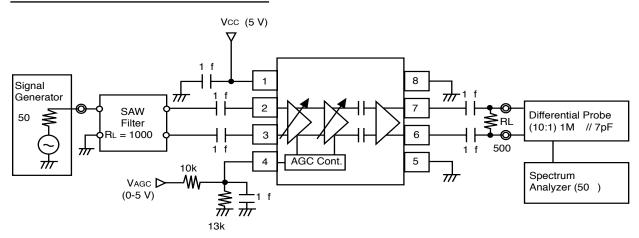
TYPICAL SCATTERING PARAMETERS



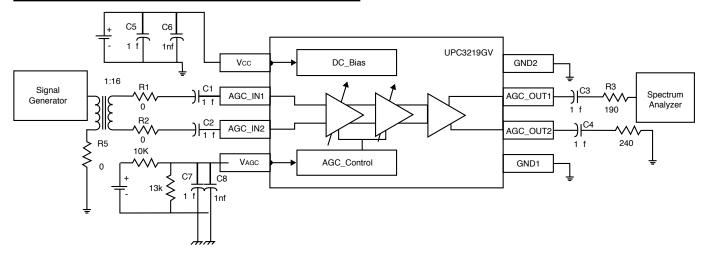
Start 1 MHz Stop 500 MHz

Marker 1 UPC3217GV	45 MHz 9.511 + j 3.869 Ω	
Marker 2 UPC3218GV	45 MHz 9.493 + j 4.317 Ω	

SYSTEM APPLICATION EXAMPLE



EVALUATION BOARD SCHEMATIC AND TEST



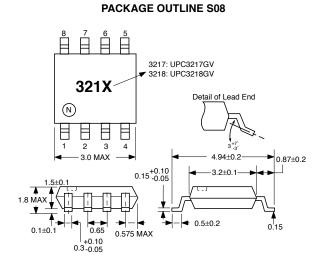
PIN EXPLANATIONS (UPC3217GV, UPC3218GV common)

Pin No.	Name	Applied Voltage (v)	Pin Voltage (v) ¹	Description	Internal Equivalent Circuit
1	Vcc	4.5 to 5.5		Power supply pin. This pin should be externally equipped with bypass capacitor to minimize ground impedance.	
2	INPUT1		1.45	Signal input pins of AGC amplifier.	
3	INPUT2		1.45		
4	VAGC	0 to 3.0 Vcc		Gain control pin. This pin's bias govern the AGC output level. Minimuim Gain at VAGC = 0.5 V Maximum Gain at VAGC = 4.5 V Recommended to use a 0 to 5 V AGC range for the system and divide this voltage through a resistive bridge (see evaluation board). This helps make the AGC slope less steep.	
5	GND 2	0		Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible.	
6	OUTPUT2		2.2	Signal output pins of video amplifier	
7	OUTPUT1		2.2		
8	GND 1	0		Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All ground pins must be connected together with wide ground pattern to decrease impedance difference.	

Note:

1. PIN is measured at Vcc = 5 V

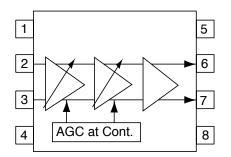
OUTLINE DIMENSIONS (Units in mm)



All dimensions are typical unless specified otherwise.

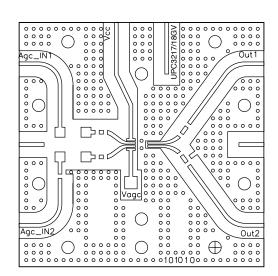
EVALUATION BOARD ASSEMBLY

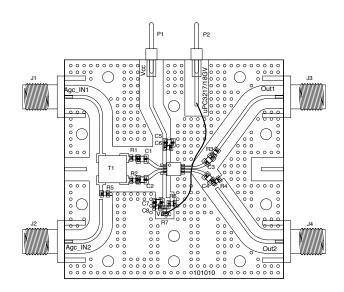
INTERNAL BLOCK DIAGRAM



T1	Transformer4:1 Coilcraft
R7	0603 10K OHM RES ROHM
R6	0603 13K OHM RES ROHM
R4	0603 240 OHM RES ROHM
R3	0603 191 OHM RES ROHM
R1,R2,R5	0603 0 OHM RES ROHM
C6, C8	0603 1000pF CAP ROHM
C1–C5, C7	0805 1uF CAP ROHM
U1	IC NEC, UPC3217/18GV IC NEC

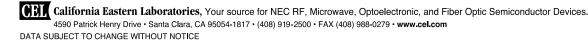
EVALUATION BOARD





Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.



05/14/2008



Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)		
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
РВВ	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

In no event shall CEL's liability arising out of such information exceed the total purchase price of the CEL part(s) at issue sold by CEL to customer on an annual basis.

See CEL Terms and Conditions for additional clarification of warranties and liability.

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