Surface Mount Ultrafast Power Rectifiers

Ideally suited for high voltage, high frequency rectification, or as free wheeling and protection diodes in surface mount applications where compact size and weight are critical to the system.

Features

- Small Compact Surface Mountable Package with J-Bend Leads
- Rectangular Package for Automated Handling
- High Temperature Glass Passivated Junction
- Low Forward Voltage Drop (0.66 V Max @ 1.0 A, T_J = 150°C)
- NRVUA and SURA8 Prefixes for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable*
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 70 mg (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Polarity Band Indicates Cathode Lead
- ESD Protection:
 - ♦ Human Body Model > 4000 V (Class 3)
 - ♦ Machine Model > 400 V (Class C)



ON Semiconductor®

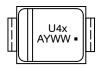
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ULTRAFAST RECTIFIERS 1 AMPERE, 50–100 VOLTS



SMA CASE 403D

MARKING DIAGRAM



U4x = Specific Device Code

x = A for MURA105T3G, SURA8105T3G = B for MURA/NRVUA/SURA8110T3G

A = Assembly Location**

Y = Year WW = Work Week ■ = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
MURA105T3G	SMA (Pb-Free)	5, 000 / Tape & Reel
SURA8105T3G*	SMA (Pb-Free)	5, 000 / Tape & Reel
MURA110T3G	SMA (Pb-Free)	5,000 / Tape & Reel
NRVUA110VT3G*	SMA (Pb-Free)	5,000 / Tape & Reel
SURA8110T3G*	SMA (Pb-Free)	5,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{**}The Assembly Location Code (A) is front side optional. In cases where the Assembly Location is stamped in the package bottom (molding ejecter pin), the front side assembly code may be blank.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage MURA105T3G, SURA8105T3G MURA110T3G, SURA8110T3G, NRVUA110VT3G	V _{RRM} V _{RWM} V _R	50 100	V
Average Rectified Forward Current @ T _L = 155°C @ T _L = 135°C	I _{F(AV)}	1.0 2.0	Α
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	IFSM	50	А
Operating Junction Temperature Range	T _J	-65 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Lead (Note 1)	Psi _{JL} (Note 2)	24	°C/W
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{0JA}	216	

^{1.} Rating applies when surface mounted on the minimum pad size recommended, PC Board FR-4.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 3) ($i_F = 1.0 \text{ A}, T_J = 25^{\circ}\text{C}$) ($i_F = 1.0 \text{ A}, T_J = 150^{\circ}\text{C}$)	VF	0.875 0.66	V
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, T _J = 25°C) (Rated dc Voltage, T _J = 150°C)	İR	2.0 50	μΑ
Maximum Reverse Recovery Time (i _F = 1.0 A, di/dt = 50 A/μs)	t _{rr}	30	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{2.} In compliance with JEDEC 51, these values (historically represented by R_{θ,JL}) are now referenced as Psi_{JL}.

^{3.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

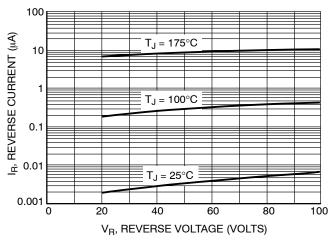


Figure 1. Typical Reverse Current

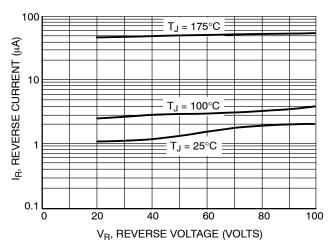


Figure 2. Maximum Reverse Current

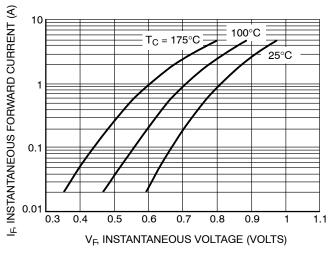


Figure 3. Typical Forward Voltage

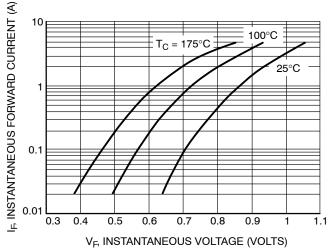


Figure 4. Maximum Forward Voltage

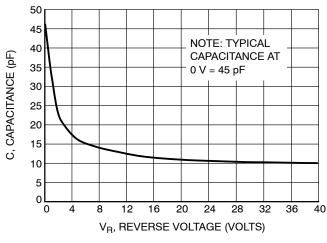


Figure 5. Typical Capacitance

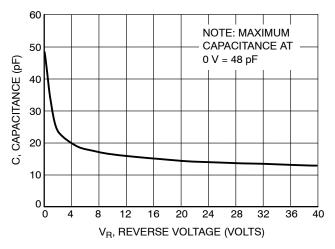


Figure 6. Maximum Capacitance

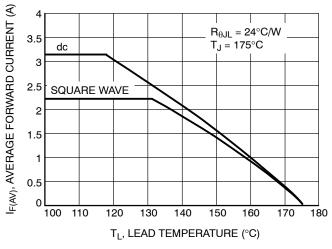


Figure 7. Current Derating, Lead

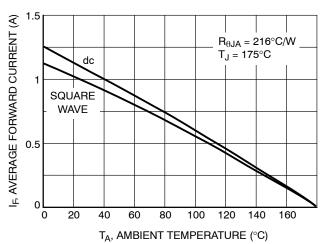


Figure 8. Current Derating, Ambient (FR-4 Board with Minimum Pad)

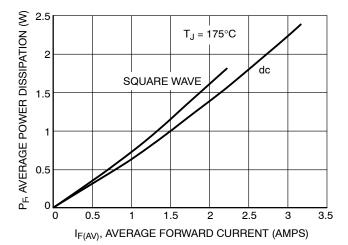
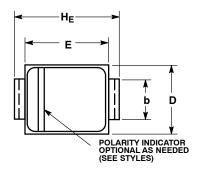


Figure 9. Power Dissipation

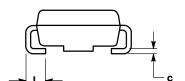
PACKAGE DIMENSIONS

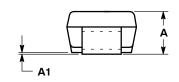
SMA CASE 403D **ISSUE H**



- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 DIMENSION b SHALL BE MEASURED WITHIN DIMENSION L.

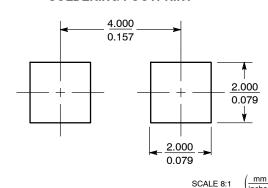
		MILLIMETERS			INCHES		
	MIC	MIN	NOM	MAX	MIN	NOM	MAX
	Α	1.97	2.10	2.20	0.078	0.083	0.087
	A 1	0.05	0.10	0.20	0.002	0.004	0.008
	b	1.27	1.45	1.63	0.050	0.057	0.064
	O	0.15	0.28	0.41	0.006	0.011	0.016
	D	2.29	2.60	2.92	0.090	0.103	0.115
	E	4.06	4.32	4.57	0.160	0.170	0.180
H	ΗE	4.83	5.21	5.59	0.190	0.205	0.220
	L	0.76	1.14	1.52	0.030	0.045	0.060





STYLE 1:
PIN 1. CATHODE (POLARITY BAND)

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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