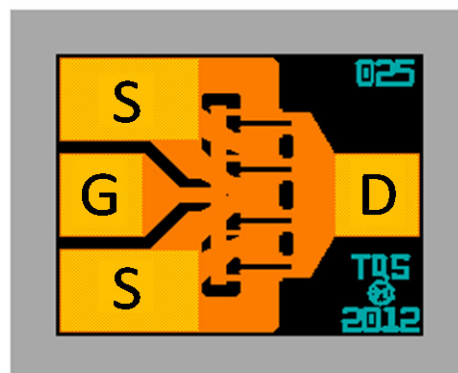


Applications

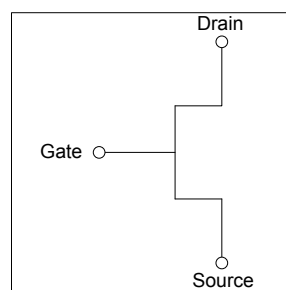
- Defense & Aerospace
- High-Reliability
- Test and Measurement
- Commercial
- Broadband Wireless



Product Features

- Frequency Range: DC - 20 GHz
- 24 dBm Typical Output Power - P1dB
- 14 dB Typical Gain at 12 GHz
- 58% Typical PAE at 12 GHz
- 0.9 dB Typical NF at 12 GHz
- No Vias
- Technology: 0.25 um GaAs pHEMT
- Chip Dimensions: 0.41 x 0.34 x 0.10 mm

Functional Block Diagram



General Description

The TriQuint TGF2025 is a discrete 250 micron pHEMT which operates from DC to 20 GHz. The TGF2025 is fabricated using TriQuint's proven standard 0.25 um power pHEMT production process. This process features advanced techniques to optimize microwave power and efficiency at high drain bias operating conditions.

The TGF2025 typically provides 24 dBm of output power at P1dB with gain of 14 dB and 58% power-added efficiency at 1 dB compression. This performance makes the TGF2025 appropriate for high efficiency applications. The protective overcoat layer with silicon nitride provides a level of environmental robustness and scratch protection.

The TGF2025 is lead-free and RoHS compliant.

Pad Configuration

Pad Dimensions	Terminals
G (71um X 71um)	Gate
D (71um X 71um)	Drain
S (121um X 71um)	Source

Ordering Information

Part	ECCN	Description
TGF2025	EAR99	250 um GaAs pHEMT

Absolute Maximum Ratings

Symbol	Parameter	Absolute	Continuous	Units
V_{DS}	Drain-Source Voltage ⁽²⁾	12	8	V
V_{GS}	Gate- Source Voltage	-7	-3	V
I_{DS}	Drain Current ⁽²⁾	I_{DSS}	I_{DSS}	mA
$I_{G,F}$	Forward Gate Current	12	2	mA
T_{CH}	Channel Temperature ⁽³⁾	175 ⁽⁴⁾	150 ⁽⁵⁾	°C
T_{STG}	Storage Temperature	-65 to 150	-65 to 150	°C
P_{IN}	Input Continuous Wave Power ⁽²⁾	18	at 3 dB Compression	dBm
P_{TOT}	Total Power Dissipation	1.34	0.89	W

Notes:

1. These ratings represent the maximum operable values for this device. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device and/or affect device lifetime. These are stress ratings only, and functional operation of the device at these conditions is not implied.
2. Combinations of supply voltage, supply current, input power, and output power shall not exceed the maximum total power dissipation listed in the table.
3. Junction operating temperature will directly affect the device median time to failure. For maximum life, it is recommended that junction temperatures be maintained at the lowest possible levels.
4. When operated at this channel temperature, the median life is 1.0E+5 hours.
5. When operated at this channel temperature, the median life is 1.0E+6 hours.

Electrical Characteristics

Test conditions unless otherwise noted: Temperature = 25°C

Symbol	Parameter	Conditions	Min	Typ	Max	Units
P1dB	Output Power at 1dB Compression	Freq = 12 GHz $V_{DS} = 8\text{ V}$ $I_{DS} = 50\% I_{DSS}$		24		dBm
G1dB	Gain at P1dB			14		dB
PAE	PAE at P1dB			58		%
NF	50 ohm Noise Figure	$V_{DS} = 2\text{ V}$, $I_{DS} = 19\text{ mA}$		0.90		dB
I_{DSS}	Saturated Drain Current	$V_{DS} = 2\text{ V}$, $V_{GS} = 0\text{ V}$	50	81 ⁽¹⁾	112	mA
Gm	Transconductance	$V_{DS} = 2\text{ V}$, $I_{DS} = 50\% I_{DSS}$		97		mS
V_P	Pinch-Off Voltage	$V_{DS} = 2\text{ V}$, $I_{DS} = 0.18\text{ mA}$	-1.5	-1.0	-0.5	V
BV_{GD}	Gate-Drain Breakdown Voltage	$I_G = 0.18\text{ mA}$, source open		-15	-12	V
BV_{GS}	Gate-Source Breakdown Voltage	$I_G = 0.18\text{ mA}$, drain open		-15		V
R_{TH}	Thermal Resistance ⁽²⁾	AuSn eutectic attach		62.5		°C/W

Notes:

1. Typical Standard Deviation of 2 mA (1 σ).
2. Based on IR Scan

S-Parameters

Test Conditions: $V_{DS}=+8$ V (typ.), $I_{DS}=50\%$ I_{DSS} , Temp= $+25^{\circ}\text{C}$, 50 Ω system

Freq (GHz)	S11 (mag)	S11 (ang)	S21 (mag)	S21 (ang)	S12 (mag)	S12 (ang)	S22 (mag)	S22 (ang)
1	0.98	-25.0	7.88	162.0	0.020	72.9	0.73	-10.1
2	0.95	-49.0	7.45	146.6	0.037	61.3	0.70	-19.7
3	0.92	-71.5	6.86	132.2	0.051	49.9	0.66	-27.9
4	0.88	-92.3	6.23	118.9	0.062	39.4	0.61	-35.0
5	0.85	-110.7	5.62	106.9	0.069	30.2	0.56	-41.4
6	0.83	-127.1	5.04	96.2	0.074	22.3	0.52	-46.8
7	0.82	-141.7	4.54	86.5	0.077	15.3	0.49	-51.4
8	0.81	-155.0	4.10	77.6	0.079	9.3	0.46	-55.0
9	0.81	-167.1	3.70	69.2	0.079	3.7	0.44	-59.1
10	0.80	-177.6	3.35	61.6	0.079	-1.4	0.42	-63.0
11	0.80	173.2	3.04	54.5	0.078	-5.5	0.40	-67.2
12	0.81	164.9	2.78	47.6	0.076	-9.4	0.39	-71.3
13	0.81	157.4	2.56	40.8	0.075	-13.2	0.39	-75.8
14	0.82	150.9	2.36	34.4	0.073	-16.5	0.38	-80.1
15	0.83	144.8	2.19	28.8	0.071	-18.8	0.37	-84.6
16	0.84	139.0	2.04	23.2	0.069	-20.8	0.37	-88.7
17	0.85	134.0	1.90	17.7	0.068	-22.8	0.36	-94.5
18	0.85	128.9	1.76	12.3	0.066	-24.8	0.36	-100.3
19	0.84	124.6	1.64	7.5	0.063	-26.0	0.37	-105.2
20	0.86	121.5	1.56	2.6	0.062	-26.4	0.38	-110.5
21	0.88	116.9	1.47	-2.6	0.061	-27.4	0.38	-116.5
22	0.89	113.5	1.38	-7.7	0.059	-28.4	0.39	-122.0
23	0.89	110.1	1.31	-12.7	0.058	-29.4	0.39	-127.3
24	0.89	106.9	1.23	-17.4	0.057	-29.1	0.40	-133.1
25	0.90	103.9	1.16	-22.2	0.056	-28.9	0.41	-138.7
26	0.91	100.9	1.10	-26.9	0.056	-29.2	0.42	-144.3

Includes 1 bond wire on Gate, 1 bond wire on Drain, and 3 bond wires on each Source pad.

Noise Parameters

$V_{DS} = 2\text{ V}$, $I_{DS} = 40\text{ mA}$, Temp. = 25°C

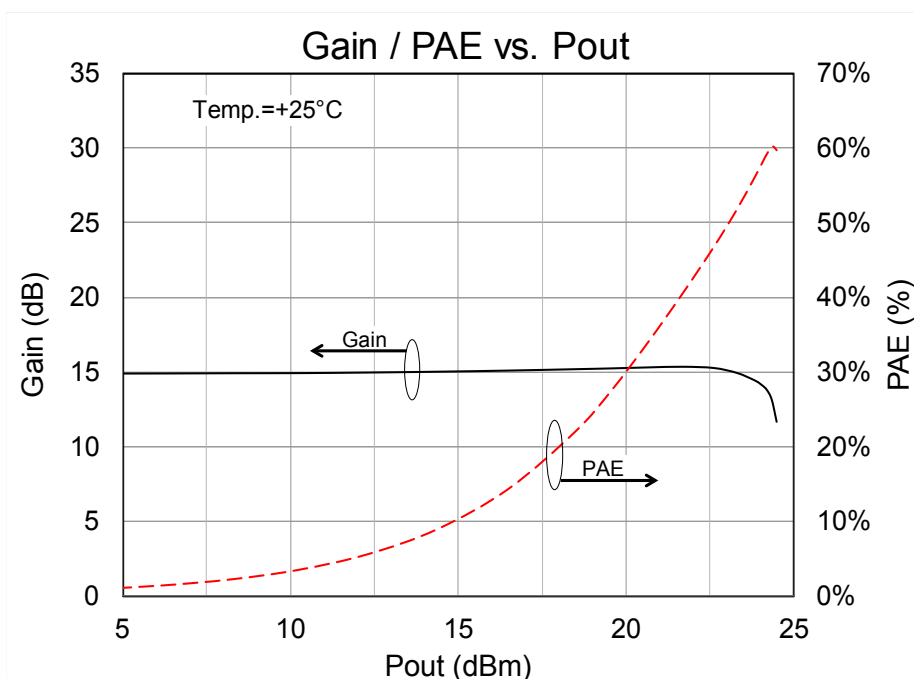
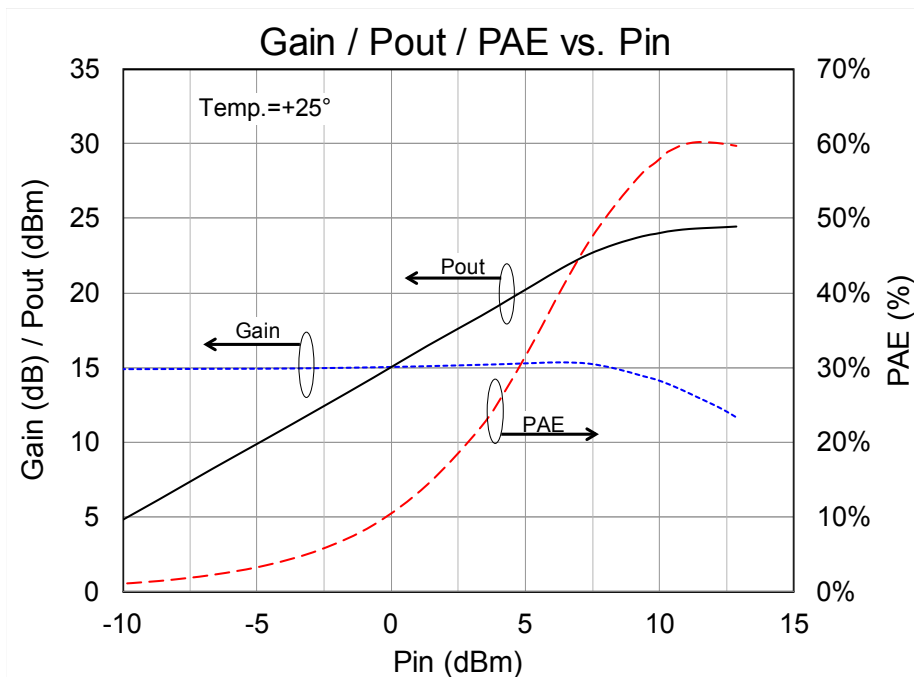
Frequency	NFmin	Rn/50	Gamma	
GHz	dB	Ω	Mag.	Angle
2	0.79	0.29	0.49	24.8
3	0.81	0.271	0.49	36.8
4	0.83	0.247	0.50	51.7
5	0.87	0.225	0.51	67.6
6	0.96	0.197	0.52	82.7
7	1.07	0.168	0.53	97.0
8	1.17	0.141	0.54	110.5
9	1.26	0.118	0.55	123.2
10	1.35	0.098	0.55	135.2
11	1.46	0.081	0.56	146.5
12	1.57	0.067	0.57	157.0
13	1.67	0.060	0.57	167.0
14	1.80	0.060	0.58	176.2
15	1.92	0.067	0.58	-175.1
16	2.06	0.084	0.59	-167.0
17	2.19	0.111	0.59	-159.5
18	2.33	0.149	0.59	-152.5
19	2.48	0.197	0.6	-146.1
20	2.64	0.262	0.6	-140.1
21	2.83	0.346	0.6	-134.6
22	3.03	0.45	0.6	-129.5
23	3.27	0.567	0.61	-124.9
24	3.45	0.695	0.61	-120.6
25	3.58	0.825	0.61	-117.0
26	3.67	0.942	0.61	-114.4

$V_{DS} = 2\text{ V}$, $I_{DS} = 20\text{ mA}$, Temp. = 25°C

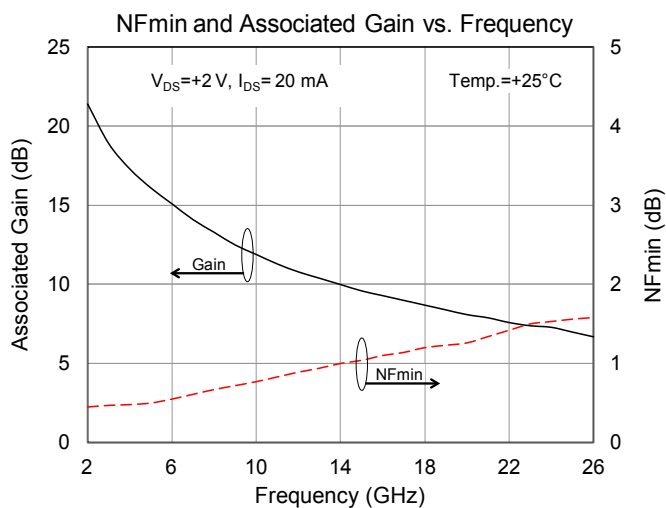
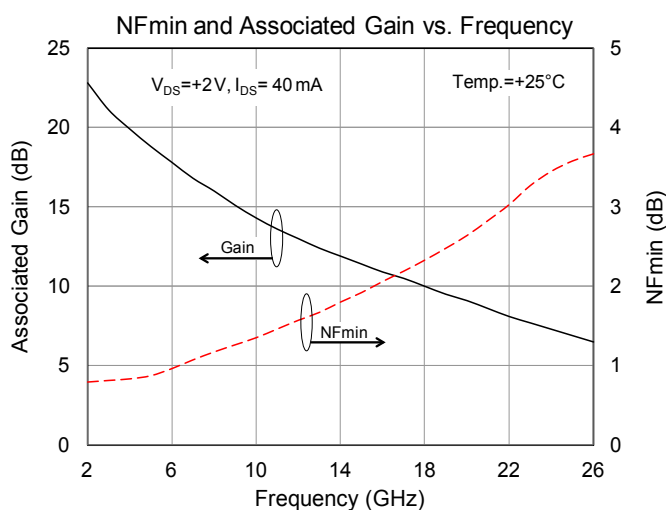
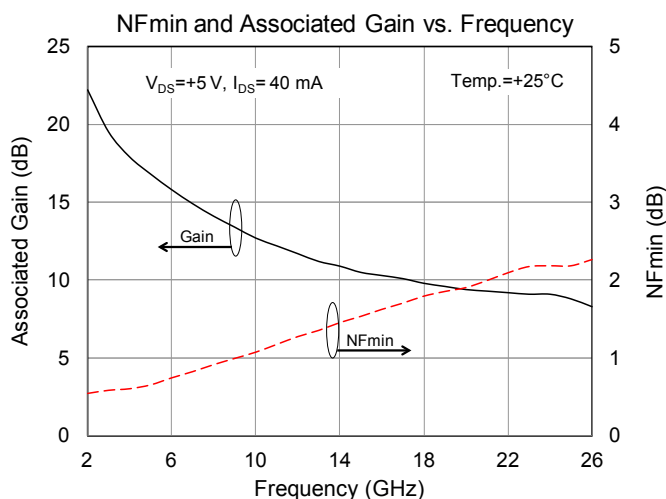
Frequency	NFmin	Rn/50	Gamma	
GHz	dB	Ω	Mag.	Angle
2	0.45	0.180	0.66	17.3
3	0.47	0.173	0.63	25.7
4	0.48	0.164	0.59	36.6
5	0.50	0.155	0.56	48.7
6	0.55	0.140	0.53	60.8
7	0.61	0.127	0.50	72.9
8	0.67	0.113	0.48	84.8
9	0.72	0.102	0.46	96.7
10	0.77	0.092	0.44	108.3
11	0.83	0.084	0.43	119.7
12	0.89	0.076	0.42	130.8
13	0.94	0.070	0.41	141.6
14	1.00	0.066	0.41	152.1
15	1.04	0.063	0.42	162.1
16	1.10	0.062	0.42	171.8
17	1.14	0.064	0.43	-179.1
18	1.20	0.067	0.45	-170.5
19	1.23	0.072	0.46	-162.4
20	1.26	0.082	0.49	-155.0
21	1.34	0.097	0.51	-148.2
22	1.42	0.119	0.54	-142.1
23	1.50	0.143	0.57	-136.8
24	1.53	0.166	0.61	-132.2
25	1.56	0.186	0.65	-128.7
26	1.58	0.203	0.68	-126.3

RF Tuned Data at 12 GHz

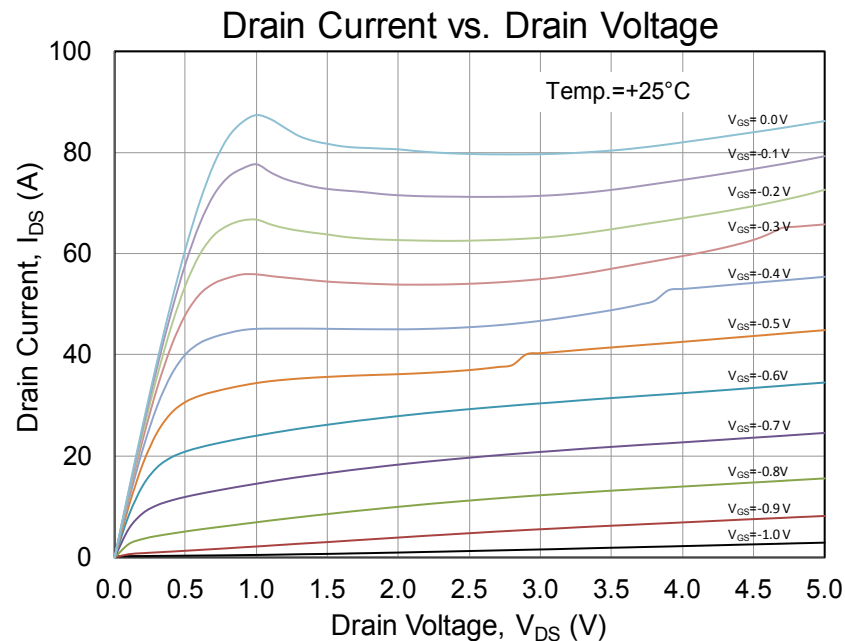
Bias conditions unless otherwise noted: $V_D = 8\text{ V}$, $I_{DQ} = 50\% I_{DSS}$, $F = 12\text{ GHz}$



Typical Performance – Various Bias Conditions



DC Characteristics



Assembly Notes

Component placement and adhesive attachment assembly notes:

- Vacuum pencils and/or vacuum collets are the preferred method of pick up.
- Air bridges must be avoided during placement.
- The force impact is critical during auto placement.
- Organic attachment (i.e. epoxy) can be used in low-power applications.
- Curing should be done in a convection oven; proper exhaust is a safety concern.

Reflow process assembly notes:

- Recommend Eutectic die attach with AuSn (80/20) solder and limit exposure to temperatures above 300°C to 30 seconds, maximum.
- An alloy station or conveyor furnace with reducing atmosphere should be used.
- Do not use any kind of flux.
- Coefficient of thermal expansion matching is critical for long-term reliability.
- Devices must be stored in a dry nitrogen atmosphere.

Interconnect process assembly notes:

- Either Thermo-compression Wedge Bonding or Thermosonic Ball Bonding can be used to bond onto the die.
- Force, time, and ultrasonics are critical bonding parameters.
- Aluminum wire should not be used.
- Devices with small pad sizes should be bonded with 0.0008-inch wire.

Product Compliance Information

ESD Sensitivity



Caution! ESD-Sensitive Device

GaAs devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

Not HAST compliant.

RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free

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