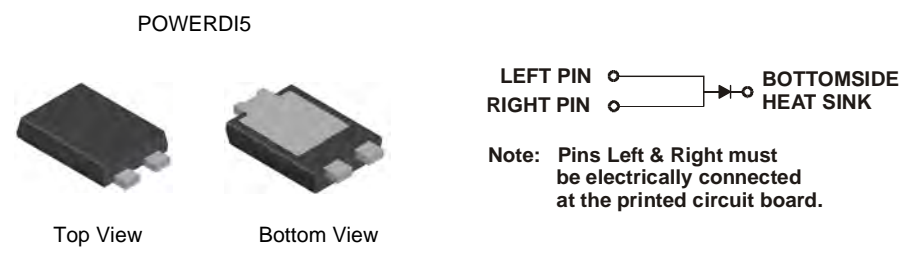


### Features

- Ultra Low Forward Voltage Drop
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

### Mechanical Data

- Case: POWERDI5
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Polarity: See Below
- Weight: 0.093 grams (approximate)

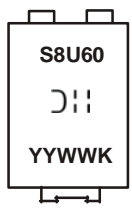


### Ordering Information (Note 4)

Part Number	Case	Packaging
SBR8U60P5-13	POWERDI5	5000/Tape & Reel
SBR8U60P5-13D (Note 5)	POWERDI5	5000/Tape & Reel
SBR8U60P5-7	POWERDI5	1500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.
  5. "D" suffix designate for the 12mm Tape and Reel option.

### Marking Information



- S8U60 = Product Type Marking Code
- ⌐⌐⌐ = Manufacturers' Code Marking
- YYWW = Date Code Marking
- YY = Last Two Digits of Year (ex: 13 for 2013)
- WW = Week Code (01 - 53)
- K = Factory Designator

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	60	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>RM</sub>		
Average Rectified Output Current	I <sub>O</sub>	8	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	280	A
Repetitive Peak Avalanche Power (1μs, +25°C)	P <sub>ARM</sub>	5,000	W

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance	R <sub>θJS</sub>	3	°C/W
Thermal Resistance Junction to Soldering (Note 6)			
Thermal Resistance Junction to Ambient (Note 7)			
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V <sub>F</sub>	—	0.30	0.35	V	I <sub>F</sub> = 1.0A, T <sub>J</sub> = +25°C
		—	0.46	0.53		I <sub>F</sub> = 8A, T <sub>J</sub> = +25°C
		—	—	0.5		I <sub>F</sub> = 8A, T <sub>J</sub> = +125°C
Leakage Current (Note 8)	I <sub>R</sub>	—	0.12	0.6	mA	V <sub>R</sub> = 60V, T <sub>J</sub> = +25°C
		—	—	100		V <sub>R</sub> = 60V, T <sub>J</sub> = +125°C

- Notes:
6. Theoretical R<sub>θJS</sub> calculated from the top center of the die straight down to the PCB cathode tab solder junction.
  7. Polyimide PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com>.
  8. Short duration pulse test used to minimize self-heating effect.

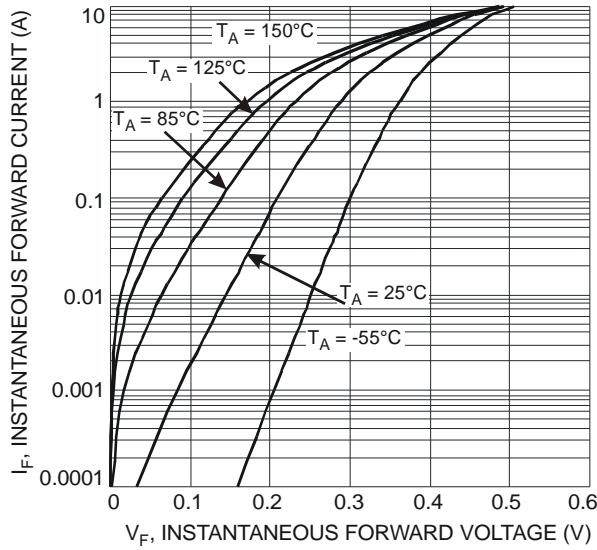


Fig. 1 Typical Forward Characteristics

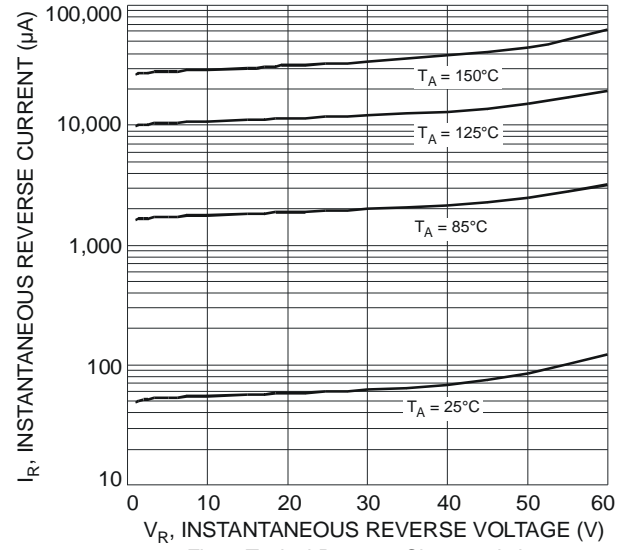


Fig. 2 Typical Reverse Characteristics

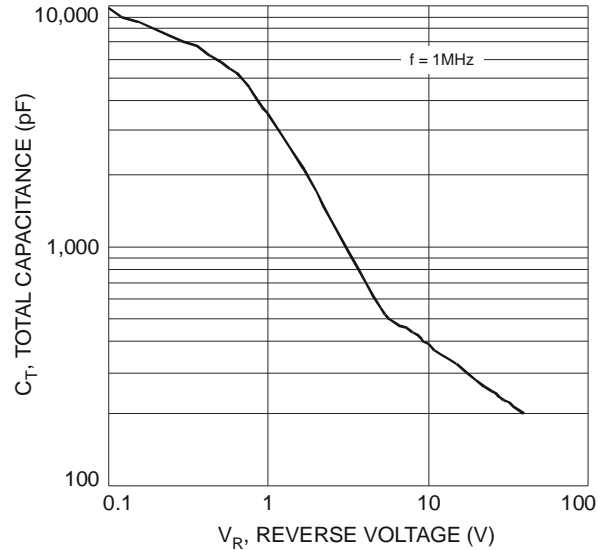


Fig. 3 Typical Total Capacitance

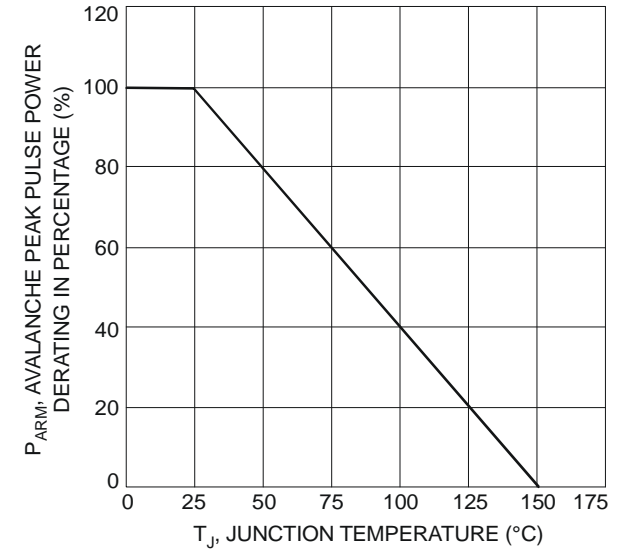


Fig. 4 Pulse Derating Curve

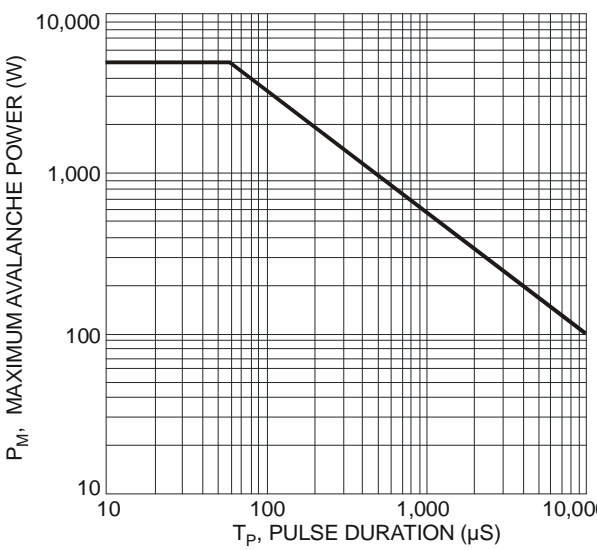


Fig. 5 Maximum Avalanche Power vs. Pulse Duration

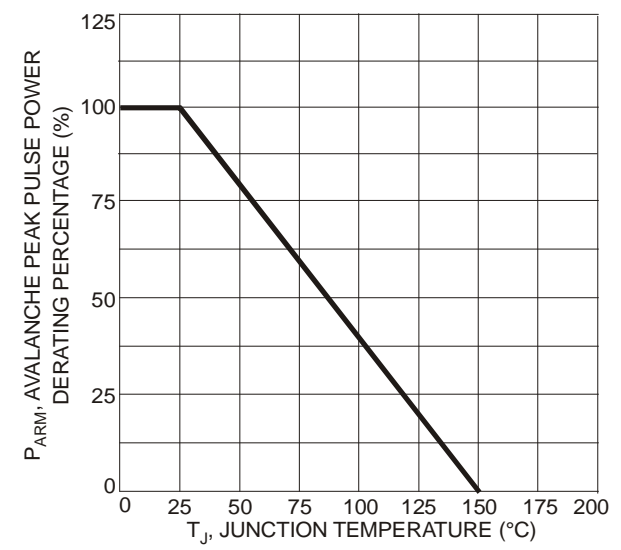


Fig. 6 Pulse Derating Curve

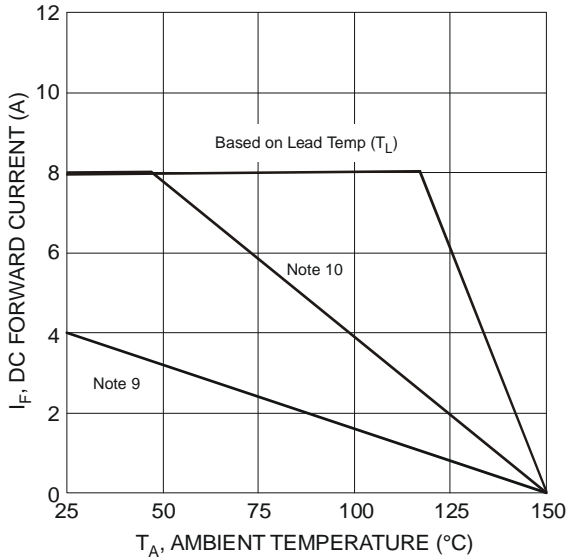


Figure 7 DC Forward Current Derating Curve

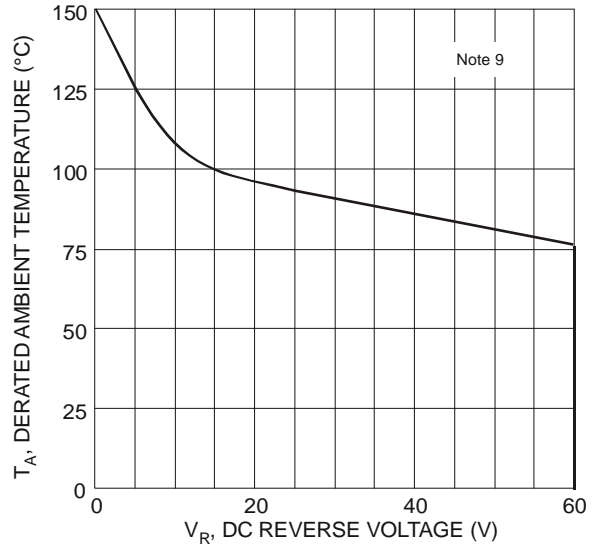
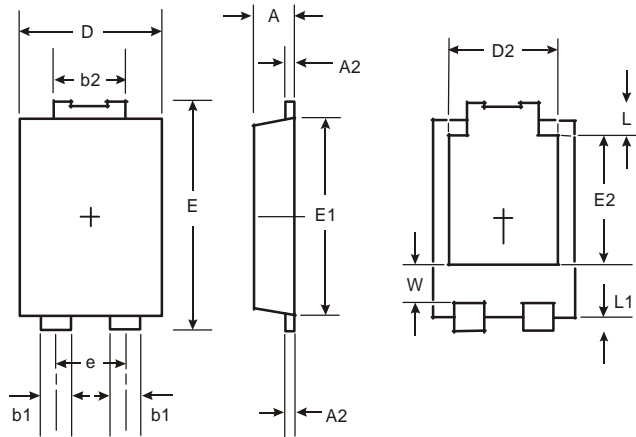


Figure 8 Operating Temperature Derating

- Notes: 9. Device mounted on FR-4 substrate, 2oz copper, with minimum recommended pad layout.  
10. Device mounted on FR-4 substrate, 2oz copper, with 10cm x 10cm pad layout.

## Package Outline Dimensions

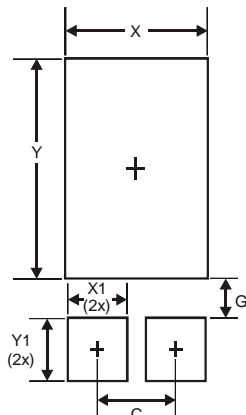
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



POWERDI5		
Dim	Min	Max
A	1.05	1.15
A2	0.33	0.43
b1	0.80	0.99
b2	1.70	1.88
D	3.90	4.05
D2	3.054 Typ	
E	6.40	6.60
e	1.84 Typ	
E1	5.30	5.45
E2	3.549 Typ	
L	0.75	0.95
L1	0.50	0.65
W	1.10	1.41
All Dimensions in mm		

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	1.840
G	0.852
X	3.360
X1	1.390
Y	4.860
Y1	1.400

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