





		REVISIONS			
Ρ	LTR	DESCRIPTION	DATE	DWN	APVD
	C4	REVISED PER ECO-11-005027	11MAR11	RK	HMR
	D	REVISED PER ECO-17-016552	21NOV2017	RS	JO

			D REVISED	PER EC0-17-01	16552	2	1NOV2017 RS JU					
	0.00 (LOC	051[.00002 ALIZED GOL	0] GOLD AT POINT OF MEASUREMENT, 0] MIN AT THE END POINTS OF AREA G, .D PLATE AREA), 0.0038[.000150] TIN-LEAD ON PLATED AREA, ALL OVER 0.0013[.000050] NICKEL									
				0010] DRILLED HOLE (1.55mm DRILL). OVER 0.02 [.001] MIN COPPER.								
$\sqrt{3}$	L DIMENSION APPLIES AT BASE OF SHROUD.											
4	THE NOTED DIMENSIONS APPLY AT THE MATING FACE OF THE HOUSING.											
	0.0038 [.000150] TIN-LEAD ON HOLD DOWN,											
6.	TO INTERCONNECT 2 BOARDS, PLEASE REFER TO THE SPACING PARAGRAPH IN APPLICATION SPEC, #114-7010											
	POINT OF MEASUREMENT.											
203	B DIMENSIONS NOTED APPLY FROM THE BASIC DIMENSION LINE (NOT THE CIRCUIT CAVITY CENTER LINE) TO THE SURFACE INDICATED.											
	PACKAGED IN TAPE AND REEL PER EIA-481 SPECIFICATIONS, SEE CHART FOR TAPE WIDTHS.											
	VACUUM COVER DESIGNED FOR 4.0 [.160] DIA. NOZZLE. VACUUM COVER TO BE REMOVED AFTER SOLDERING.											
$\wedge$	5.5 [.216] MIN TARGET AREA FOR VACUUM PICK-UP.											
$\overline{\wedge}$	VACUUM COVER SHOWN IN PHANTOM LINE.											
HOUSING: LCP, COLOR-BLACK. POST: PHOSPHOR BRONZE. HOLD DOWN: COPPER ALLOY												
$\wedge$	VACUUM COVER: ALUMINUM. 14 0.00076(.000030) GOLD AT POINT OF MEASUREMENT,											
	0.00051(.000020) MIN AT THE END POINTS OF AREA G, (LOCALIZED											
GOLD PLATE AREA), 0.0038(.000150) TIN ON LOCALIZED TIN PLATED AREA, ALL OVER 0.0013(.000050) NICKEL.												
				/0010) DF VER 0.02(.00		COPPER.						
16	HOLE FINISH TO BE TIN OVER 0.02(.001)MIN COPPER. 0.0038(.000150) TIN ON HOLDDOWN, ALL OVER .0013(.000050) NICKEL.											
$\wedge$			/EC COMPL									
65.3	33	32.66	66.59	64.05	49	100	<del>5–147382–</del> 9					
[2.57 52.6	53	[1.286]	[2.622] 53.89	[2.522] _51.35	39							
[2.07	28	[1.036] 23.13	47.54	[2.022] 45.00	39	80 70	5-147382-8 5-147382-7					
[1.82 39.9		[.911] 19.96	[1.872] 41.19	[1.772] 38.65								
[1.57	72]	[.786] 16.78	[1.622] 34.84	[1.522] 32.30	29	60	5-147382-6					
[1.32	22]	[.661] 13.61	[1.372]	[1.272] 25.95	24	50	<del>5-147382</del> -5					
[1.07	72]	[.536]	[1.122]	[1.022]	19	40	5-147382-4					
20.8 [.82	2]	10.43 [.411]	22.14 [.872]	19.60 [.772]	14	30	5-147382-3					
14.5 [.57	I	7.26 [.286]	15.79 [.622]	13.25 [.522]	9	20	5-147382-2	В				
8.1 [.32	I	4.08 [.161]	9.44 [.372]	6.90 [.272]	4	10	5-147382-1					
65.3	33	32.66	66.59	64.05	49	100	147382-9					
[2.57 52.6	53	[1.286] _26.31	[2.622] 53.89	[2.522] 51.35	39	80	<del>147382-8</del>					
[2.072] 46.28		[1.036] 23.13	[2.122] 47.54	[2.022] 45.00								
[1.822] 39.93		[.911]	[1.872] 41.19	[1.772] 38.65	34	70	<del>147382-7</del>					
[1.572]		[.786]	[1.622]	[1.522]	29	60	<del>147382-6</del>					
33.58 [1.322]		16.78 [.661]	34.84 [1.372]	32.30 [1.272]	24	50	<del>147382<b>-</b>5</del>					
27.2 [1.07		13.61 [.536]	28.49 [1.122]	25.95 [1.022]	19	40	147382-4					
20.88 [.822]		10.43 [.411]	22.14 [.872]	19.60 [.772]	14	30	<del>147382-3</del> -					
14.53 [.572]		7.26 [.286]	15.79 [.622]	13.25 [.522]	9	20	147382-2					
8.1 [.32	I	4.08 [.161]	9.44 [.372]	6.90 [.272]	4	10	147382-1					
E		D	С	В	A	NUMBER OF POSITIONS	PART NUMBER	A				
DNTROLLED D					<b>TE</b>		l nectivity					
TOLERANCES OTHERWISE	S UNLESS SPECIFIE	D: APVD	28JAN00	NAME								
0 PLC ± 1 PLC ± 2 PLC ±		product spec	1332	HEADER ASSEMBLY,SURFACE MOUNT, AMPMODU 50/50 GRID (8.12 [ 320] MATED HEICHT)								
	- 0.13[.00 - ± -	-	7010	- (8.12 [.320] MATED HEIGHT)								
SEE TABLE												
ı	CUSTOMER DRAWING											

## **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

TE Connectivity: 5-147382-1