TOSHIBA Photocoupler GaAlAs IRed & Photo-Triac

TLP168J

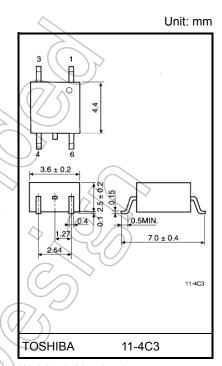
Triac Driver Programmable Controllers AC-Output Modules Solid State Relays

The TOSHIBA mini-flat coupler TLP168J is a small-outline coupler suitable for surface mount assembly.

The TLP168J consists of a GaAlAs infrared emitting diode optically coupled to a triac-output photocoupler.

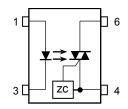
- Peak off-state voltage: 600 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 70 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL approved: UL1577, File No.E67349
- cUL approved : CSA Component Acceptance Service
 No. 5A, File No.E67349
- Option (V4) VDE approved : EN60747-5-5 (Note1)

Note 1: When a EN60747-5-5 approved type is needed, please designate "Option(V4)"



Weight: 0.09 g (typ.)

Pin Configurations (top view)



- 1: Anode
- 3: Cathode
- 4: Triac Terminal
- 6: Triac Terminal

Start of commercial production 1993-01

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Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit		
	Forward current		lF	20	mA	
LED	Forward current derating (Ta ≥ 25°C)		ΔIF / °C	-0.2	mA / °C	
	Peak forward current (100 µs pulse, 100 pps)		IFP	1	А	
	Reverse voltage		VR	5	V	
	Diode power dissipation		PD	100	mW	
	Diode power dissipation derating (Ta ≥ 25°C)		ΔP _D /°C	-1.0	mW/°C	((//)
	Junction temperature		Tj	125	°C	
	Off-state output terminal voltage		V _{DRM}	600	v ((
	On-state RMS current	Ta = 25°C	I _{T(RMS)}	70	mA	
	On-state RIVIS current	Ta = 70°C		40	JINA V	\supset \swarrow
	On-state current derating (Ta ≥ 25°C)		ΔI _T / °C	-0.67	mA/°C	2
Detector	Peak on–state current (100 µs pulse, 120 pps)		I _{TP}	2		4 (9)/
	Peak non-repetitive surge current (P _W =10 ms)		I _{TSM}	1.2	> A	
	Output power dissipation		Po	200	mW	
	Output power dissipation derating (Ta ≥ 25°C)		ΔP _o /°C	-2.0	mW / °C /	7
	Junction temperature		Ţj	115	°C	(\mathcal{O})
Storage temperature range		T _{stg}	-55 to 125	°C		
Operating temperature range		Topr	-40 to 100	°C		
Lead	soldering temperature (10 s)		T _{sol}	260	°C	/
	tion voltage AC, 60 s, R.H. ≤ 60%)	(Note 1)	BVS	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/ voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/ "Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two-terminal device: Pins 1 and 3 shorted together and Pin 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	240	Vac
Forward current	lF	4.5	6	7.5	mA
Peak on-state current	ITP	_	_	1	Α
Operating temperature	Topr	-10	-	85	°C

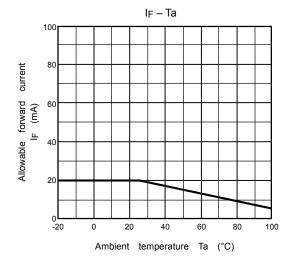
Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

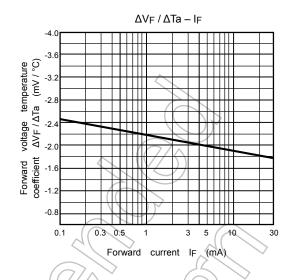
Electrical Characteristics (Ta = 25°C)

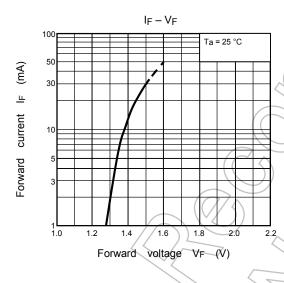
	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	I _F =10 mA	1.2	1.4	1.7	V
	Reverse current	IR	V _R = 3 V	_	_	10	μΑ
	Capacitance	Ст	VF = 0 V, f = 1 MHz	7	30	_	pF
Detector	Peak off-state current	IDRM	V _{DRM} = 600 V		10	1000	nA
	Peak on-state voltage	Vтм	I _{TM} = 70 mA	1))1.7	2.8	V
	Holding current	Ін) <u> </u>	0.6	_	mA
	Critical rate of rise of off-state voltage	dv / dt	V _{in} = 240 Vrms, Ta = 85°C	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	V _{in} = 60 Vrms, I _T = 15 mA	· _	0.2	_	V / µs

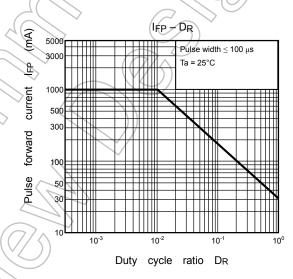
Coupled Electrical Characteristics (Ta = 25°C)

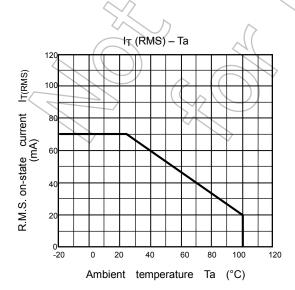
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	V _T = 3V		70	3	mA
Inhibit voltage	VIH	IF = Rated IFT	(-	\ -	50	V
Leakage in inhibited state	Іін	IF = Rated IFT VT = Rated VDRM		200	600	μΑ
Capacitance (input to output)	Cs	V _S = 0 V, f = 1 MHz	2))—	0.8	-	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
		AC, 60 s	2500	_	_	Vrms
Isolation voltage	BVs	AC, 1 s, in oil	_	5000	_	vims
		DC, 60 s, in oil	_	5000	_	Vdc

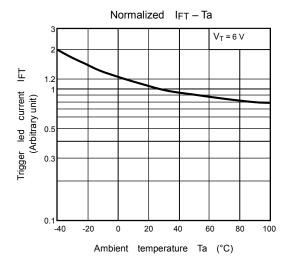


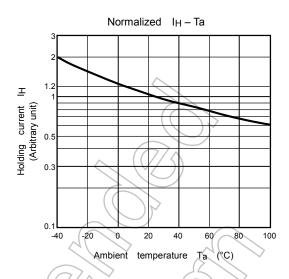


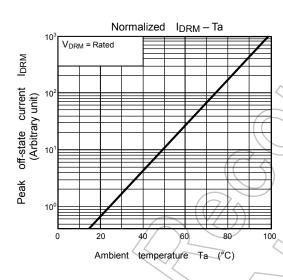


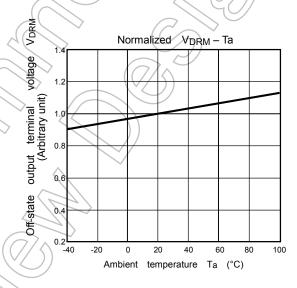


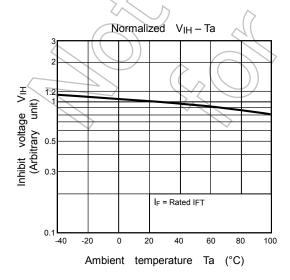


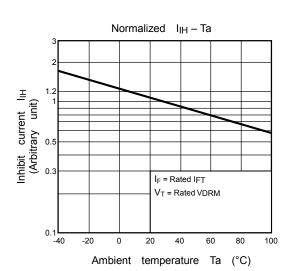












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