

SURFACE MOUNT SILICON UNI-DIRECTIONAL TRANSIENT VOLTAGE SUPPRESSOR

CTLTVS6V2



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CTLTVS6V2 is a low leakage, fast response silicon TVS packaged in an ultra small, ultra low profile TLM2D3D6 surface mount case. This device is designed to protect sensitive equipment against ESD damage.

MARKING CODE: S



APPLICATIONS:

- User interface protection
- Voltage rail protection
- Data line protection
- Voltage clamping

FEATURES:

- Ultra small, ultra low profile 0.3mm x 0.6mm x 0.3mm leadless surface mount package
- · High peak pulse current capability
- · Low leakage current

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Power Dissipation (8x20µs)	P_{PK}	35	W
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40	Α
ESD Voltage (IEC 61000-4-2, Air)	V_{ESD}	15	kV
ESD Voltage (IEC 61000-4-2, Contact)	V_{ESD}	8.0	kV
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS: (TA=25°C) VF=1.0V MAX @ IF=10mA

Maximum Reverse Stand-off Voltage VRWM	,	eakdov Voltage	•	Test Current	Maximum Reverse Leakage Current IR @ VRWM	Clamping TLP Voltage \(8x20\mu s\)		Typical TLP Clamping Voltage (Note 1) VCL @ IPP		Typical Dynamic Resistance (Note 1) RDYN	Typical Junction Capacitance @ 0V Bias CJ
V	MIN V	NOM V	MAX	mA	μΑ	V	Α	v	A	Ω	pF
4.0	<i>E</i> 0	6.0	6.6	F.0	1.0	10.7	2.0	7.0	1.5	0.54	25
4.0	5.8	6.2	6.6	5.0	1.0	10.7	3.0	10	7.0	0.54	25

Note 1: Transmission Line Pulse (TLP) conditions: $Z_0=50\Omega$, tp=100ns

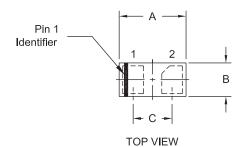
CTLTVS6V2

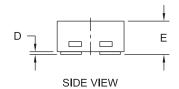
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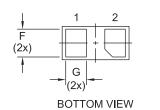


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TLM2D3D6 CASE - MECHANICAL OUTLINE







R2

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.022	0.026	0.55	0.65		
В	0.010	0.014	0.25	0.35		
С	0.014		0.35			
D	0.000	0.002	0.00	0.05		
E	0.011	0.013	0.28	0.32		
F	0.008	0.012	0.20	0.30		
G	0.005	0.009	0.13	0.24		
TI MODODO (DEV DO)						

TLM2D3D6 (REV: R2)

LEAD CODE:

- 1) Cathode
- 2) Anode

MARKING CODE: S

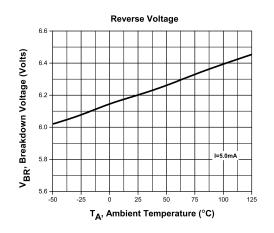


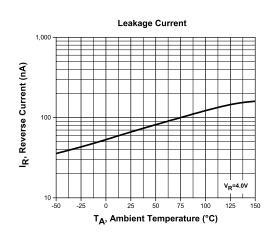
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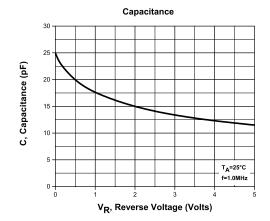


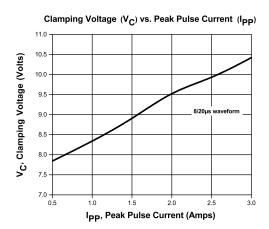
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TYPICAL ELECTRICAL CHARACTERISTICS









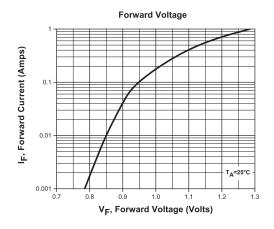


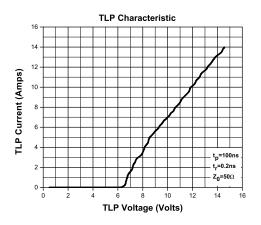
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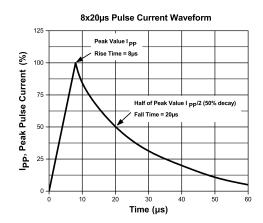


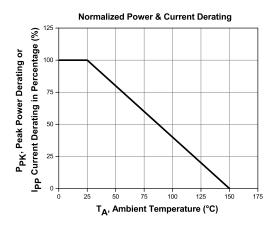
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TYPICAL ELECTRICAL CHARACTERISTICS









OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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