**Product data sheet** 

## 1. General description

Passivated sensitive gate Silicon Controlled Rectifier (SCR) in a SOT428 (DPAK) surface mountable plastic package intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. These devices are intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

### 2. Features and benefits

- Direct interfacing with low power drivers and microcontrollers
- High bidirectional blocking voltage capability
- High junction operating temperature capability
- · High thermal cycling performance
- Planar passivated for voltage ruggedness and reliability
- Surface mountable package
- Very sensitive gate for logic level controls

## 3. Applications

- General purpose switching and phase control
- Ignition circuits, CDI for 2- and 3-wheelers
- Motor control e.g. small kitchen appliances
- Protection circuits for Switched-Mode Power Supplies (SMPS)
- Protection circuits in lighting ballasts

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RRM}$	repetitive peak reverse voltage			-	-	800	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; $T_{mb} \le 135 ^{\circ}\text{C}$ ; Fig. 1		-	-	5	A
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{mb} \le 135 ^{\circ}\text{C}$ ; Fig. 2; Fig. 3		-	-	8	A
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 10 \text{ ms}$ ; $Fig. 4$ ; $Fig. 5$		-	-	75	A
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms		-	-	82	A
Tj	junction temperature		[1]	-	-	150	°C
Static characteristics							

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C}; Fig. 8$		20	-	50	μA
Dynamic chara	Dynamic characteristics						
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; $T_j$ = 150 °C; $R_{GK}$ = 100 Ω; $(V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; Fig. 13		35	70	-	V/µs

<sup>[1]</sup> Operation above junction temperatures of 110  $^{\circ}$ C may require the use of a gate to cathode resistor of 1 k $\Omega$  or less.

## 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		А <del>-    </del> К
2	Α	anode		G sym037
3	G	gate		Symoch
mb	Α	mounting base; connected to anode	DPAK (SOT428)	

# 6. Ordering information

### **Table 3. Ordering information**

Type number		Package		
		Name	Description	Version
	BT258S-800LT	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	SOT428

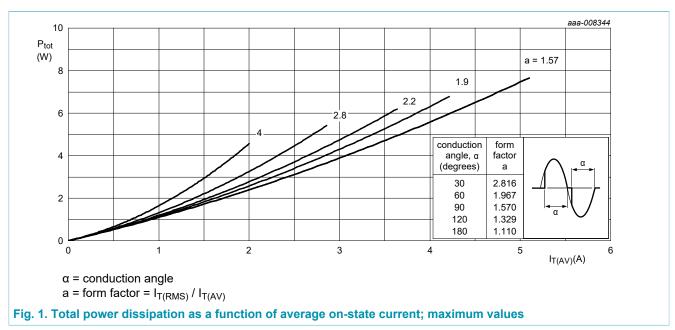
## 7. Limiting values

### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage			-	800	V
$V_{RRM}$	repetitive peak reverse voltage			-	800	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; T <sub>mb</sub> ≤ 135 °C; <u>Fig. 1</u>		-	5	Α
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{mb} \le 135 ^{\circ}\text{C}$ ; Fig. 2; Fig. 3		-	8	Α
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 10 ms; Fig. 4; Fig. 5		-	75	A
		half sine wave; $T_{j(init)} = 25  ^{\circ}C$ ; $t_p = 8.3  ms$		-	82	Α
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; sine-wave pulse		-	28	A²s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 50 mA		-	50	A/µs
$I_{GM}$	peak gate current			-	2	Α
$P_{GM}$	peak gate power			-	5	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period		-	0.5	W
T <sub>stg</sub>	storage temperature			-40	150	°C
Tj	junction temperature		[1]	-	150	°C

[1] Operation above junction temperatures of 110 °C may require the use of a gate to cathode resistor of 1 k $\Omega$  or less.



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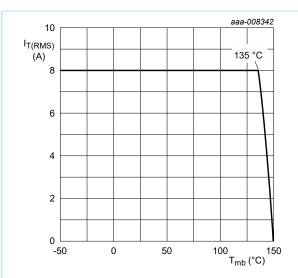


Fig. 2. RMS on-state current as a function of mounting base temperature; maximum values

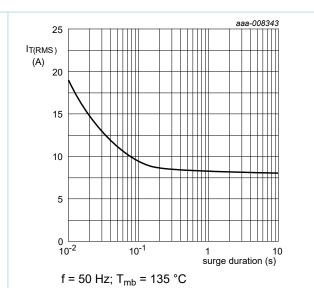


Fig. 3. RMS on-state current as a function of surge duration; maximum values

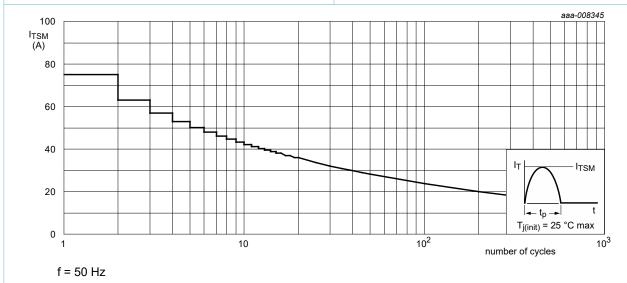
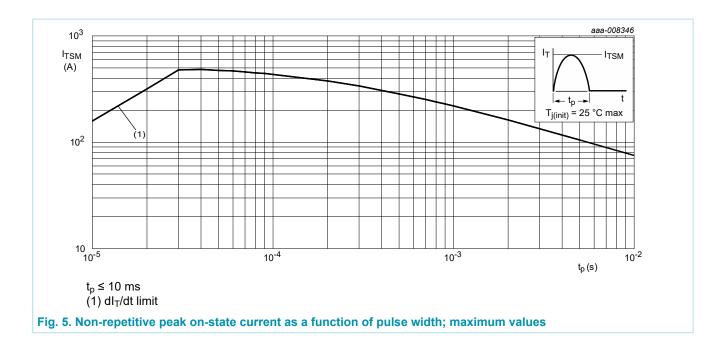


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

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## 8. Thermal characteristics

**Table 5. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	Fig. 6	-	-	2	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	Device mounted on a FR4 printed- circuit board, single-sided copper, tin- plated and standard footprint; Fig. 7	-	75	-	K/W

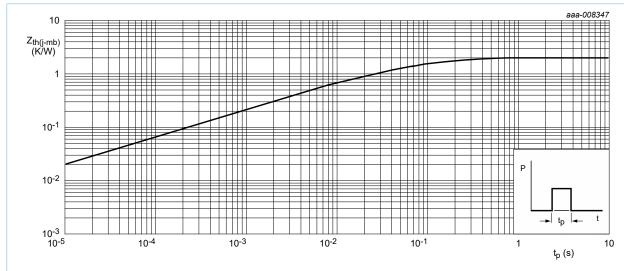
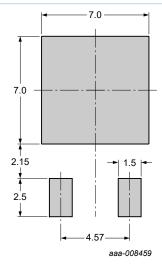


Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse width



All dimensions are in mm

Plastic meets requirements of UL94 V-O at 3.175 mm

Fig. 7. SOT428: minimum pad sizes for surface-mounting

## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics			,		
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C}; Fig. 8$	20	-	50	μΑ
IL	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C}; Fig. 9$	-	0.4	10	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	0.3	6	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 16 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	1.3	1.6	V
$V_{GT}$	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 ^{\circ}\text{C};$ Fig. 12	-	0.4	1	V
		$V_D = 800 \text{ V}; I_T = 0.1 \text{ A}; T_j = 110 ^{\circ}\text{C};$ Fig. 12	0.1	0.2	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 800 V; T <sub>j</sub> = 150 °C	-	0.5	2.5	mA
I <sub>R</sub>	reverse current	V <sub>R</sub> = 800 V; T <sub>j</sub> = 150 °C	-	0.5	2.5	mA
Dynamic ch	naracteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; $T_j$ = 150 °C; $R_{GK}$ = 100 Ω; $(V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; Fig. 13	35	70	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM}$ = 10 A; $V_D$ = 800 V; $I_G$ = 5 mA; $dI_G/dt$ = 0.2 A/ $\mu$ s; $T_j$ = 25 °C	-	2	-	μs

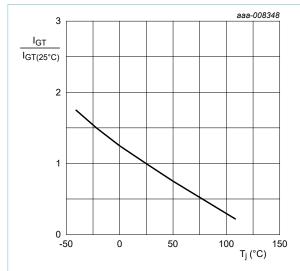


Fig. 8. Normalized gate trigger current as a function of junction temperature

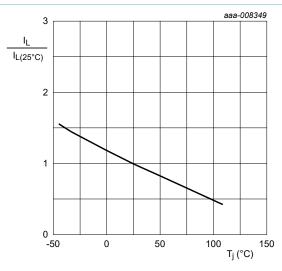


Fig. 9. Normalized latching current as a function of junction temperature

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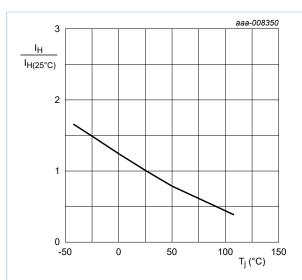
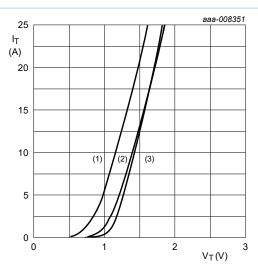


Fig. 10. Normalized holding current as a function of junction temperature



 $V_o = 1.0 \text{ V}; R_s = 0.04 \Omega$ 

(1)  $T_j = 150$  °C; typical values (2)  $T_j = 150$  °C; maximum values

(3) T<sub>i</sub> = 25 °C; maximum values

Fig. 11. On-state current as a function of on-state voltage

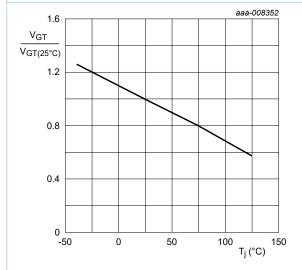


Fig. 12. Normalized gate trigger voltage as a function of junction temperature

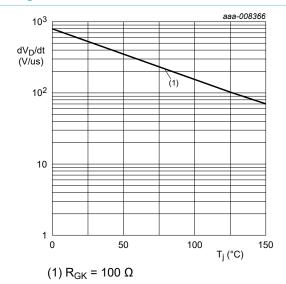
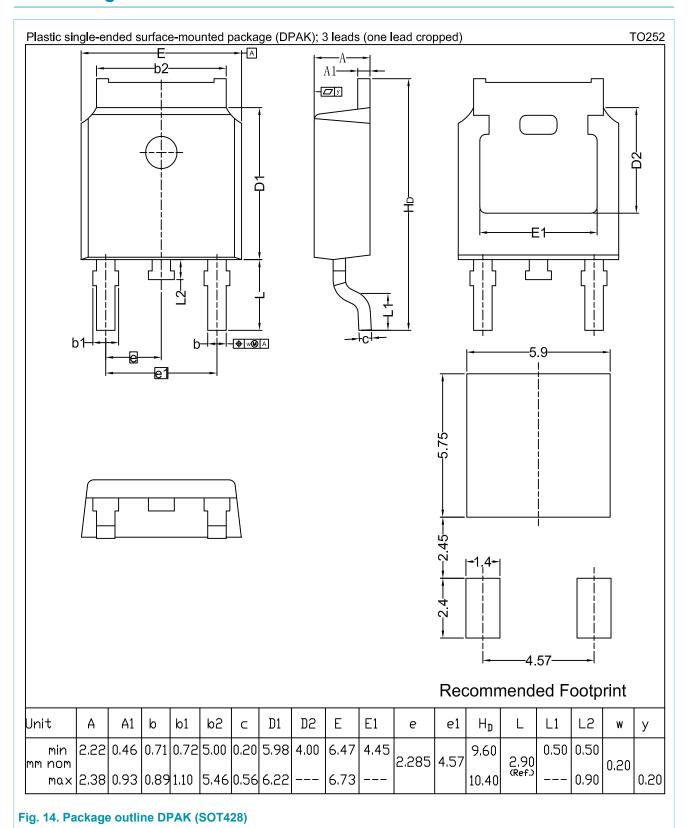


Fig. 13. Critical rate of rise of off-state voltage as a function of junction temperature; typical values

## 10. Package outline



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## 11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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