

Hyperfast Rectifier, 5 A FRED Pt[®]



TO-252AA (D-PAK)

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) 1	3 O
/C	Anode

PRODUCT SUMMARY								
Package	TO-252AA (D-PAK)							
I _{F(AV)}	5 A							
V _R	600 V							
V _F at I _F	1.55 V							
t _{rr} (typ.)	14 ns							
T _J max.	175 °C							
Diode variation	Single die							

FEATURES

- Hyperfast recovery time, extremely low Q_{rr}
- 175 °C maximum operating junction temperature
- For PFC CCM operation
- · Low forward voltage drop
- Low leakage current
- HALOGEN • Meets MSL level 1, per J-STD-020, LF maximum FREE peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Peak repetitive reverse voltage	V _{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 144 °C	5							
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	50	А						
Peak repetitive forward current	I _{FM}	$T_{C} = 144 \ ^{\circ}C, f = 20 \ kHz, d = 50 \ \%$	10							
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-				
	V _F	I _F = 5 A	-	2.39	2.9	V			
Forward voltage	۷F	I _F = 5 A, T _J = 150 °C	-	1.55	1.8				
		$V_{R} = V_{R}$ rated	-	-	20				
Reverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μΑ			
Junction capacitance	CT	V _R = 600 V	-	3.5	-	pF			
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	8	-	nH			

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RoHS COMPLIANT



DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 10$	00 A/µs, V _R = 30 V	-	14	21				
	+	$I_F = 1 \text{ A}, dI_F/dt = 50$	-	16	-	ns				
	t _{rr}	T _J = 25 °C		-	18	-	- A - nC			
		T _J = 125 °C		-	27	-				
Pools receivers ourrent	I _{RRM}	T _J = 25 °C	l _F = 5 A dI _F /dt = 200 A/µs	-	3.0	-				
Peak recovery current		T _J = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	3.9	-				
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	26	-				
		T _J = 125 °C		-	54	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C				
Thermal resistance, junction to case per leg	R _{thJC}		-	-	3	°C/W				
Approximate weight				0.3		g				
Approximate weight			0.01			oz.				
Marking device		Case style TO-252AA (D-PAK)	5EWX06FN							

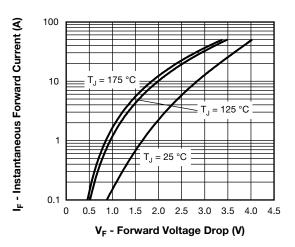
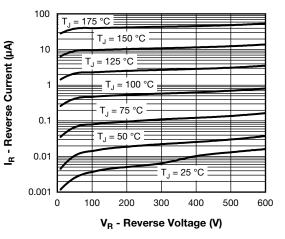
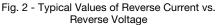


Fig. 1 - Typical Forward Voltage Drop Characteristics







VS-5EWX06FN-M3

Vishay Semiconductors

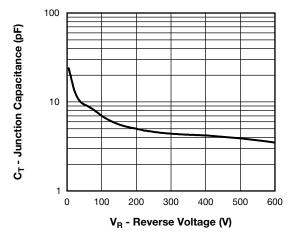


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

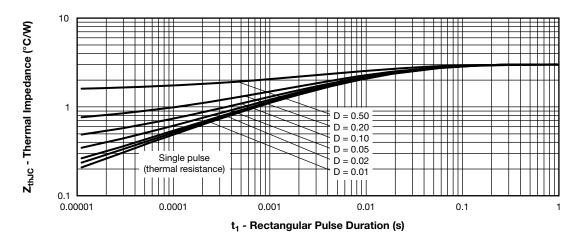
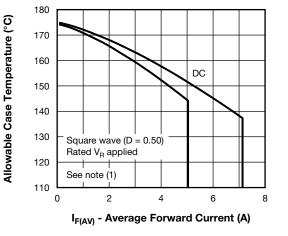
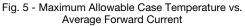
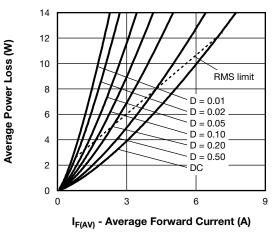
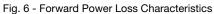


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics









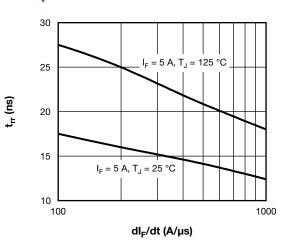
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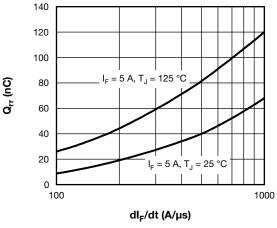
Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

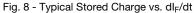
Note

SHA

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \\ \end{array}$





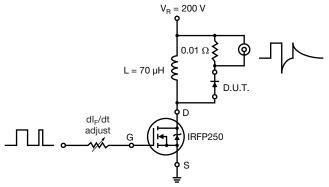


Fig. 9 - Reverse Recovery Parameter Test Circuit

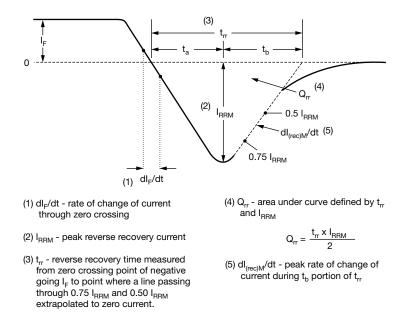


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

www.vishay.com

Device code	VS-	5	Е	w	x	06	FN	TRL	-M3		
	1	2	3	4	5	6	7	8	9		
	1 - Vishay Semiconductors product										
	2	2 - Current rating (5 = 5 A)									
	3	- Circ	Circuit configuration:								
		E =	E = single diode								
	4	- Pac	kage id	entifier:							
		VV =	D-PAK								
	5	- X=	X = hyperfast recovery time								
	E	- Vol	tage rati	ng (06 =	= 600 V)						
	E	- FN	= TO-25	52AA							
	8	- • N	one = tu	lbe							
		• TI	• TR = tape and reel								
			• TRL = tape and reel (left oriented)								
			 TRR = tape and reel (right oriented) 								
	9		' ironmer				,				
				-							

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-5EWX06FN-M3	75	3000	Antistatic plastic tube							
VS-5EWX06FNTR-M3	2000	2000	13" diameter reel							
VS-5EWX06FNTRL-M3	3000	3000	13" diameter reel							
VS-5EWX06FNTRR-M3	3000	3000	13" diameter reel							

LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?95627							
Part marking information	www.vishay.com/doc?95176							
Packaging information	www.vishay.com/doc?95033							
SPICE model	www.vishay.com/doc?95215							





D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		S NOTES		ES SYMBOL		IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51 BSC		0.020 BSC		
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA



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